

Using MPLAB® ICD 3

In-Circuit Debugger

1 Install the Latest Software

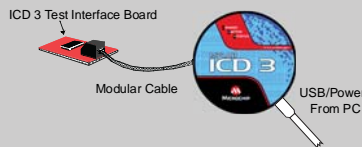
Install the MPLAB IDE software onto your PC using the MPLAB IDE CD-ROM or download the software from the MPLAB IDE page of the Microchip web site (www.microchip.com/MPLAB). Check the latest Release Notes for additional information.

2 Configure PC USB Communications

Connect MPLAB ICD 3 In-Circuit Debugger to a PC USB port via a USB cable. If the drivers do not install automatically, then install the drivers as instructed in: C:\Program Files\Microchip\MPLAB IDE\ICD 3\Drivers\ddri.htm.

Note: If a USB hub is used, the hub must be powered with its own power supply.

3 Use the ICD 3 Test Interface Board



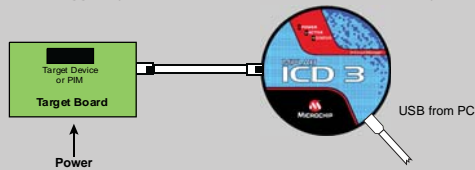
Use the supplied ICD 3 Test Interface Board to verify that the MPLAB ICD 3 is functioning properly:

1. Connect the ICD 3 Test Interface board to the debugger using the modular cable.
2. Ensure that "MPLAB ICD 3" is selected on either the Debugger or Programmer menu in MPLAB IDE.
3. From that menu, select "Settings", **Status** tab, then click on **Run ICD 3 Test Interface**. The status (pass/fail) is displayed in the Output window. If a pass message is displayed, the MPLAB ICD 3 is functioning properly.

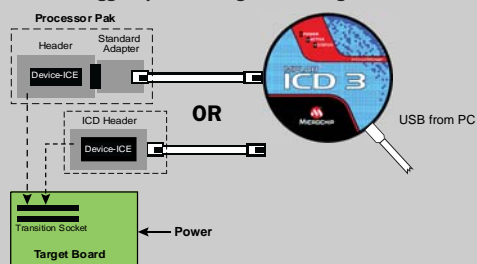
4 Connect to Target Device

1. Ensure the MPLAB ICD 3 is attached to the PC using the USB cable, if not already.
2. Remove the ICD 3 Test Interface Board and attach the modular cable between the debugger and target board.
3. Connect power to the target board.

Typical Debugger System – Device with on-board ICE circuitry

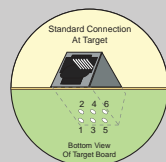


Alternate Debugger System Configuration Using an ICE Device



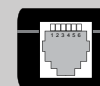
Circuitry and Connector Pinouts

Target Connector Pinout

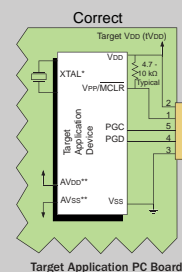


Modular Connector Pin	Microcontroller Pin
1	MCLR/Vpp
2	Vpp
3	Ground
4	PGD (ICSPDAT)
5	PGC (ICSPCLK)
6	PGM (LVP)

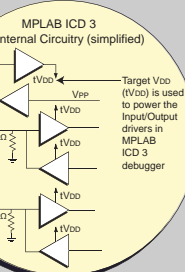
MPLAB ICD 3 RJ-11 Jack Pinout



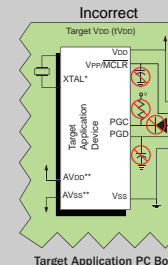
Pin	Signal
PGM	1
PGC	2
PGD	3
Ground	4
Vpp	5
MCLR/Vpp	6



*Target device must be running with an oscillator for the debugger to function as a debugger.
** If the device has AVDD and AVSS lines, they must be connected for the debugger to operate.



Target Circuit Design Precautions



- Do not use greater than 100 μ F capacitance on VDD – depending on the overall load, it will prevent the target from powering quickly when MPLAB ICD 3 is the source of power.
- Do not use capacitors on MCLR – they will prevent fast transitions of Vpp.
- Do not use pull-ups on PGC/PGD – they will divide the voltage levels since these lines have 4.7 k Ω pull-down resistors in MPLAB ICD 3.
- Do not use multiplexing on PGC/PGD – they are dedicated for communications to MPLAB ICD 3.
- Do not use capacitors on PGC/PGD – they will prevent fast transitions on data and clock lines during programming and debug communications.
- Do not use diodes on PGC/PGD – they will prevent bidirectional communication between MPLAB ICD 3 and the target PIC® MCU.

Recommended Settings

COMPONENT	SETTING
Oscillator	• OSC bits set properly • Running
Power	Supplied by target
WDT	Disabled (device dependent)
Code-Protect	Disabled
Table Read Protect	Disabled
LVP	Disabled
BOD	VDD > BOD VDD min
JTAG	Disabled
AVDD and AVSS	Must be connected
PGCx/PGDx	Proper channel selected, if applicable
Programming	VDD voltage levels meet programming specs

Note: See the MPLAB ICD 3 User's Guide for more component and setting information.

5 Build Your Project

1. Launch MPLAB IDE.
2. Load your project or use the Project Wizard to create a new one.
3. Build your project based on your configurations and options.
4. Attach the MPLAB ICD 3 to the PC using the USB cable.
5. Select the MPLAB ICD 3 as either a debugger (*Debugger>Select Tool>ICD 3*) or as a programmer (*Programmer>Select Programmer>ICD 3*).

6 Program and Debug

1. Program your device.
2. As a programmer, MPLAB ICD 3 will automatically run your code. As a debugger, you can run, halt, single step and set breakpoints in your code.

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Reserved Resources

For information on reserved resources used by the debugger, see the MPLAB ICD 3 on-line help.