SN75173
SN75175

QUAD EIA-485 LINE RECEIVERS

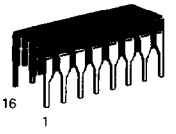
The Motorola SN75173/175 are monolithic quad differential line receivers with three-state outputs. They are designed specifically to meet the requirements of EIA-485, EIA-422A/23A Standards and CCITT recommendations.

The devices are optimized for balanced multipoint bus transmission at rates up to 10 megabits per second. They also feature high input impedance, input hysteresis for increased noise immunity, and input sensitivity of ±200 millivolts over a common mode input voltage range of −12 volts to 12 volts. The SN75173/175 are designed for optimum performance when used with the SN75172 or SN75174 quad differential line drivers.

- Meets EIA Standards EIA-422A and EIA-423A, EIA-485
- Meets CCITT Recommendations V.10, V.11, X.26, and X.27
- Designed for Multipoint Transmission on Long Bus Lines in Noisy Environments
- 3-State Outputs
- Common-Mode Input Voltage Range . . . −12 V to 12 V
- Input Sensitivity . . . ±200 mV
- Input Hysteresis . . . 50 mV Typ
- High Input Impedance . . . 1 EIA-485 Unit Load
- Operates from Single 5.0 V Supply
- Low Power Requirements
- Plug-In Replacement for MC3486 (SN75175) AM26LS32 (SN75173)

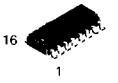
QUAD EIA-485 LINE RECEIVERS WITH THREE-STATE OUTPUTS

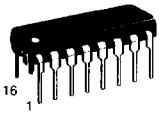
SILICON MONOLITHIC INTEGRATED CIRCUITS



J SUFFIX
CERAMIC PACKAGE
CASE 620

D SUFFIX
PLASTIC PACKAGE
CASE 751B
(SO-16)



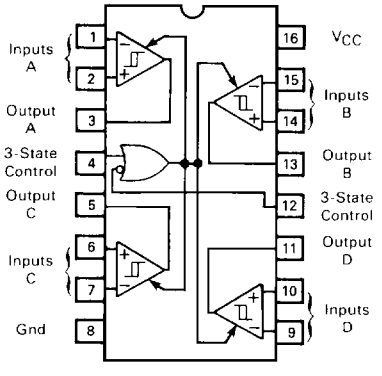


N SUFFIX
PLASTIC PACKAGE
CASE 648

7

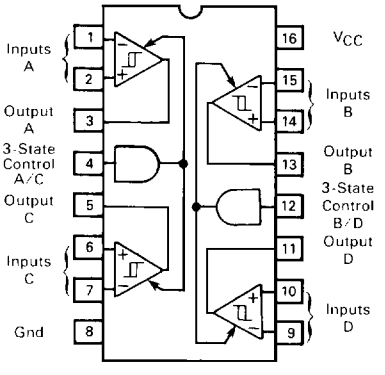
PIN CONNECTIONS

SN75173



ORDERING INFORMATION		
Device	Temperature	Package
SN75173J	0 to +70°C	Ceramic DIP
SN75173N	0 to +70°C	Plastic DIP

SN75175



ORDERING INFORMATION		
Device	Temperature	Package
SN75175J	0 to +70°C	Ceramic DIP
SN75175N	0 to +70°C	Plastic DIP

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Power Supply Voltage	V_{CC}	7.0	Vdc
Input Common Mode Voltage	V_{ICM}	±25	Vdc
Input Differential Voltage	V_{ID}	±25	Vdc
Three-State Control Input Voltage	V_I	7.0	Vdc
Output Sink Current	I_O	50	mA
Storage Temperature	T_{stg}	-65 to +150	°C
Operating Junction Temperature — Ceramic Package — Plastic Package	T_J	+175 +150	°C

RECOMMENDED OPERATING CONDITIONS

Rating	Symbol	Value	Unit
Power Supply Voltage	V_{CC}	4.75 to 5.25	Vdc
Operating Ambient Temperature	T_A	0 to +70	°C
Input Common Mode Voltage Range	V_{ICM}	-12 to +12	Vdc
Input Differential Voltage Range	V_{IDR}	-12 to +12	Vdc

ELECTRICAL CHARACTERISTICS (Unless otherwise noted, minimum and maximum limits apply over recommended temperature and power supply voltage ranges. Typical values are for T_A = 25°C, V_{CC} = 5.0 V and V_{ICM} = 0 V) (Note 1)

Characteristic	Symbol	Min	Typ	Max	Unit
Differential Input Threshold Voltage (Note 2) (-12 V ≤ V_{ICM} ≤ 12 V, V_{IH} = 2.0 V) (I_O = -0.4 mA, V_{OH} ≥ 2.7 V) (I_O = 16 mA, V_{OL} ≤ 0.5 V)	$V_{TH(D)}$	 -- --	 — —	 0.2 0.2	V
Input Hysteresis	$V_{T+} - V_{T-}$	—	50	—	mV
Input Line Current (Differential Inputs) (Unmeasured Input at 0 V — Note 3) (V_I = +12 V) (V_I = -7.0 V)	I_I	 — --	 — —	 1.0 -0.8	mA
Input Resistance (Note 4)	r_i	1 Unit Load	—	—	
Input Balance and Output Level (Note 3) (-12 V ≤ V_{ICM} ≤ 12 V, V_{IH} = 2.0 V) (I_O = -0.4 mA, V_{ID} = 0.2 V) (I_O = 8.0 mA, V_{ID} = -0.2 V) (I_O = 16 mA, V_{ID} = 0.2 V)	 V_{OH} V_{OL} V_{OL}	 2.7 — --	 — — —	 0.45 0.5	V
Input Voltage — High Logic State (Three-State Control)	V_{IH}	2.0	—		V
Input Voltage — Low Logic State (Three-State Control)	V_{IL}	--	-	0.8	V
Input Current — High Logic State (Three-State Control) (V_{IH} = 2.7 V) (V_{IH} = 5.5 V)	I_{IH}	 — —	 — —	 20 100	μA
Input Current — Low Logic State (Three-State Control) (V_{IL} = 0.4 V)	I_{IL}	—	—	-100	μA
Input Clamp Diode Voltage (Three-State Control) (I_{IK} = -18 mA)	V_{IK}	—	—	-1.5	V
Output Third State Leakage Current ($V_{I(D)}$ = 3.0 V, V_{IL} = 0.8 V, V_O = 0.4 V) ($V_{I(D)}$ = -3.0 V, V_{IL} = 0.8 V, V_O = 2.4 V)	I_{OZ}	 — —	 — —	 -20 20	μA
Output Short-Circuit Current (Note 5) ($V_{I(D)}$ = 3.0 V, V_{IH} = 2.0 V, V_O = 0 V)	I_{OS}	-15	—	-85	mA
Power Supply Current (V_{IL} = 0 V) (All Inputs Grounded)	I_{CC}	—	—	70	mA

NOTES:
1. All currents into device pins are shown as positive, out of device pins are negative. All voltages referenced to ground unless otherwise noted.
2. Differential input threshold voltage and guaranteed output levels are done simultaneously for worst case.
3. Refer to EIA-485 for exact conditions. Input balance and guaranteed output levels are done simultaneously for worst case.
4. Input resistance should be derived from input line current specifications and is shown for reference only. See EIA-485 and input line current specifications for more specific input resistance information.
5. Only one output at a time should be shorted.

SWITCHING CHARACTERISTICS (Unless otherwise noted, V_{CC} = 5.0 V and T_A = 25°C)

Characteristic	Symbol	SN75173			SN75175			Unit
		Min	Typ	Max	Min	Typ	Max	
Propagation Delay Time — Differential Inputs to Output								ns
(Output High to Low)	$t_{PHL(D)}$	—	25	35	—	25	35	
(Output Low to High)	$t_{PLH(D)}$	—	25	35	—	25	35	
Propagation Delay Time — Three-State Control to Output								ns
(Output Low to Third State)	t_{PLZ}	—	20	40	—	16	35	
(Output High to Third State)	t_{PHZ}	—	20	30	—	19	35	
(Output Third State to High)	t_{PZH}	—	16	22	—	11	30	
(Output Third State to Low)	t_{PZL}	—	16	25	—	11	30	

SN75173

FUNCTION TABLE (EACH RECEIVER)

Differential Inputs	3-State Control		Output Y
	4	12	
$V_{ID} \geq 0.2$ V	H	X	H
	X	L	H
-0.2 V $< V_{ID} < 0.2$ V	H	X	?
	X	L	?
$V_{ID} \leq -0.2$ V	H	X	L
	X	L	L
X	L	H	Z

SN75175

FUNCTION TABLE (EACH RECEIVER)

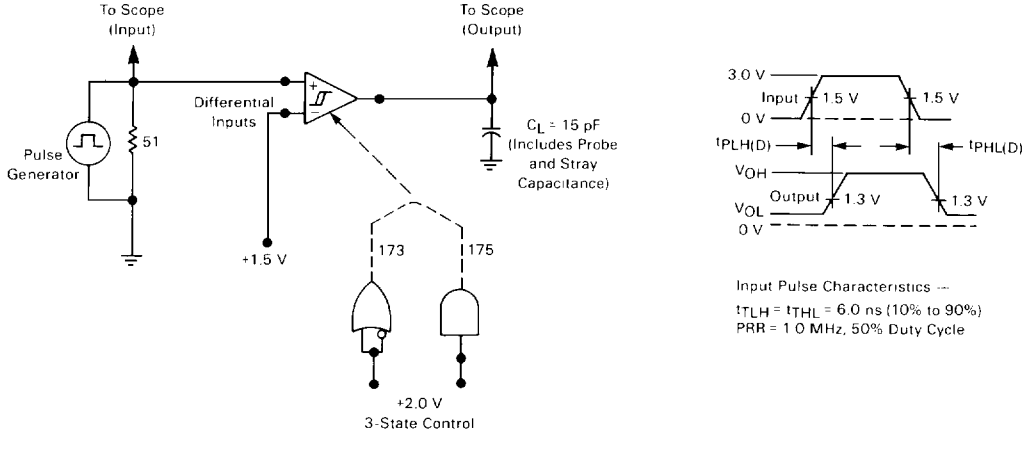
Differential Inputs	3-State Control	Output Y
$V_{ID} \geq 0.2$ V	H	H
-0.2 V $< V_{ID} < 0.2$ V	H	?
$V_{ID} \leq -0.2$ V	H	L
X	L	Z

H = high level
L = low level
X = irrelevant
? = indeterminate
Z = high-impedance (off)

7

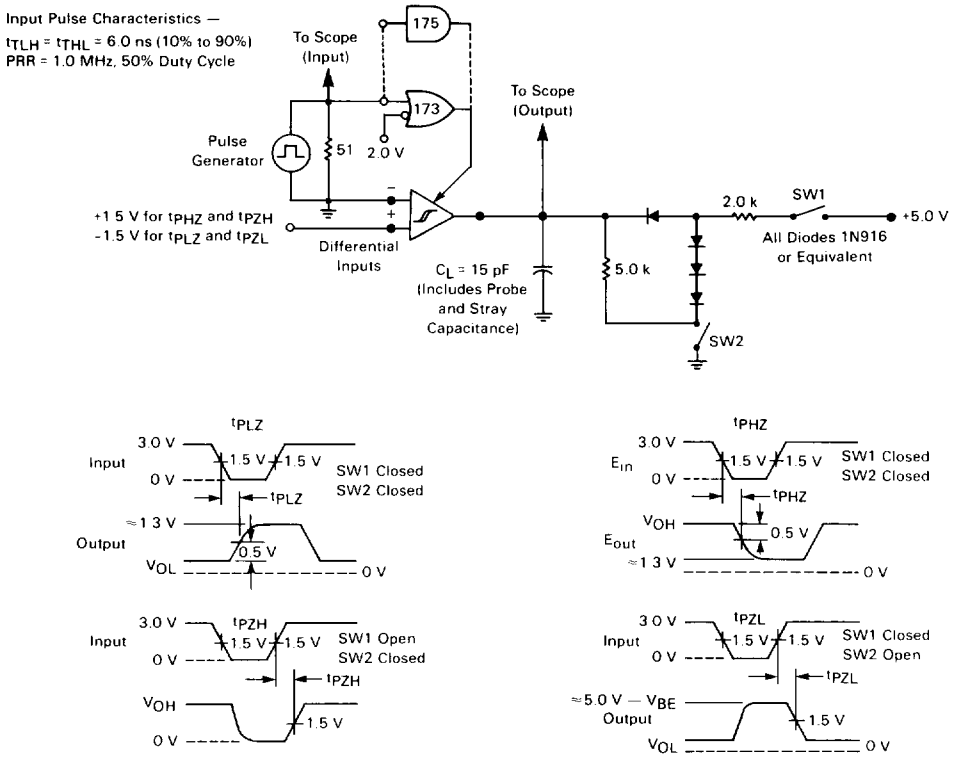
SWITCHING TEST CIRCUIT AND WAVEFORMS

FIGURE 1 — PROPAGATION DELAY, DIFFERENTIAL INPUT TO OUTPUT



SWITCHING TEST CIRCUIT AND WAVEFORMS (continued)

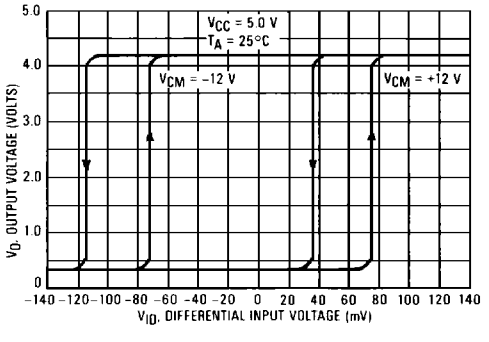
FIGURE 2 — PROPAGATION DELAY, THREE-STATE CONTROL INPUT TO OUTPUT



TYPICAL CHARACTERISTICS

(Both Device Types, Unless Otherwise Noted)

FIGURE 3 — OUTPUT VOLTAGE versus DIFFERENTIAL INPUT VOLTAGE



TYPICAL CHARACTERISTICS (continued)

FIGURE 4 — OUTPUT VOLTAGE versus 3-STATE CONTROL VOLTAGE

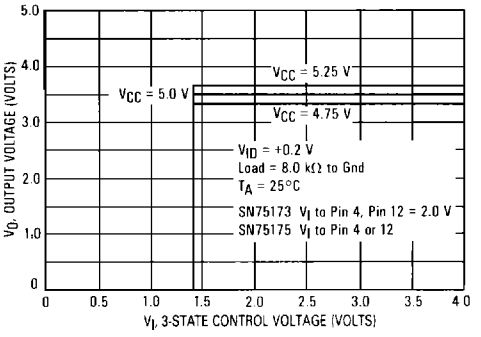


FIGURE 5 — OUTPUT VOLTAGE versus (INVERTED) 3-STATE CONTROL VOLTAGE — SN75173

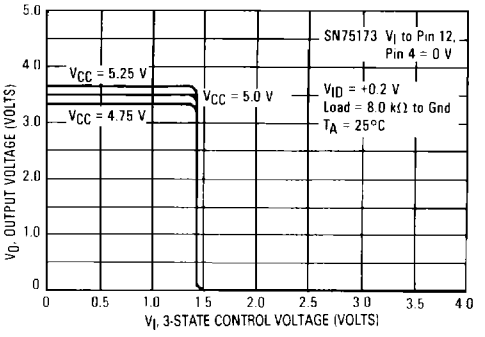


FIGURE 6 — HIGH LEVEL OUTPUT VOLTAGE versus OUTPUT CURRENT

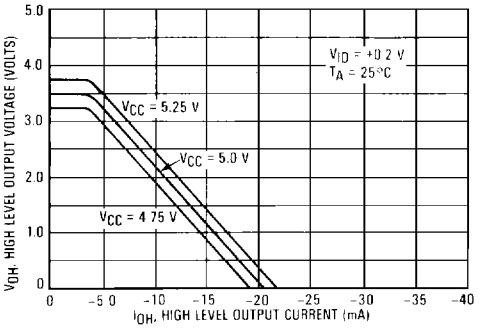


FIGURE 7 — LOW LEVEL OUTPUT VOLTAGE versus OUTPUT CURRENT

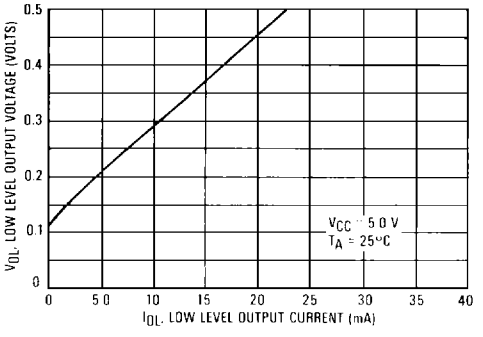


FIGURE 8 — HIGH LEVEL OUTPUT VOLTAGE versus TEMPERATURE

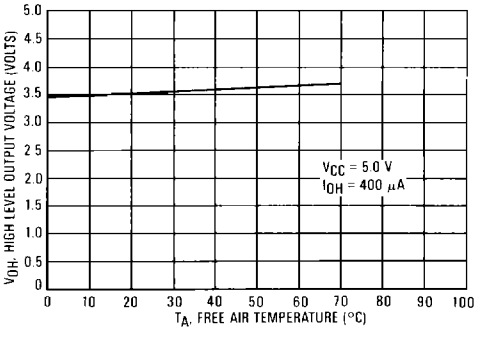
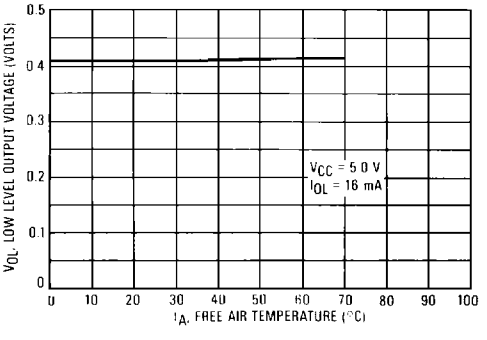


FIGURE 9 — LOW LEVEL OUTPUT VOLTAGE versus TEMPERATURE



7