TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7PH04FE

Dual Inverter

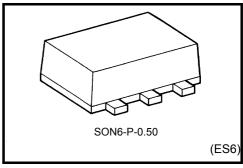
Features

Operating voltage range : V_{CC} = 2.0~5.5 V
 High-speed operation : t_{pd} = 3.8 ns (typ.)

at $V_{CC} = 5 \text{ V}, C_L = 15 \text{ pF}$

Low power dissipation :I_{CC} = 2 μA (max) at Ta=25°C
 High noise immunity :V_{NIH} = V_{NIL} = 28 %V_{CC}(min)

• 5.5-V tolerant inputs

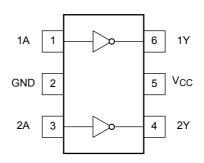


Weight: 0.003g (typ.)

Marking

Product name

Pin Assignment (top view)



Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Supply voltage range	V _{CC}	-0.5~7.0	V	
DC input voltage	V _{IN}	-0.5~7.0	V	
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5	V	
Input diode current	I _{IK}	-20	mA	
Output diode current	lok	±20	mA	
DC output current	lout	±25	mA	
DC V _{CC} /ground current	Icc	±50	mA	
Power dissipation	PD	150	mW	
Storage temperature	T _{stg}	-65~150	°C	

Logic Diagram



Truth Table

А	Y
L	Н
Н	L

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	2~5.5	V	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V _{OUT}	0~ V _{CC}	٧	
Operating temperature	T _{opr}	-40~85	°C	
Input rise and fall time	dt/dv	$0\sim100~(V_{CC}=3.3~V\pm0.3~V)$	ns/V	
input rise and rail time	uvuv	0~20 (V _{CC} = 5 V ± 0.5 V)		

Electrical Characteristics

DC Characteristics

Characteristics Symbol T		Tool	Test Condition V _{CC} (V)		7	「a = 25°(2	Ta = -4	l lm:4	
		resi			Min	Тур.	Max	Min	Max	Unit
High-level input				2.0	1.5	_	_	1.5	_	٧
voltage VIH		_		V _{CC} × 0.7	_	_	V _{CC} × 0.7	_		
			2.0	_	_	0.50	_	0.50		
Low-level input voltage			_	3.0~5.5			V _{CC} × 0.3	_	V _{CC} × 0.3	V
			Ι _{ΟΗ} = -50 μΑ	2.0	1.9	2.0	_	1.9	_	V
High-level output voltage				3.0	2.9	3.0	_	2.9	_	
	V _{OH}	$V_{IN} = V_{IL} \\$		4.5	4.4	4.5	_	4.4	_	
			I _{OH} = -4 mA	3.0	2.58	_	_	2.48	_	
			I _{OH} = -8 mA	4.5	3.94	_	_	3.80	_	
Low-level output voltage				2.0	_	0.0	0.1	_	0.1	
		Ι _{ΟL} = 50 μΑ	3.0	_	0.0	0.1	_	0.1	V	
	$V_{IN} = V_{IH} \\$		4.5	_	0.0	0.1	_	0.1		
		I _{OL} = 4 mA	3.0	_	_	0.36	_	0.44	,	
		I _{OL} = 8 mA	4.5	_		0.36	_	0.44		
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0~5.5	_		±0.1	_	±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		5.5	_	_	2.0	_	20.0	μА

3 2005-11-29

AC Characteristics (Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit	
			V _{CC} (V)	C _{L (} pF)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	t _{PLH}	3.3 ± 0.3 5.0 ± 0.5	33+03	15	_	5.0	7.1	1.0	8.5	
			50		7.5	10.6	1.0	12.0	ns	
			5.0 ± 0.5	15		3.8	5.5	1.0	6.5	113
				50		5.3	7.5	1.0	8.5	
Input capacitance	C _{IN}	_			_	4	10	_	10	pF
Power dissipation capacitance	C _{PD}	(Note)			_	15	_	_	_	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

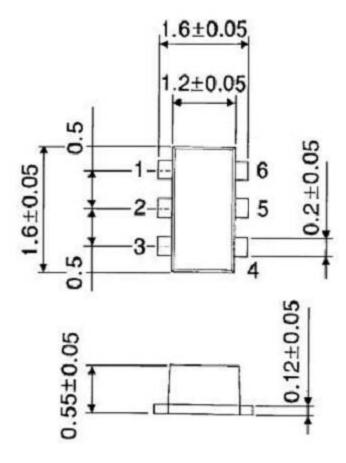
4

Average operating current can be obtained by the equation:

$$I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$$

Package Dimensions

SON6-P-0.50 Unit: mm



Weight: 0.003 g (typ.)

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6