



Features

- ◊ For surface mounted application
- ◊ Metal silicon junction, majority carrier conduction
- ◊ Low forward voltage drop
- ◊ Easy pick and place
- ◊ High surge current capability
- ◊ Plastic material used carries Underwriters Laboratory Classification 94V-0
- ◊ Epitaxial construction
- ◊ High temperature soldering:
260°C / 10 seconds at terminals

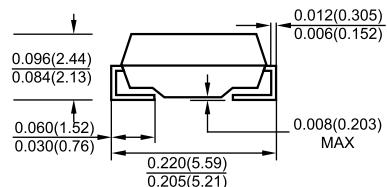
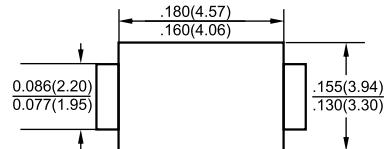
Mechanical Data

- ◊ Cases: Molded plastic
- ◊ Terminals: Matte tin plating
- ◊ Polarity: Indicated by cathode band
- ◊ Weight: 0.093 gram

SSL22 - SSL24

2.0 AMP. Surface Mount Low V_F
Schottky Barrier Rectifiers

SMB/DO-214AA



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitive load, derate current by 20%

Type Number	Symbol	SSL22	SSL23	SSL24	Units
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	20	30	40	V
Maximum RMS Voltage	V _{RMS}	14	21	28	V
Maximum DC Blocking Voltage	V _{DC}	20	30	40	V
Maximum Average Forward Rectified Current See Fig. 1	I _(AV)	2.0			A
Peak Forward Surge Current, 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I _{FSM}	80			A
Maximum Instantaneous Forward Voltage (Note 1) @ 2.0A	V _F	0.41			V
Maximum DC Reverse Current @ T _A =25°C at Rated DC Blocking Voltage @ T _A =100°C	I _R	0.4		mA mA	
		50	60		
Maximum Thermal Resistance (Note 2)	R _{θJL} R _{θJA}	25 70			°C/W
Marking Code		SL22	SL23	SL24	
Operating Temperature Range	T _J	-55 to +125			°C
Storage Temperature Range	T _{STG}	-55 to + 150			°C

Notes: 1. Pulse Test with PW=300 usec, 1% Duty Cycle.

2. Measured on P.C. Board with 0.4 x .4"(10 x 10mm) Copper Pad Areas.

SSL22 - SSL24

2.0 AMP. Surface Mount Low V_F
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RATINGS AND CHARACTERISTIC CURVES (SSL22 THRU SSL24)

FIG.1- MAXIMUM FORWARD CURRENT DERATING CURVE

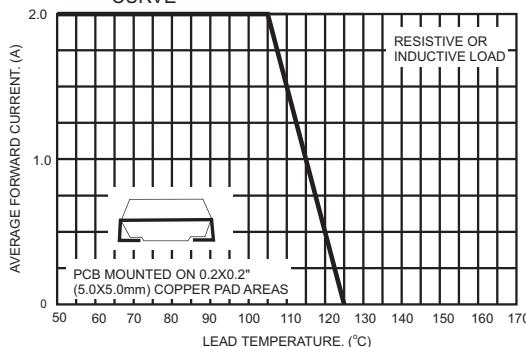


FIG.2- MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

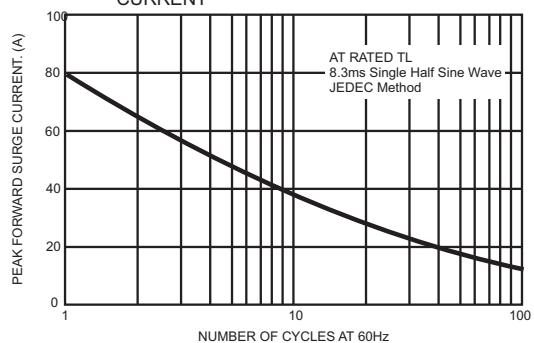


FIG.3- TYPICAL FORWARD CHARACTERISTICS

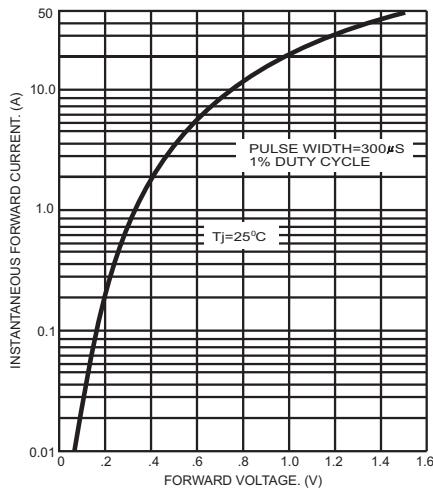


FIG.4- TYPICAL REVERSE CHARACTERISTICS

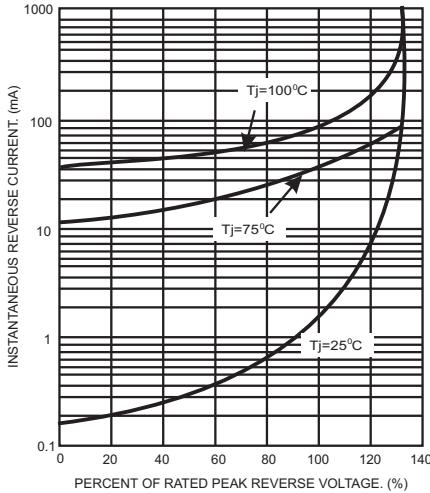


FIG.5- TYPICAL JUNCTION CAPACITANCE

