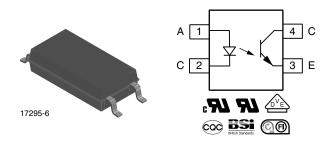
# VOL617A

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Vishay Semiconductors

## Optocoupler, Phototransistor Output, Low Input Current, 4 Pin LSOP, Long Creepage Mini-Flat Package



### DESCRIPTION

The VOL617A has a GaAs infrared emitting diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a 4 pin LSOP wide body package.

It features a high current transfer ratio, low coupling capacitance, and high isolation voltage.

The coupling device is designed for signal transmission between two electrically separated circuits.

### FEATURES

### Low profile package

- High collector emitter voltage, V<sub>CEO</sub> = 80 V
- Isolation test voltage, 5000 V<sub>BMS</sub>
- Isolation voltage V<sub>IORM</sub> = 1050 V<sub>peak</sub>
- Low coupling capacitance
- High common mode transient immunity
- Material categorization: For definitions of [5-2008] compliance please see www.vishay.com/doc?99912

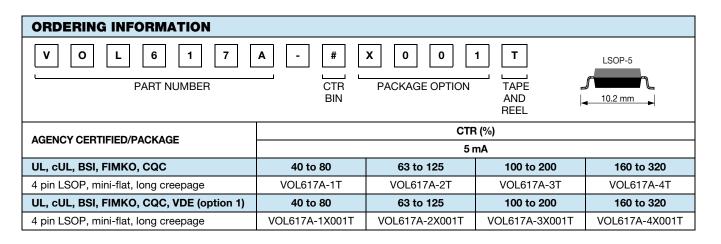
#### **APPLICATIONS**

- Telecom
- Industrial controls
- Battery powered equipment
- · Office machines
- Programmable controllers

### AGENCY APPROVALS

(All parts are certified under base model VOL617A)

- UL1577, file no. E76222
- cUL CSA 22.2 bulletin 5A, double protection
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- BSI: EN 60065:2002, EN 60950-1:2006
- FIMKO EN60950-1
- CQC: GB8898-2011, GB4943.1-2011



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COMPLIANT

GREEN

VOL617A



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### **Vishay Semiconductors**

ABSOLUTE MAXIMUM RATINGS (Tamb =	25 °C, unless otherwise s	specified)			
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT					
Reverse voltage		V <sub>R</sub>	6	V	
Power dissipation		P <sub>diss</sub>	100	mW	
Forward current		I <sub>F</sub>	60	mA	
Junction temperature		Тj	125	°C	
OUTPUT					
Collector emitter voltage		V <sub>CEO</sub>	80	V	
Emitter collector voltage		V <sub>ECO</sub>	7	V	
Collector current		Ι <sub>C</sub>	50	mA	
	t <sub>p</sub> /T = 0.5, t <sub>p</sub> < 10 ms	Ι <sub>C</sub>	100	mA	
Power dissipation		P <sub>diss</sub>	150	mW	
Junction temperature		Tj	125	°C	
COUPLER					
Isolation test voltage between emitter and detector	t = 1 min	V <sub>ISO</sub>	5000	V <sub>RMS</sub>	
Total power dissipation		P <sub>tot</sub>	250	mW	
Storage temperature range		T <sub>stg</sub>	- 55 to + 125	°C	
Ambient temperature range		T <sub>amb</sub>	- 55 to + 110	°C	
Soldering temperature <sup>(1)</sup>	≤ 10 s	T <sub>sld</sub>	260	°C	

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(1)</sup> Refer to reflow profile for soldering conditions for surface mounted devices.

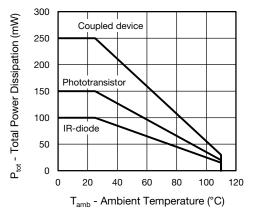


Fig. 1 - Total Power Dissipation vs. Ambient Temperature

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb} = 25 \degree C$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT	·	•			•		
Forward voltage	I <sub>F</sub> = 5 mA	VF		1.16	1.5	V	
Capacitance	$V_R = 0 V$ , f = 1 MHz	Co		45		pF	
Reverse current	V <sub>R</sub> = 6 V	I <sub>R</sub>			100	μA	
OUTPUT	·						
Collector emitter leakage current	$V_{CE} = 10 \text{ V}, I_F = 0 \text{ A}$	I <sub>CEO</sub>		10	200	nA	
Collector emitter capacitance	$V_{CE} = 5 V, f = 1 MHz$	C <sub>CE</sub>		7		pF	
COUPLER	·				•		
Collector emitter saturation voltage	$I_{\rm C} = 1.0$ mA, $I_{\rm F} = 5$ mA	V <sub>CEsat</sub>		0.25	0.4	V	
Coupling capacitance	f = 1 MHz	C <sub>C</sub>		0.25		pF	

#### Note

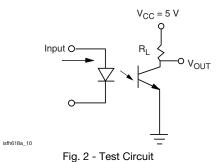
• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

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<b>CURRENT TRANSFER RATIO</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub>	I <sub>F</sub> = 5 mA, V <sub>CF</sub> = 5 V	VOL617A-1	CTR	40		80	%
		VOL617A-2	CTR	63		125	%
	$r_F = 3 mA$ , $v_{CE} = 3 v$	VOL617A-3	CTR	100		200	%
		VOL617A-4	CTR	160		320	%

SWITCHING CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn on time	$V_{CC}$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	t <sub>on</sub>		6		μs
Rise time	$V_{CC}$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	t <sub>r</sub>		3.5		μs
Turn off time	$V_{CC}$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	t <sub>off</sub>		5.5		μs
Fall time	$V_{CC}$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	t <sub>f</sub>		5		μs



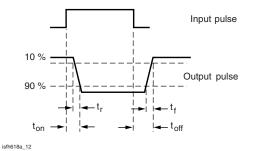


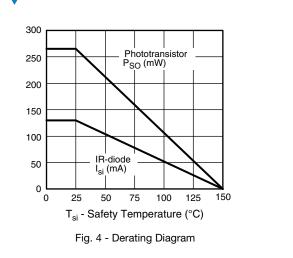
Fig. 3 - Test Circuit and Waveforms

SAFETY AND INSULATION RATED PARAMETERS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Partial discharge test voltage - routine test	100 %, t <sub>test</sub> = 1 s	V <sub>pd</sub>	2			kV	
Partial discharge test voltage -	t <sub>Tr</sub> = 60 s, t <sub>test</sub> = 10 s,	VIOTM	8			kV	
lot test (sample test)	(see figure 4)	V <sub>pd</sub>	1.68			kV	
Insulation voltage		VIORM			1050	V <sub>peak</sub>	
Insulation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$	R <sub>IO</sub>	10 <sup>12</sup>			Ω	
	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$	R <sub>IO</sub>	10 <sup>11</sup>			Ω	
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 150 °C (construction test only)	R <sub>IO</sub>	10 <sup>9</sup>			Ω	
Safety rating - maximum input current		I <sub>si</sub>			130	mA	
Safety rating - maximum power dissipation		P <sub>SO</sub>			265	mW	
Rated impulse voltage		V <sub>IOTM</sub>			8	kV	
Safety rating - maximum ambient temperature		T <sub>si</sub>			150	°C	
Clearance distance			8			mm	
Creepage distance			8			mm	
Insulation distance (internal)			0.4			mm	

Note

According to DIN EN 60747-5-5 (VDE 0884), § 7.4.3.8.2, (see figure 4). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.





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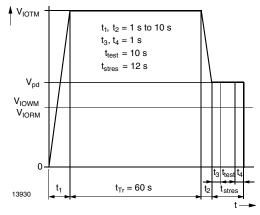
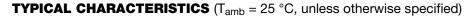


Fig. 5 - Test Pulse Diagram for Sample Test according to DIN EN 60747-5-5



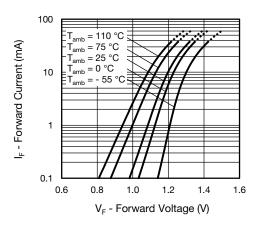


Fig. 6 - Forward Current vs. Forward Voltage

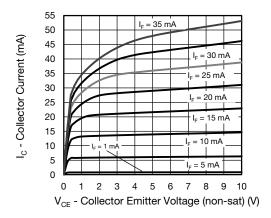


Fig. 7 - Collector Current vs. Collector Emitter Voltage (non-saturated)

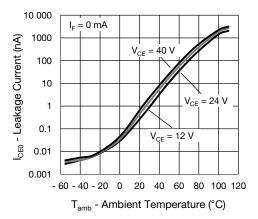


Fig. 8 - Collector Emitter Current vs. Ambient Temperature

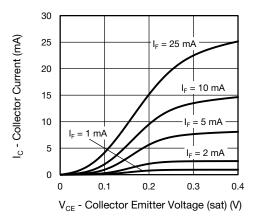
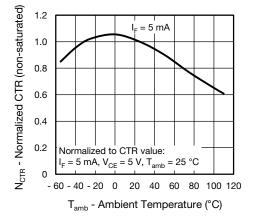


Fig. 9 - Collector Current vs. Collector Emitter Voltage (saturated)

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Fig. 10 - Normalized Current Transfer Ratio (non-sat) vs. Ambient Temperature

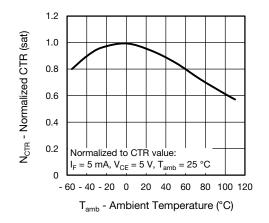


Fig. 11 - Normalized Current Transfer Ratio (sat) vs. Ambient Temperature

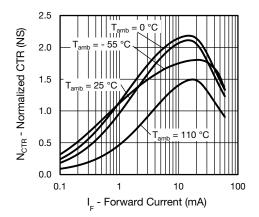


Fig. 12 - Normalized Current Transfer Ratio (non-sat) vs. Forward Current

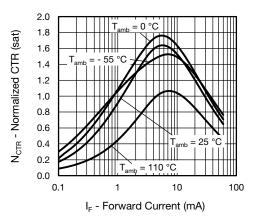


Fig. 13 - Normalized Current Transfer Ratio (sat) vs. Forward Current

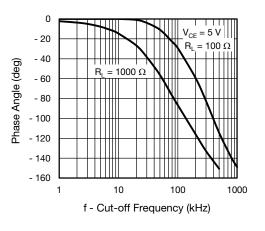


Fig. 14 - f<sub>CTR</sub> vs. Phase Angle

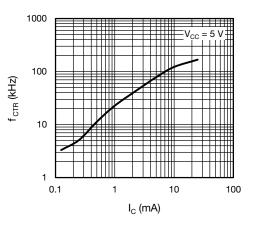


Fig. 15 - f<sub>CTR</sub> vs. Collector Current

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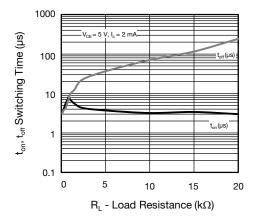


Fig. 16 - Switching Time vs. Load Resistance

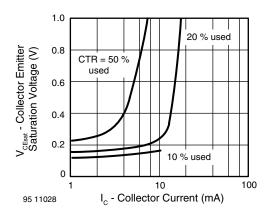


Fig. 17 - Collector Emitter Saturation Voltage vs. Collector Current

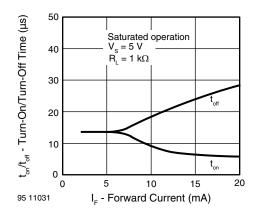


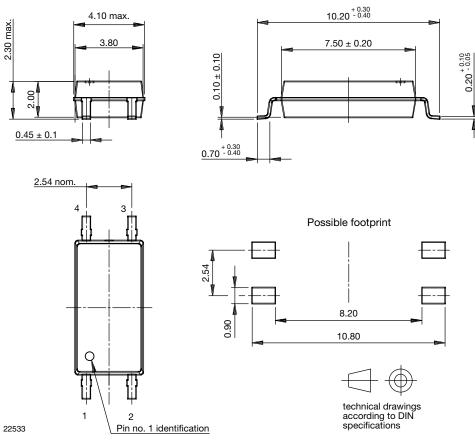
Fig. 18 - Turn-On/Turn-Off Time vs. Forward Current

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### **PACKAGE DIMENSIONS** in millimeters



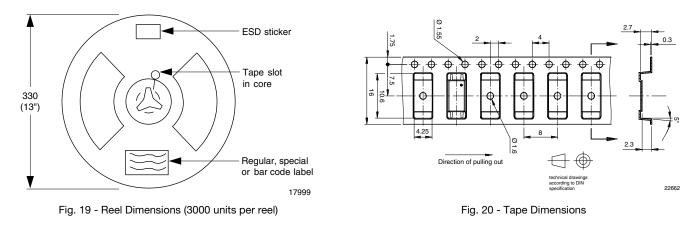
### PACKAGE MARKING (example of VOL617A-3X001T)



#### Notes

- Only option 1 is reflected in the package marking with the characters "X1".
- Tape and reel suffix (T) is not part of the package marking.

#### TAPE AND REEL DIMENSIONS in millimeters



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