

UTC UNISONIC TECHNOLOGIES CO., LTD

X0405 **Preliminary SCR**

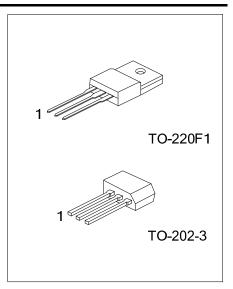
4A SCR

DESCRIPTION

The UTC X0405 is a 4A SCR, it uses UTC's advanced technology to provide customers with highly sensitive triggering levels, etc.

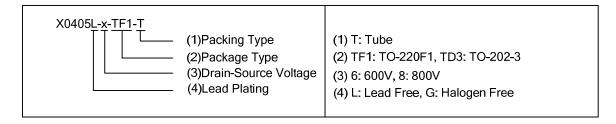
The UTC X0405 is suitable for all applications, such as motor control in kitchen aids, capacitive discharge ignitions, and overvoltage crowbar protection in low power supplies, etc.

FEATURES



ORDERING INFORMATION

Ordering Number		Dookooo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
X0405L-x-TF1-T	X0405G-x-TF1-T	TO-220F1	K	Α	G	Tube	
X0405L-x-TD3-T X0405G-x-TD3-T		TO-202-3	K	Α	G	Tube	



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^{*} Highly sensitive triggering levels

■ ABSOLUTE MAXIMUM RATINGS (limiting values)

PARAMETER		SYMBOL	RATINGS	UNIT	
Description Description of Other Market	X0405-6		600	V	
Repetitive Peak Off-State Voltages	X0405-8	V_{DRM}/V_{RRM}	800	V	
RMS On-State Current (180° Conduction	TI=60°C		4	Α	
Angle)	T _{AMB} =25°C	I _{T(RMS)}	1.35	Α	
Average On-State Current (180° Conduction	-State Current (180° Conduction TI=60°C		2.5	Α	
Angle)	T _{AMB} =25°C	I _{T(AV)}	0.9	Α	
N D 4:4: O	tp=8.3ms, T _J =25°C] ,	33	Α	
Non Repetitive Surge Peak On-State Current	tp=10ms, T _J =25°C	I _{TSM}	30	Α	
I ² t Value for Fusing	tp=10ms, T _J =25°C	l ² t	4.5	A ² s	
Critical Rate of Rise of On-State Current I _G =2xI _{GT} ,tr≤100ns	F=60Hz, T _J =125°C	dI/dt	50	A/µs	
Peak Gate Current	tp=20μs, T _J =125°C	I _{GM}	1.2	Α	
Average Gate Power Dissipation	T _J =125°C	$P_{G(AV)}$	0.2	W	
Storage Junction Temperature		T _{STG}	-40~+150	°C	
Operating Junction Temperature	·	TJ	-40~+125	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL RESISTANCES

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient (DC)	θ_{JA}	100	°C/W
Junction to Case (DC)	$\theta_{ extsf{JC}}$	15	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J=25°C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Gate Trigger Current	I_{GT}	V _D =12V, R _L =140Ω			50	μΑ
Gate Trigger Voltage	V_{GT}				0.8	V
Gate Non-Trigger Voltage	V_{GD}	$V_D=V_{DRM}$, $R_L=3.3k\Omega$, $R_{GK}=1k\Omega$, $T_J=125$ °C	0.1			V
Repetitive Gate Voltage	V_{RG}	I _{RG} =10µA	8			V
Holding Current	I _H	I_T =50mA, R_{GK} =1k Ω			5	mA
Latching Current	IL	$I_G=1mA$, $R_{GK}=1k\Omega$	6			mA
Critical Rate of Rise of Off-State Voltage	dV/dt	V_D =67% V_{DRM} , R_{GK} =1k Ω , T_J =110°C	15			V/µs
Peak On-State Voltage	V_{TM}	I _{TM} =8A, t _p =380μs, T _J =25°C			1.8	V
Threshold Voltage	V_{TO}	T _J =125°C			0.95	V
Dynamic Resistance	R_D	T _J =125°C			100	mΩ
Repetitive Peak Off-State Current	I_{DRM}	$V_{DRM}=V_{RRM}$, $R_{GK}=1k\Omega$, $T_{J}=25^{\circ}C$			5	μΑ
	I _{RRM}	$V_{DRM}=V_{RRM}$, $R_{GK}=1k\Omega$, $T_{J}=125$ °C			1	mA

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