



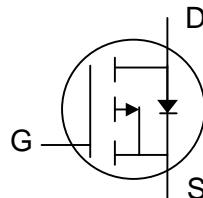
P-channel Enhancement-mode Power MOSFET

Fast Switching Characteristics

Low Gate Charge

Small Footprint, Low Profile

RoHS-compliant, halogen-free

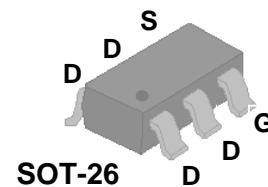


| | |
|--------------|-------|
| BV_{DSS} | -20V |
| $R_{DS(ON)}$ | 57mΩ |
| I_D | -5.1A |

Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, low on-resistance and cost-effectiveness.

The SOT-26 package is widely used for commercial and industrial applications, where space is at a premium.



Absolute Maximum Ratings

| Symbol | Parameter | Rating | Units |
|---------------------------|---------------------------------------|------------|---------------|
| V_{DS} | Drain-Source Voltage | -20 | V |
| V_{GS} | Gate-Source Voltage | ± 12 | V |
| I_D at $T_A=25^\circ C$ | Continuous Drain Current ³ | -5.1 | A |
| I_D at $T_A=70^\circ C$ | Continuous Drain Current ³ | -4.0 | A |
| I_{DM} | Pulsed Drain Current ¹ | -20 | A |
| P_D at $T_A=25^\circ C$ | Total Power Dissipation | 2 | W |
| | Linear Derating Factor | 0.016 | W/ $^\circ C$ |
| T_{STG} | Storage Temperature Range | -55 to 150 | $^\circ C$ |
| T_J | Operating Junction Temperature Range | -55 to 150 | $^\circ C$ |

Thermal Data

| Symbol | Parameter | Value | Unit |
|-------------|--|-------|--------------|
| R_{thj-a} | Maximum Thermal Resistance, Junction-ambient | 62.5 | $^\circ C/W$ |

Ordering Information

AP2609GY-HF-3TR : in RoHS-compliant halogen-free SOT-26 shipped on tape and reel (3000pcs/reel)



Electrical Specifications at $T_j=25^\circ\text{C}$ (unless otherwise specified)

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Units |
|--------------------------|--|--|------|------|-----------|------------------|
| BV_{DSS} | Drain-Source Breakdown Voltage | $V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=-250\mu\text{A}$ | -20 | - | - | V |
| $R_{\text{DS(ON)}}$ | Static Drain-Source On-Resistance ² | $V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-5\text{A}$ | - | 40 | 57 | $\text{m}\Omega$ |
| | | $V_{\text{GS}}=-2.5\text{V}$, $I_{\text{D}}=-3\text{A}$ | - | 60 | 100 | $\text{m}\Omega$ |
| $V_{\text{GS(th)}}$ | Gate Threshold Voltage | $V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=-250\mu\text{A}$ | -0.5 | -0.7 | -1.2 | V |
| g_{fs} | Forward Transconductance | $V_{\text{DS}}=-5\text{V}$, $I_{\text{D}}=-3\text{A}$ | - | 10 | - | S |
| I_{DSS} | Drain-Source Leakage Current | $V_{\text{DS}}=-16\text{V}$, $V_{\text{GS}}=0\text{V}$ | - | - | -10 | uA |
| I_{GSS} | Gate-Source Leakage | $V_{\text{GS}}= \pm 12\text{V}$, $V_{\text{DS}}=0\text{V}$ | - | - | ± 100 | nA |
| Q_{g} | Total Gate Charge | $I_{\text{D}}=-3\text{A}$ | - | 8.5 | 14 | nC |
| Q_{gs} | Gate-Source Charge | $V_{\text{DS}}=-10\text{V}$ | - | 1.2 | - | nC |
| Q_{gd} | Gate-Drain ("Miller") Charge | $V_{\text{GS}}=-4.5\text{V}$ | - | 3 | - | nC |
| $t_{\text{d(on)}}$ | Turn-on Delay Time | $V_{\text{DS}}=-10\text{V}$ | - | 10 | - | ns |
| t_{r} | Rise Time | $I_{\text{D}}=-1\text{A}$ | - | 20 | - | ns |
| $t_{\text{d(off)}}$ | Turn-off Delay Time | $R_{\text{G}}=3.3\Omega$ | - | 27 | - | ns |
| t_{f} | Fall Time | $V_{\text{GS}}=-5\text{V}$ | - | 22 | - | ns |
| C_{iss} | Input Capacitance | $V_{\text{GS}}=0\text{V}$ | - | 660 | 1050 | pF |
| C_{oss} | Output Capacitance | $V_{\text{DS}}=-10\text{V}$ | - | 135 | - | pF |
| C_{rss} | Reverse Transfer Capacitance | $f=1.0\text{MHz}$ | - | 120 | - | pF |
| R_{g} | Gate Resistance | $f=1.0\text{MHz}$ | - | 7.2 | 14.4 | Ω |

Source-Drain Diode

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Units |
|-----------------|---------------------------------|---|------|------|------|-------|
| V_{SD} | Forward On Voltage ² | $I_{\text{S}}=-1.7\text{A}$, $V_{\text{GS}}=0\text{V}$ | - | - | -1.2 | V |
| t_{rr} | Reverse Recovery Time | $I_{\text{S}}=-3\text{A}$, $V_{\text{GS}}=0\text{V}$, | - | 24 | - | ns |
| Q_{rr} | Reverse Recovery Charge | $dI/dt=100\text{A}/\mu\text{s}$ | - | 11 | - | nC |

Notes:

- 1.Pulse width limited maximum junction temperature.
- 2.Pulse test - pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.Surface mounted on 1 in² copper pad of FR4 board, $t \leq 10\text{sec}$; 156 °C/W when mounted on minimum copper pad.

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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Typical Electrical Characteristics

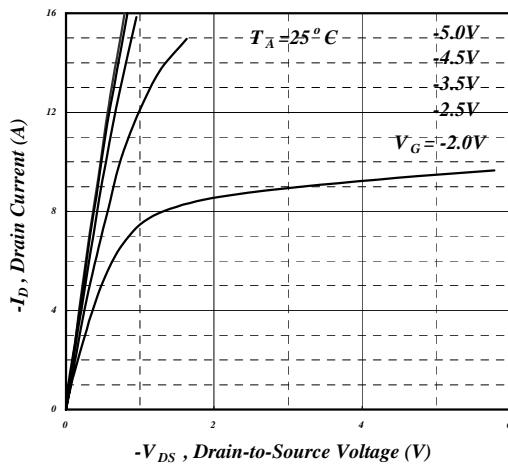


Fig 1. Typical Output Characteristics

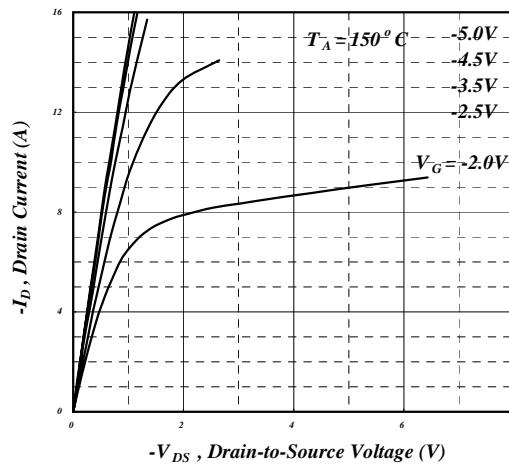


Fig 2. Typical Output Characteristics

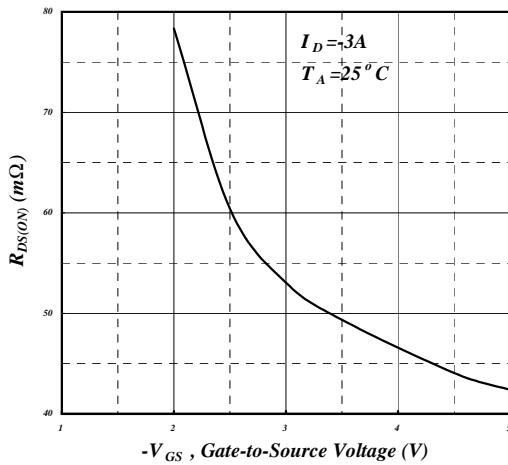


Fig 3. On-Resistance vs. Gate Voltage

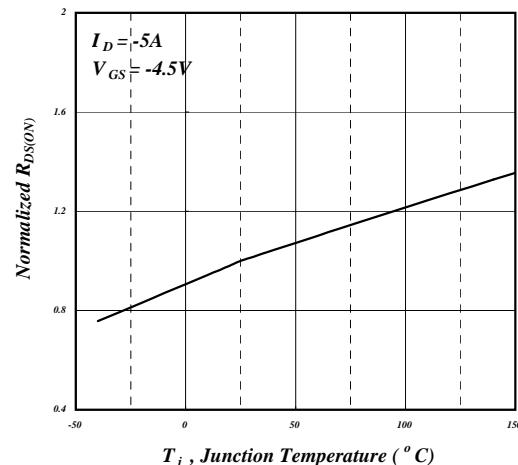


Fig 4. Normalized On-Resistance vs. Junction Temperature

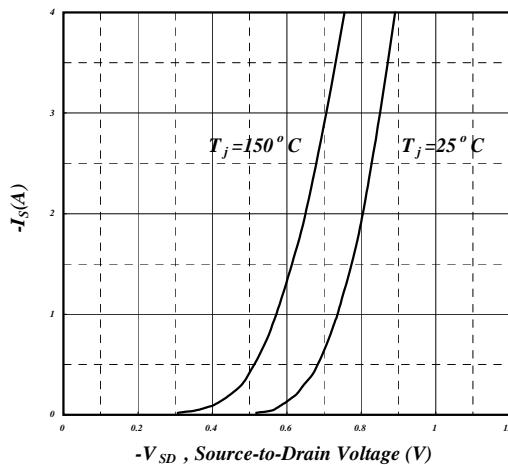


Fig 5. Forward Characteristic of Reverse Diode

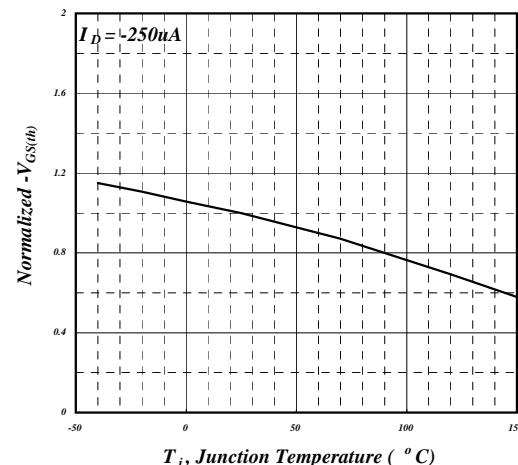


Fig 6. Gate Threshold Voltage vs. Junction Temperature



Typical Electrical Characteristics (cont.)

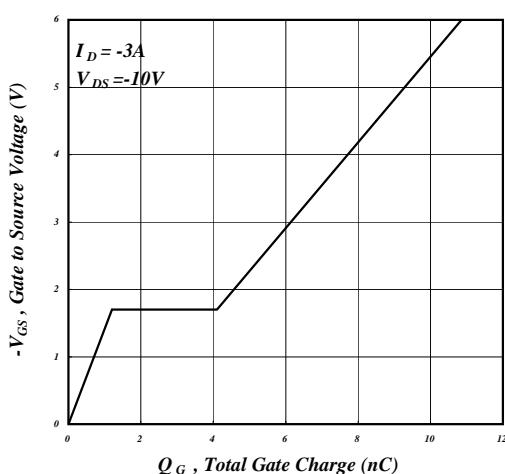


Fig 7. Gate Charge Characteristics

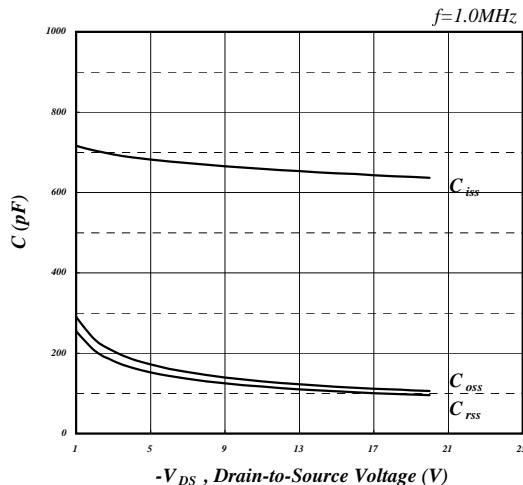


Fig 8. Typical Capacitance Characteristics

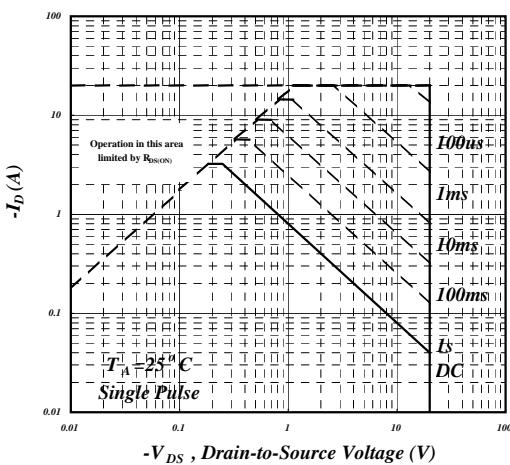


Fig 9. Maximum Safe Operating Area

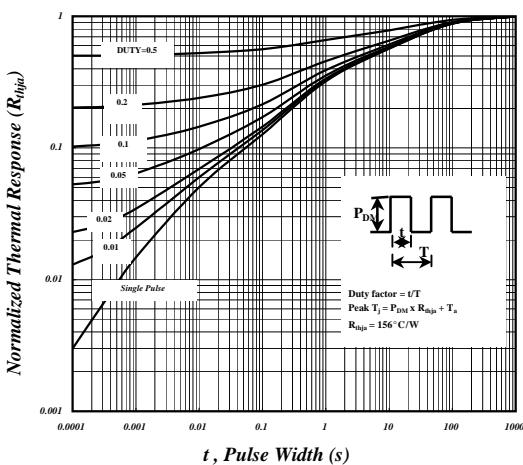


Fig 10. Effective Transient Thermal Impedance

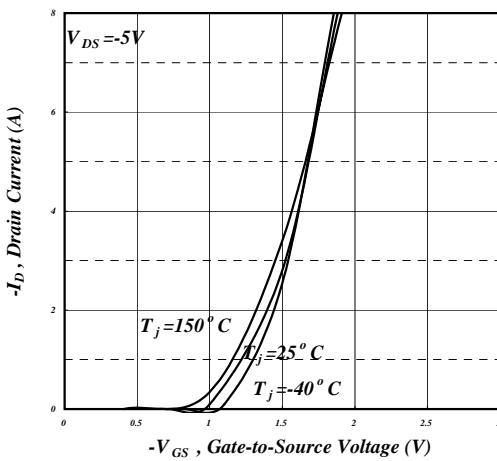


Fig 11. Transfer Characteristics

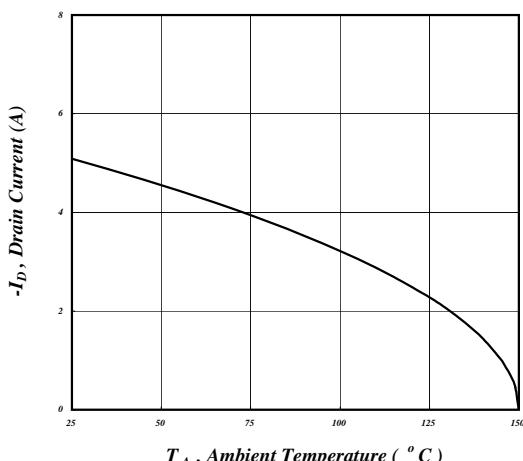
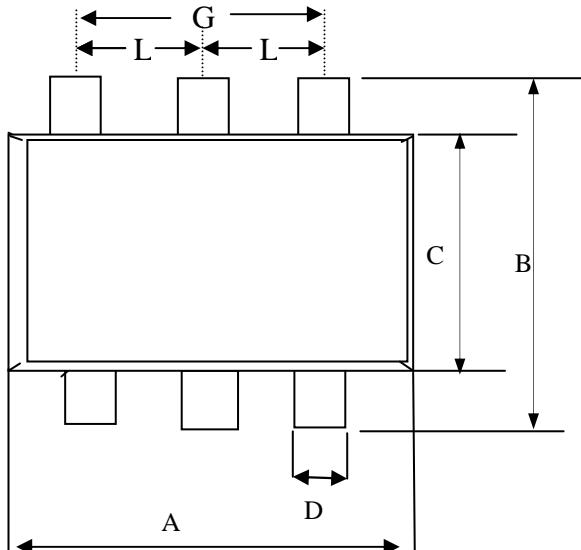


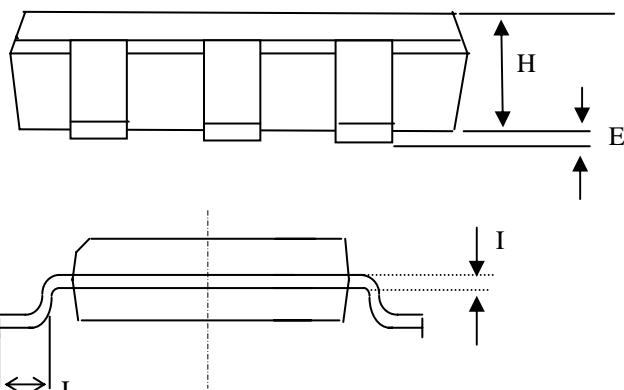
Fig 12. Maximum Continuous Drain Current vs. Ambient Temperature



Package Dimensions: SOT-26



| SYMBOLS | Millimeters | | |
|---------|-------------|------|------|
| | MIN | NOM | MAX |
| A | 2.70 | 2.90 | 3.10 |
| B | 2.60 | 2.80 | 3.00 |
| C | 1.40 | 1.60 | 1.80 |
| D | 0.30 | 0.43 | 0.55 |
| E | 0.00 | 0.05 | 0.10 |
| H | 1.20REF | | |
| G | 1.90REF | | |
| I | 0.12REF | | |
| J | 0.37REF | | |
| L | 0.95REF | | |



1. All dimensions are in millimeters.
2. Dimensions do not include mold protrusions.

Marking Information:

Laser Marking

