

8 pin Dual-in-Line

60.0MHz to 240.0MHz

- Frequency range 60MHz to 240MHz
- **LVDS Output**
- **Supply Voltage 3.3 VDC**
- Phase jitter 0.2ps typical
- Pull range from ±30ppm to ±150ppm

DESCRIPTION

GDA14 VCXOs are packaged in an industry-standard 8 pin dual-inline package. Typical phase jitter for GDA series VCXOs is 0.2 ps. Output is LVDS. Applications include phase lock loop, SONET/ATM, set-top boxes, MPEG, audio/video modulation, video game consoles and HDTV.

SPECIFICATION

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Frequency Range:	60.0MHz to 240.0MHz	
Supply Voltage:	3.3 VDC ±5%	
Output Logic:	LVDS	
RMS Period Jitter		
60.0MHz ~ 120MHz:	2.5ps typical	
120MHz ~ 240MHz:	4.7ps typical	
Peak to Peak Jitter		
60.0MHz ~ 120MHz:	17.5ps typical	
120MHz ~ 240MHz:	24.5ps typical	
Phase Jitter:	0.2ps typical	
Initial Frequency Accuracy:	Tune to the nominal frequency with Vc= 1.65 ±0.2VDC	
Output Voltage HIGH (1):	1.4 Volts typical	
Output Voltage LOW (0):	1.1 Volts typical	
Pulling Range:	From ±30ppm to ±150ppm	
Control Voltage Range:	1.65 ±1.35 Volts	
Temperature Stability:	See table	
Output Load:	50Ω into Vdd or Thevenin equiv.	
	(Terminating resistors required on all outputs)	
Rise/Fall Times:	0.5ns typ., 0.7ns max.	
	20% Vdd to 80% Vdd	
Duty Cycle:	50% ±5%	
	(Measured at Vdd-1.25V)	
Start-up Time:	10ms maximum, 5ms typical	
Current Consumption:	55mA typical, 60mA maximum	
Static Discharge Brotastics	(for 202.50MHz) 2kV maximum	
Static Discharge Protection: Storage Temperature:	-55° to +150°C	
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Ageing: Enable/Disable:	±2ppm per year maximum Not inplemented - 4 pin Package	
RoHS Status:	Fully compliant or non-compliant	
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FREQUENCY STABILITY

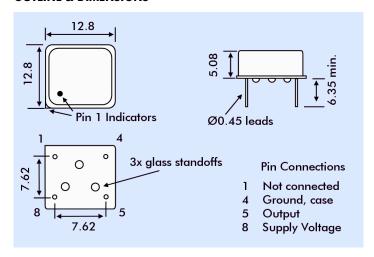
Stability Code	Stability ±ppm	Temp. Range
Α	25	0°~+70°C
В	50	0°∼+70°C
С	100	0°∼+70°C
D	25	-40°~+85°C
E	50	-40°~+85°C
F	100	-40°∼+85°C

If non-standard frequency stability is required Use 'I' followed by stability, i.e. 120 for ±20ppm





OUTLINE & DIMENSIONS



PART NUMBERING

