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DESCRIPTION

iC-BM features four analog multipliers. Each fourquadrant multiplier consists of a Gilbert cell multiplier with a 0.4 scale factor, a linearisation circuit and a unity gain output amplifier. For higher precision all internal bias currents are derived from an internal band-gap reference.

All pins are ESD protected.

PACKAGES

PIN CONFIGURATION SO18W



PIN FUNCTIONS

No. Name Function

1	W1	Channel 1: Analog multiplier output
2	GND1	Channel 1: Ground
3	X1	Channel 1: First input of multiplier
4	Y1	Channel 1: Second input of multiplier
5	VCC	Positive power supply +5 V
6	Y2	Channel 2: Second input of multiplier
7	X2	Channel 2: First input of multiplier
8	GND2	Channel 2: Ground
9	W2	Channel 2: Analog multiplier output
10	W3	Channel 3: Analog multiplier output
11	GND3	Channel 3: Ground
12	Х3	Channel 3: First input of multiplier
13	Y3	Channel 3: Second input of multiplier
14	VEE	Negative power supply -5 V
15	Y4	Channel 4: Second input of multiplier
16	X4	Channel 4: First input of multiplier
17	GND4	Channel 4: Ground
18	W4	Channel 4: Analog multiplier output



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PACKAGE DIMENSIONS SO18W

SIDE









RECOMMENDED PCB-FOOTPRINT



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ABSOLUTE MAXIMUM RATINGS

Beyond these values damage may occur; device operation is not guaranteed.

ltem	Symbol	Parameter	Conditions			Unit
No.	-			Min.	Max.	
G001	VCC	Positive Power Supply			7	V
G002	VEE	Negative Power Supply		-7		V
G003	V()	Voltage at Pins X_{14} , Y_{14} and W_{14}		-7	7	V
G004	Tj	Chip Temperature		-40	150	°C
G005	Ts	Storage Temperature		-40	150	°C

THERMAL DATA

Operating Conditions: VCC = 5 V ±0.25 V , VEE = -5 V ±0.25 V, Tj = -40...100 °C, $R_L = 2 k\Omega$, if not other specified

Item	Symbol	Parameter	Conditions			Unit	
No.	-			Min.	Тур.	Max.	
T01	Та	Operating Ambient Temperature Range		-40		85	°C
T02	Rthja	Thermal Resistance Chip/Ambient			68		K/W



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ELECTRICAL CHARACTERISTICS

Operating Conditions: VCC = 5 V ± 0.25 V , VEE = -5 V ± 0.25 V, Tj = -40...100 °C, R_L = 2 k Ω , if not other specified

ltem No.	Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Gener	al	1	1				
101	V(VCC)	Positive Supply Voltage Range		4.75	5	5.25	V
102	V(VEE)	Negative Supply Voltage Range		-5.25	-5	-4.75	V
103	I(VCC)	Positive Supply Current	W ₁₄ without load resistors		15	20	mA
104	I(VEE)	Negative Supply Current	W ₁₄ without load resistors	-20	-15		mA
105	P _{DISS}	Power Dissipation	$P_{DISS} = 5V\timesI_{CC} + 5V\timesI_{EE}$		150	200	mW
Multip	lier Perform	ance	1	0			
201	V(X ₁₄)os	Offset Voltage X ₁₄	$V(X_{14}) = 0 V, V(Y_{14}) = \pm 2.5 V$	-50		50	mV
202	V(Y ₁₄)os	Offset Voltage Y ₁₄	$V(Y_{14}) = 0 V, V(X_{14}) = \pm 2.5 V$	-50		50	mV
203	V(W ₁₄)os	Output Offset Voltage W ₁₄	$V(X_{14}) = 0 V, V(Y_{14}) = 0 V$	-50		50	mV
204	TCV()os	Output Offset Drift W ₁₄	$V(X_{14}) = 0 V, V(Y_{14}) = 0 V$		50		µV/°C
205	К	Fix Scale Factor	$V(X_{14}) = \pm 2.5 V, V(Y_{14}) = \pm 2.5 V$	0.38	0.4	0.42	1/V
206	TE(X ₁₄)	Total Error X ₁₄	-2.5 V \leq X \leq 2.5 V, Y = 2.5 V, measured as % of the ±2.5 V full scale	-5	±2	5	%
207	TE(Y ₁₄)	Total Error Y ₁₄	-2.5 V \leq Y \leq 2.5 V, X = 2.5 V, measured as % of the ±2.5 V full scale	-5	±2	5	%
208	TCE(X ₁₄)	Total Error Drift X ₁₄	$V(X_{14}) = -2.5 V, V(Y_{14}) = 2.5 V$		0.005		%/°C
209	TCE(Y ₁₄)	Total Error Drift Y ₁₄	$V(Y_{14}) = -2.5 V, V(X_{14}) = 2.5 V$		0.005		%/°C
210	SE()	Total Square Error X ₁₄ , Y ₁₄	$V(X_1) = V(Y_1), V(X_2) = V(Y_2), V(X_3) = V(Y_3)$ and $V(X_4) = V(Y_4)$		5		%
211	LE(X ₁₄)	Linearity Error X ₁₄	-2.5 V \leq X \leq 2.5 V, Y = 2.5 V	-1	±0.2	1	%
212	LE(Y ₁₄)	Linearity Error Y ₁₄	-2.5 V \leq Y \leq 2.5 V, X = 2.5 V	-1	±0.2	1	%
Dynan	nic Performa	ance					u
301	BW	Small Signal Bandwidth	$V(W_{14}) = 0.1 V_{rms}$		3.5		MHz
302	SR	Slew Rate	$V(W_{14}) = \pm 2.5 V$		30		V/µs
303	t _S	Settling Time	$V(W_{14}) = \Delta 2.5 V$ and 1% error band		1		μs
304	FT _{AC}	AC Feedthrough	$V(X_{14}) = 0 V$, $V(Y_{14}) = 1 V_{rms}$ and $f = 1 kHz$		-65		dB
305	CT _{AC}	Crosstalk	$V(X_{14}) = V(Y_{14}) = 1 V_{rms}$, f = 100 kHz, applied to adjecent channel		-90		dB
Outputs: W ₁₄							
401	lsc()	Short Circuit Current			±30		mA
402	THD(X ₁₄)	Total Harmonic Distortion X_{14}	f = 1 kHz, V(Y ₁₄) = 2.5 V		0.1		%
403	THD(Y ₁₄)	Total Harmonic Distortion Y_{14}	f = 1 kHz, V(X ₁₄) = 2.5 V		0.02		%
404	PSSR()	Power Supply Sensitivity Ratio	$V(X_{14}) = V(Y_{14}) = 0 V$, VCC = $\Delta 5\%$ or VEE = $\Delta 5\%$			10	mV/V
405	ENA	Audio Band Noise	BW = 10 Hz to 50 kHz		70		μV _{rms}
406	ENW	Wide Band Noise	BW = 1.9 MHz		590		μV _{rms}
407	en	Spot Noise Voltage	Noise at f = 1 kHz		0.3		µV/√Hz
408	Vmax()	Voltage Swing	VCC = +5 V, VEE = -5 V	3.0	3.3		V
409	ROUT()	Open Loop Output Resistance	VCC = +5 V, VEE = -5 V, T = +25 °C		60		Ω
Inputs: X ₁₄ , Y ₁₄							
501	VR()in	Analog Input Range	V(GND ₁₄) = 0 V	-2.5		2.5*	V
502	l()in	Input Current	$V(X_{14}) = V(Y_{14}) = 0 V$		2.3	10	μA
503	R()in	Input Resistance			1		MΩ
504	C()in	Input Capacitance			3		pF

* For input voltages > 3 V the output is undefined.



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ORDERING INFORMATION

Туре	Package	Order Designation
iC-BM	SO18W	iC-BM SO18W

For technical support, information about prices and terms of delivery please contact:

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