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# RJK0330DPB

# Silicon N Channel Power MOS FET Power Switching

REJ03G1639-0400 Rev.4.00 Apr 10, 2008

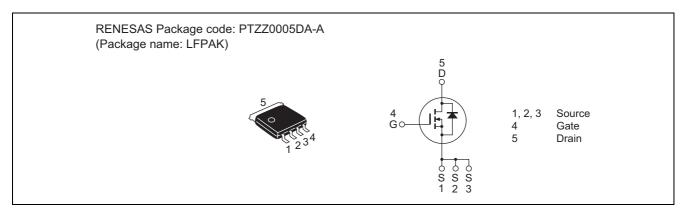
### **Features**

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance

 $R_{DS(on)} = 2.1 \text{ m}\Omega \text{ typ. (at } V_{GS} = 10 \text{ V})$ 

• Pb-free

### **Outline**



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	$V_{GSS}$	±20	V
Drain current	I <sub>D</sub>	45	Α
Drain peak current	I <sub>D(pulse)</sub> Note1	180	Α
Body-drain diode reverse drain current	I <sub>DR</sub>	45	А
Avalanche current	I <sub>AP</sub> Note 2	22	А
Avalanche energy	E <sub>AR</sub> Note 2	48.4	mJ
Channel dissipation	Pch Note3	55	W
Channel to Case Thermal Resistance	θch-C	2.27	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$ 

3. Tc = 25°C

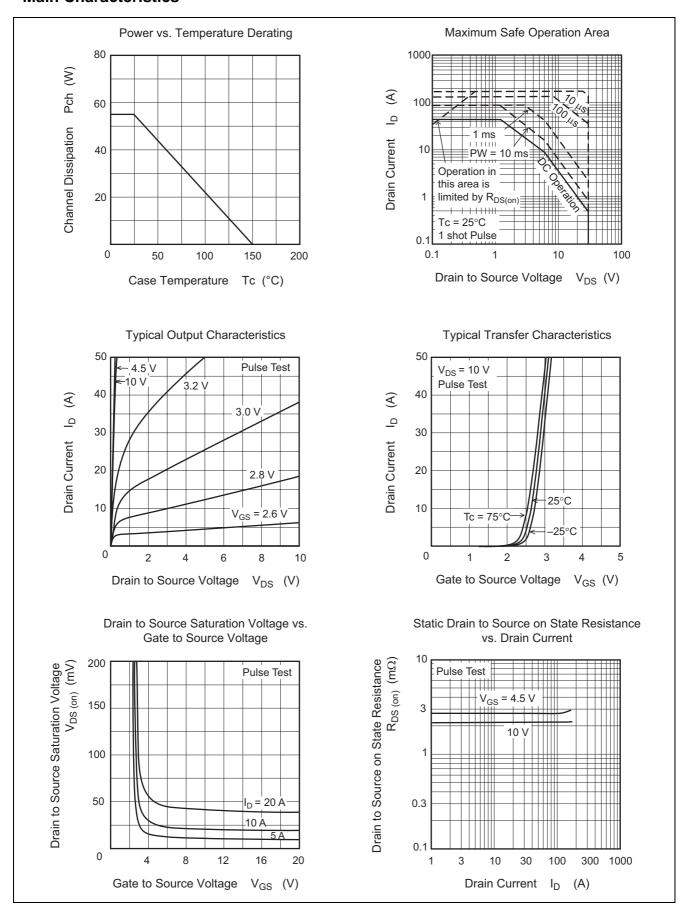
# **Electrical Characteristics**

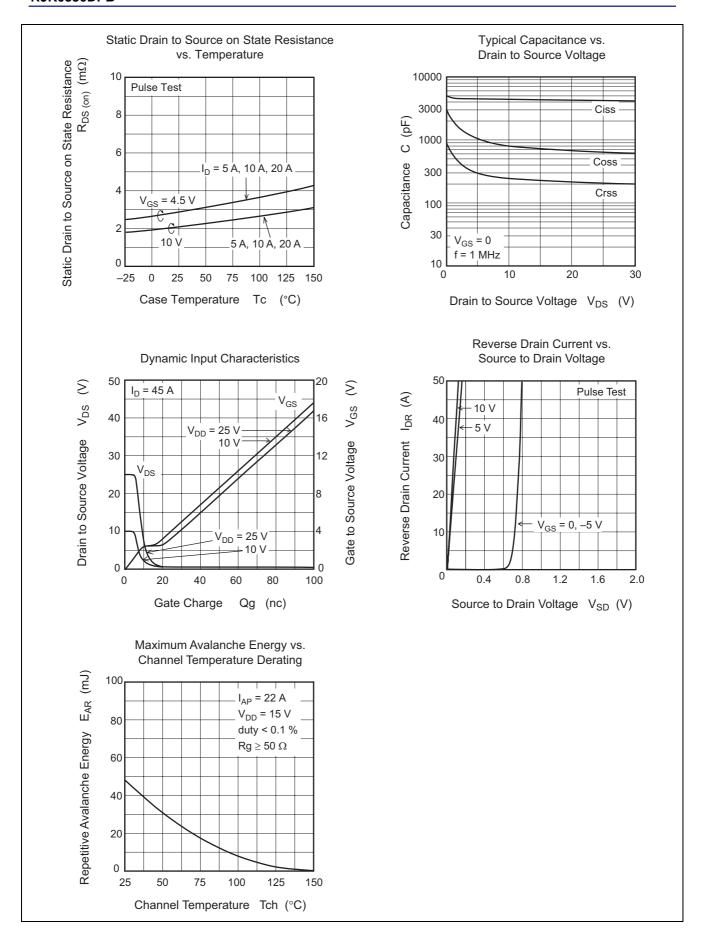
 $(Ta = 25^{\circ}C)$ 

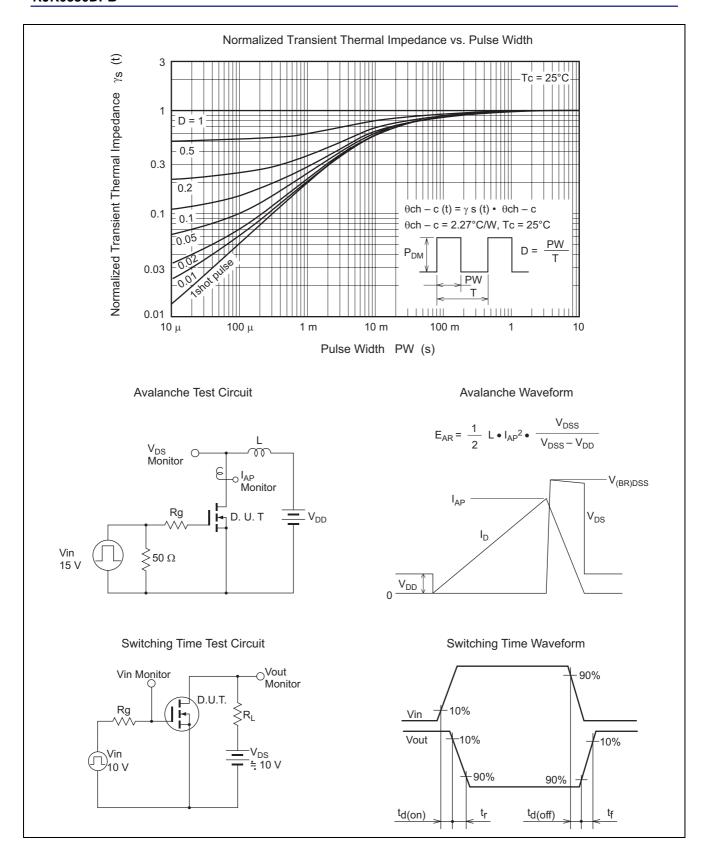
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.2	_	2.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	_	2.1	2.7	mΩ	$I_D = 22.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R <sub>DS(on)</sub>	_	2.8	3.9	mΩ	$I_D = 22.5 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y <sub>fs</sub>	_	90	_	S	$I_D = 22.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	4300	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	800	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	245	_	pF	
Gate Resistance	Rg	_	0.4	_	Ω	
Total gate charge	Qg	_	27	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 4.5 \text{ V},$
Gate to source charge	Qgs	_	10.5	_	nC	I <sub>D</sub> = 45 A
Gate to drain charge	Qgd	_	5.8	_	nC	
Turn-on delay time	t <sub>d(on)</sub>	_	6.8	_	ns	$V_{GS} = 10 \text{ V}, I_D = 22.5 \text{ A},$
Rise time	t <sub>r</sub>	_	3.9	_	ns	$V_{DD} \cong 10 \text{ V}, R_L = 0.44 \Omega,$
Turn-off delay time	t <sub>d(off)</sub>	_	50	_	ns	$Rg = 4.7 \Omega$
Fall time	t <sub>f</sub>	_	5.4	_	ns	
Body-drain diode forward voltage	$V_{DF}$	_	0.78	1.02	V	$I_F = 45 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body–drain diode reverse recovery time	t <sub>rr</sub>	_	36	_	ns	$I_F = 45 \text{ A}, V_{GS} = 0$ $di_F/dt = 100 \text{ A}/ \mu \text{s}$
Body–drain diode reverse recovery charge	Q <sub>rr</sub>	_	34	_	nC	

Notes: 4. Pulse test

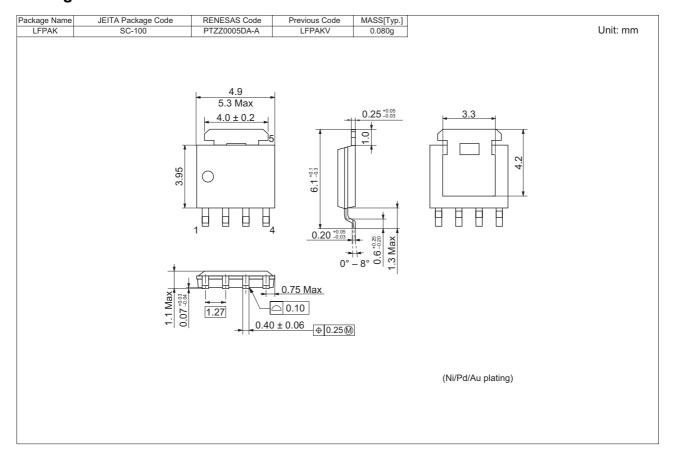
### **Main Characteristics**







# **Package Dimensions**



# **Ordering Information**

Part No.	Quantity	Shipping Container
RJK0330DPB-00-J0	2500 pcs	Taping

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