

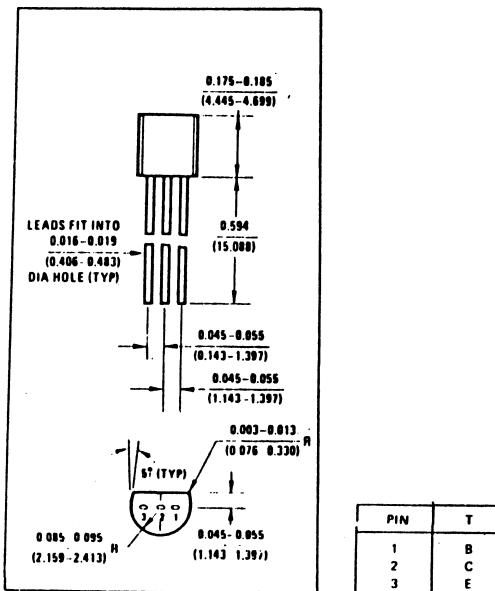
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2N3391

NPN Silicon Transistors



absolute maximum ratings (25°C) unless otherwise specified

Voltages

Collector to Emitter	V_{CEO}	25	V
Emitter to Base	V_{BBO}	5	V
Collector to Base	V_{CBO}	25	V

Current

Collector (Steady State) ⁽¹⁾	I_C	100	mA
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Dissipation

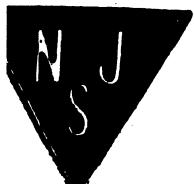
Total Power (Free Air @ 25°C) ⁽²⁾	P_T	360	mW
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Temperature

Storage	T_{STG}	-55 to +150	°C
Operating	T_J	+125	°C
Lead Soldering, $\chi_1'' \pm \chi_2''$ from case for 10 seconds max.	T_L	+260	°C

⁽¹⁾ Determined from power limitations due to saturation voltage at this current.

⁽²⁾ Derate 3.6 mW/°C increase in ambient temperature above 25°C.



electrical characteristics (25°C) unless otherwise specified

		Min.	Typ.	Max.
Collector Cutoff Current ($V_{ce} = 25V$) ($V_{ce} = 25V, T_A = 100^\circ C$)	I_{cso} I_{cs0}		.1 10	μA μA
Emitter Cutoff Current ($V_{ee} = 5V$)	I_{eso}		.1	μA
Forward Current Transfer Ratio ($V_{ce} = 4.5V, I_c = 2 \text{ mA}$)	h_{fe}	250		500
SMALL SIGNAL CHARACTERISTICS				
Forward Current Transfer Ratio ($V_{ce} = 10V, I_c = 100 \mu A, f = 1 \text{ KHz}$)	h_{fe}	170 ^(a)	200	
Input Impedance ($V_{ce} = 10V, I_c = 2 \text{ mA}, f = 1 \text{ KHz}$)	h_{ib}		15	ohms
Output Capacitance ($V_{ce} = 10V, I_e = 0, f = 1 \text{ MHz}$)	C_{cio}	2.0	7	10 pF
Gain Bandwidth Product ($I_c = 2 \text{ mA}, V_{ce} = 5 \text{ V}$)	f_t		120	MHz
NOISE				
(wide band—15 Hz to 10 KHz, Equivalent Noise Bandwidth = 15.7 KHz)				
Noise Figure ($I_c = 100 \mu A, V_{ce} = 4.5V, R_s = 5000 \text{ ohms}$)	NF		1.9	5 db

^(a) Typically a minimum of 95% of the distribution is above this value.