

WPT2N40B

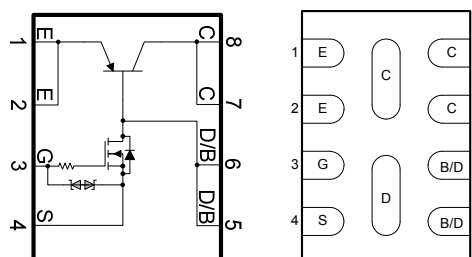
PNP, -32V, -1A, Power Transistor with 20V N-MOSFET

Descriptions

The WPT2N40B is PNP bipolar power transistor with 20V N-MOSFET. This device is suitable for use in charging circuit and other power management. Standard Products are Pb-free and Halogen-free.



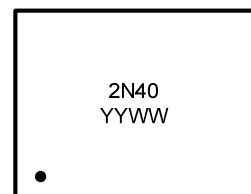
DFN3x2-8L



Pin configuration (Top view)

Features

- Ultra low collector-to-emitter saturation voltage
- High DC current gain >100
- 1A continue collector current
- Small package DFN3x2-8L



2N40 = Device code
YY = Year
WW = Week
Marking

Applications

- Charging circuit
- Other power management in portable equipments

Order information

Device	Package	Shipping
WPT2N40-8/TR	DFN3x2-8L	3000/Reel&Tape

Absolute maximum ratings

Parameter	Symbol	Value	Unit
PNP Transistor			
Collector-emitter voltage	V_{CEO}	-32	V
Collector-base voltage	V_{CBO}	-45	V
Emitter-base voltage	V_{EBO}	-6	V
Continues collector current	I_C	-1	A
Pulse collector current ^c	I_{CM}	-6	A
N-MOSFET			
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	±6	V
Continuous Drain Current	I_D	0.69	A
Pulsed Drain Current ^c	I_{DM}	1.4	A
Power Dissipation and temperature			
Power dissipation ^a	P_D	1.2	W
Power dissipation ^b		0.8	W
Junction Temperature	T_J	150	°C
Lead Temperature	T_L	260	°C
Operation Temperature	T_A	-40 ~ 85	°C
Storage Temperature Range	T_{stg}	-55 to 150	°C

Thermal resistance characteristics

Parameter	Symbol	Value	Unit
Junction-to-Ambient Thermal Resistance ^a	$R_{\theta JA}$	104	°C/W
Junction-to-Ambient Thermal Resistance ^b	$R_{\theta JA}$	155	°C/W

a Surface mounted on FR-4 Board using 1 square inch pad size, 1oz copper

b Surface mounted on FR-4 board using minimum pad size, 1oz copper

c Pulse width=300μs, Duty Cycle<2%

d Maximum junction temperature $T_J=150^{\circ}\text{C}$.

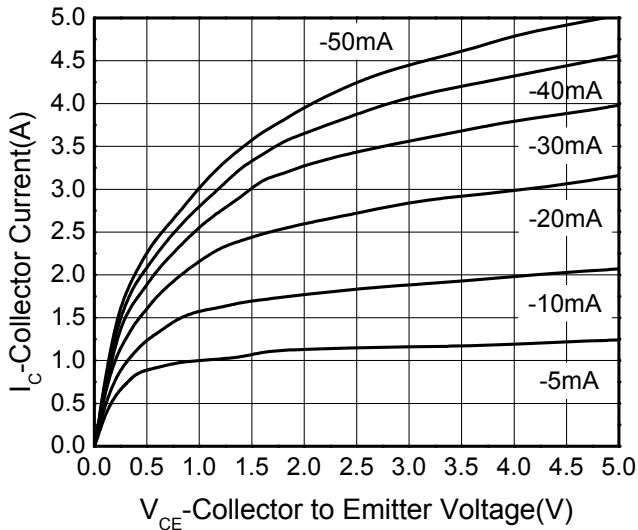
Electronics Characteristics (Ta=25°C, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
PNP Transistor						
Collector-emitter breakdown voltage	BV_{CEO}	$I_C=-10mA, I_B=0mA$	-32			V
Collector-base breakdown voltage	BV_{CBO}	$I_C=-100\mu A, I_E=0mA$	-45			V
Emitter-base breakdown voltage	BV_{EBO}	$I_E=-1mA, I_C=0mA$	-6			V
Collector cutoff current	I_{CBO}	$V_{CB}=-40V, I_E=0mA$			-100	nA
Emitter cutoff current	I_{EBO}	$V_{EB}=-5V, I_C=0mA$			-100	nA
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C=-2A, I_B=-200mA$		-0.38	-0.5	V
Base-emitter saturation voltage	$V_{BE(sat)}$	$I_C=-2A, I_B=-200mA$		-1	-1.5	V
DC current gain	h_{FE}	$V_{CE}=-2V, I_C=-1A$	100	163	320	
N-MOSFET						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	20			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=16V, V_{GS}=0V$			1	μA
Gate –Source leakage current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 5V$			± 5	μA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.44	0.67	0.86	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=4.5V, I_D=0.55A$		210	270	m Ω
		$V_{GS}=2.5V, I_D=0.55A$		250	320	m Ω
		$V_{GS}=1.8V, I_D=0.35A$		305	390	m Ω
Input Capacitance	C_{iss}	$V_{DS}=10V,$		50		pF
Output Capacitance	C_{oss}	$V_{GS}=0V,$		13		pF
Reverse Transfer Capacitance	C_{rss}	$F=1Mhz$		8		pF
Total Gate Charge	$Q_{G(TOT)}$	$V_{DS}=10V,$ $V_{GS}=4.5V,$ $I_D=0.6A$		1.15		nC
Threshold gate charge	$Q_{G(TH)}$			0.06		nC
Gate-Source Charge	Q_{GS}			0.15		nC
Gate-Drain Charge	Q_{GD}			0.23		nC
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=10V, V_{GS}=4.5V,$ $I_D=0.5A,$ $R_L=10\Omega, R_G=6\Omega$		22		ns
Turn-On Rise Time	t_r			80		ns
Turn-Off Delay Time	$t_{d(off)}$			700		ns
Turn-Off Fall Time	t_f			650		ns
Body Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=0.35A$	0.5	0.85	1.5	V

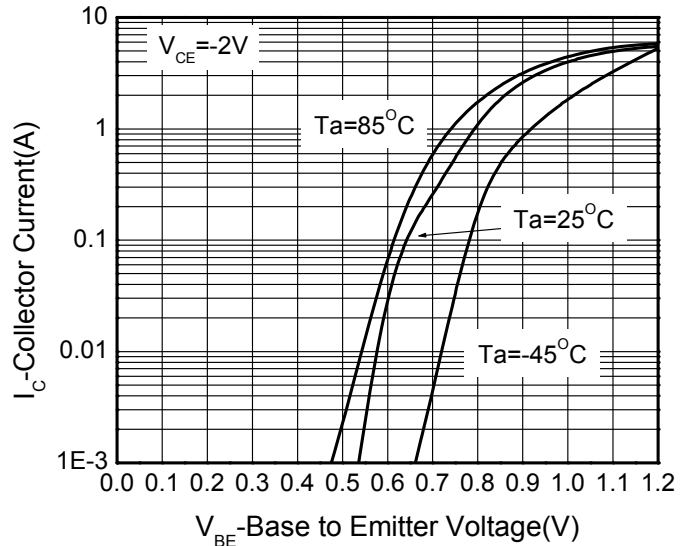
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Typical Characteristics (Ta=25°C, unless otherwise noted)

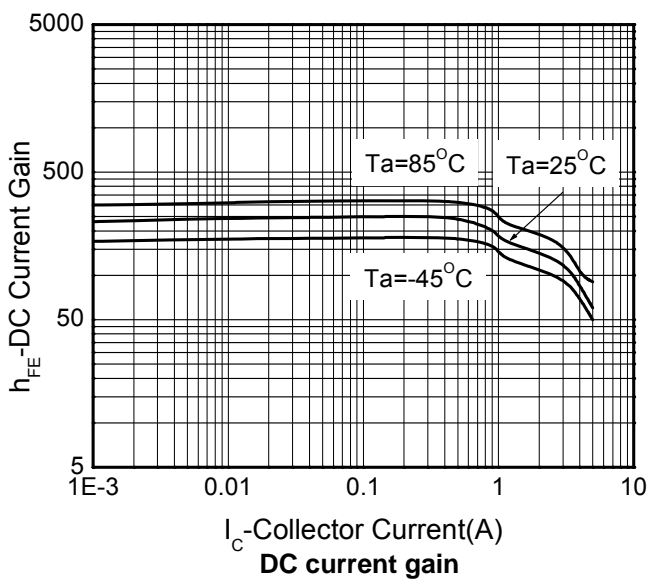
PNP Transistor



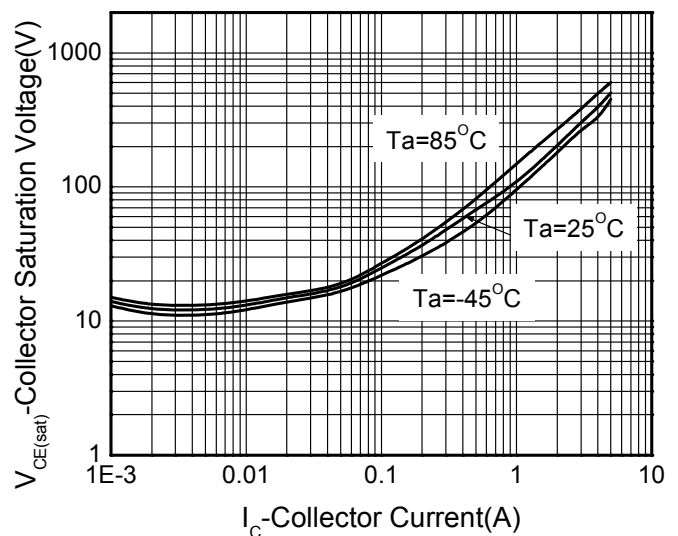
Output characteristics



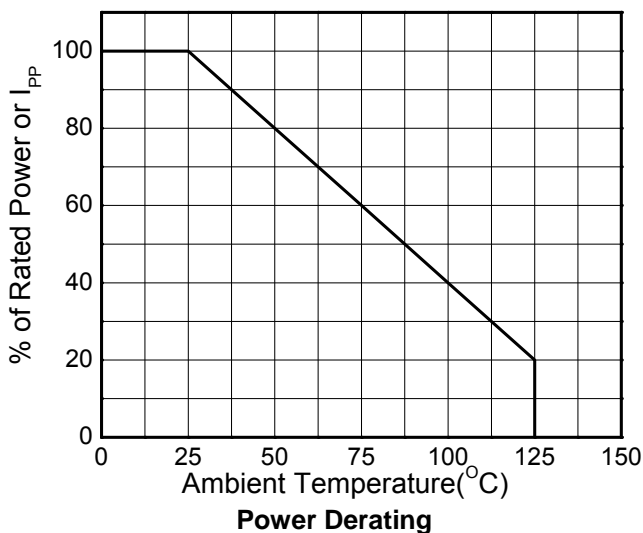
Transfer characteristics



DC current gain



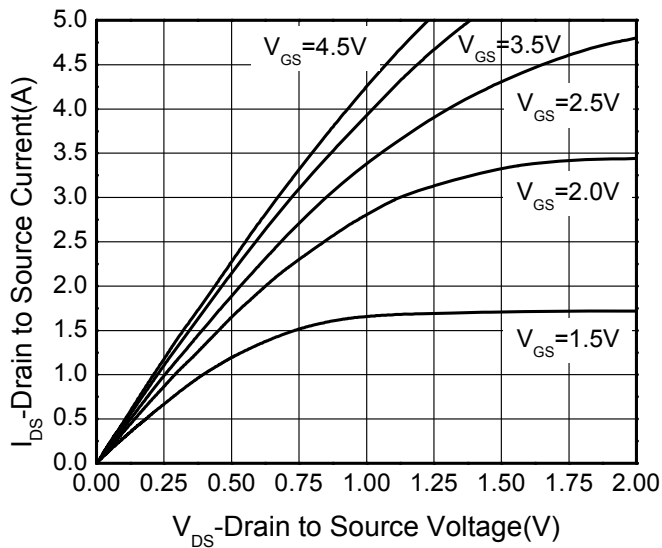
C-E saturation voltage vs. Collector current



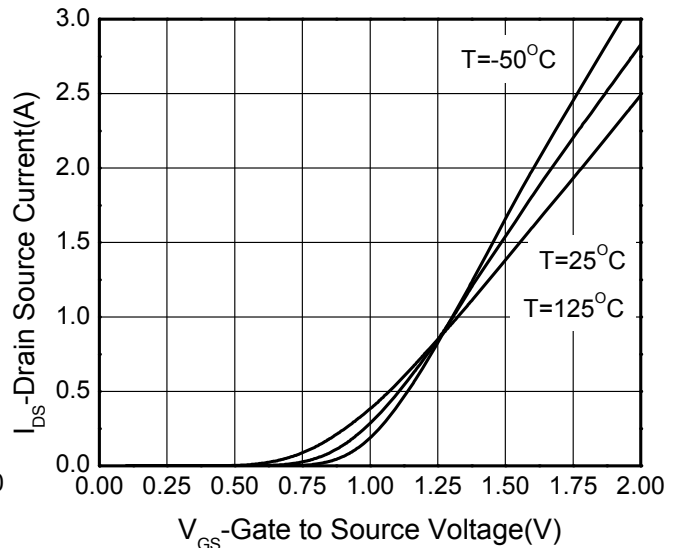
Power Derating

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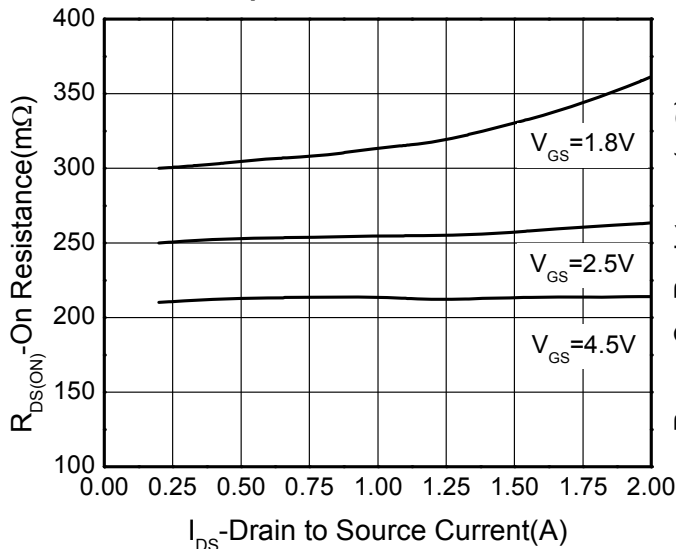
N-MOSFET



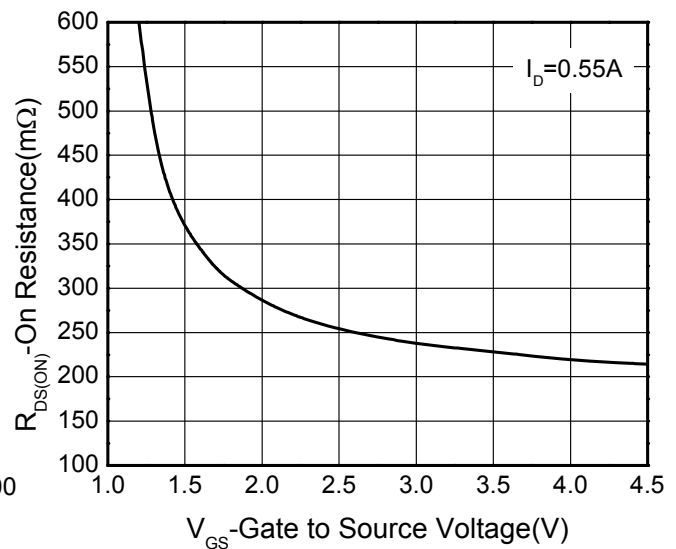
Output Characteristics



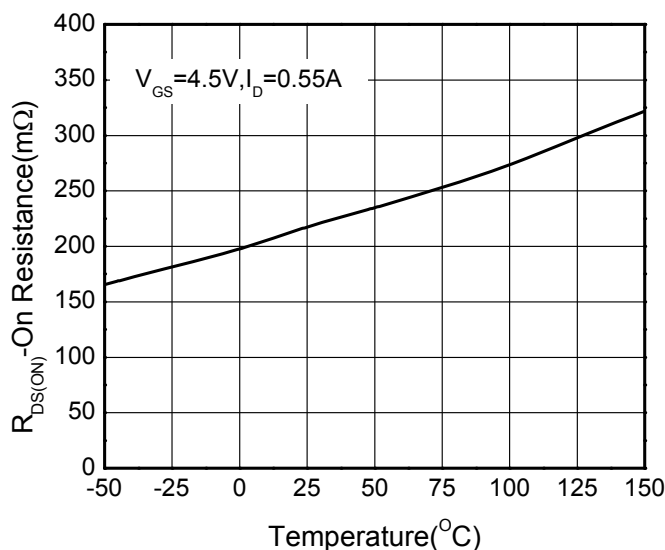
Transfer Characteristics



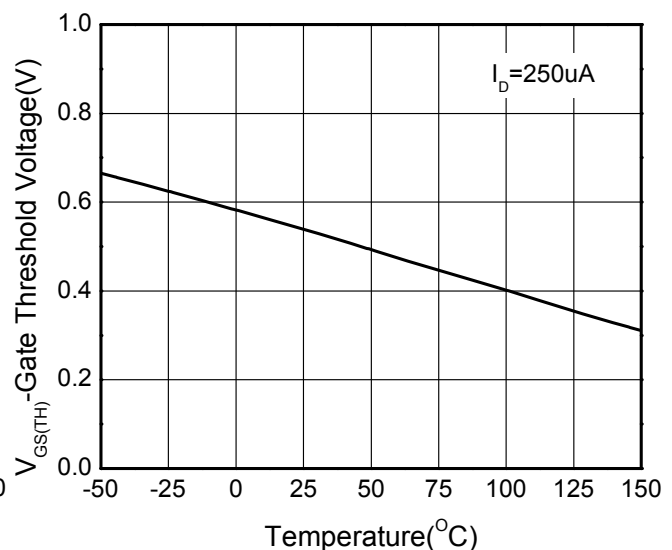
ON Resistance vs. Drain Current



ON Resistance vs. Gate-to-Source Voltage

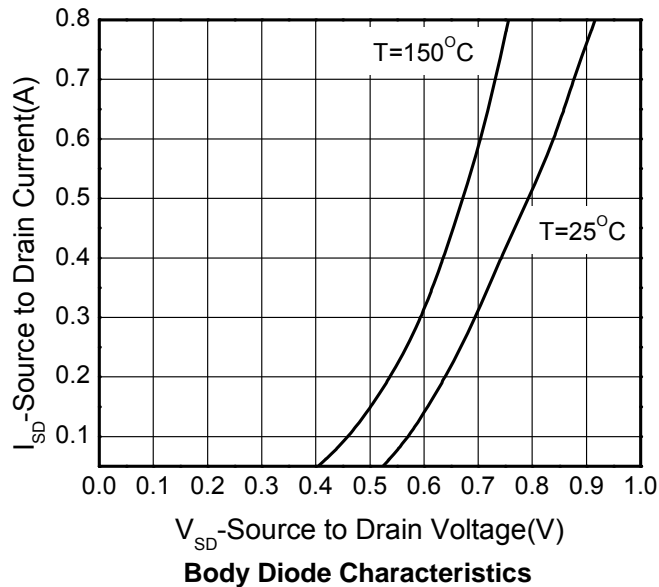
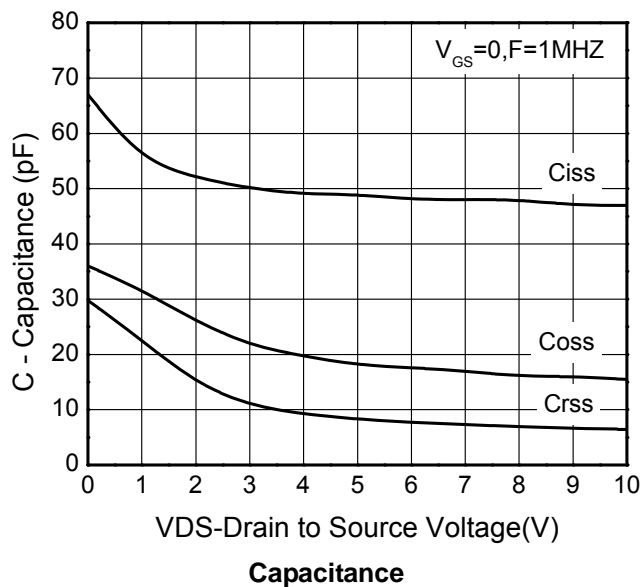


ON Resistance vs. Junction Temperature



Threshold Voltage vs. Temperature

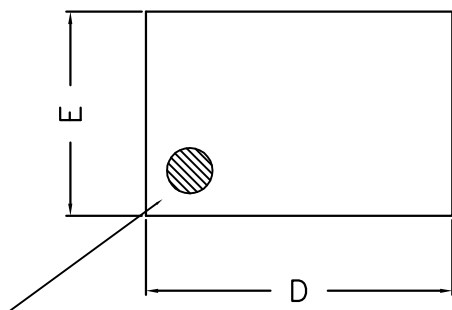
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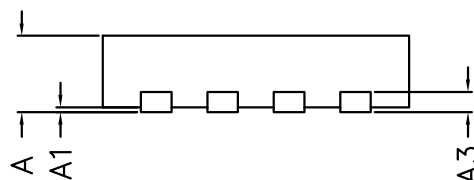
Package outline dimensions

DFN3x2-8L

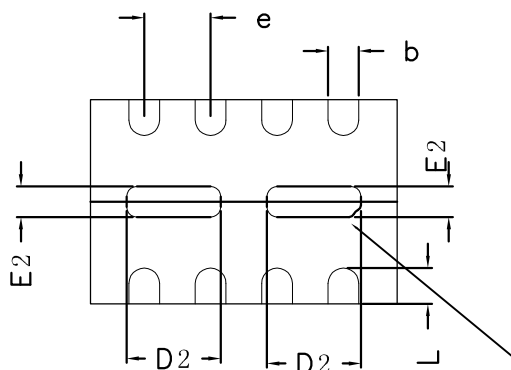


PIN 1 DOT
BY MARKING

TOP VIEW



SIDE VIEW



R=0.15
PIN #1 IDENTIFICATION
CHAMFEP

BOTTOM VIEW

Symbol	Dimensions In Millimeters		
	Min.	Typ.	Max.
A	0.70	0.75	0,80
A1	0.00	-	0.05
A3	0.2REF		
D	2.95	3.00	3.05
E	1.95	2.00	2.05
B	0.25	0.30	0.35
L	0.25	0.35	0.45
D2	0.77	0.92	1.02
E2	0.15	0.30	0.40
e	0.65 BSC		