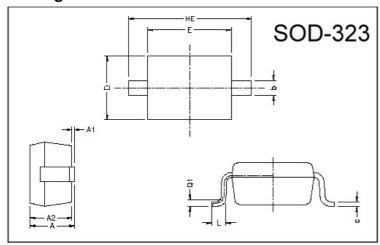
GD421SD

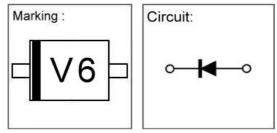
SURFACE MOUNT, SCHOTTKY BARRIER DIODE **VOLTAGE 40V, CURRENT 0.1A**

Description

The GD421SD is designed for low power rectification.

Package Dimensions





| REF. | Millimeter | | REF. | Millimeter | |
|------|------------|------|------|------------|------|
| | Min. | Max. | NEF. | Min. | Max. |
| Α | 0.85 | 1.05 | | | |
| A1 | 0 | 0.10 | L | 0.20 | 0.40 |
| A2 | 0.80 | 1.00 | b | 0.25 | 0.40 |
| D | 1.15 | 1.45 | С | 0.10 | 0.18 |
| Е | 1.60 | 1.80 | | | |
| HE | 2.30 | 2.70 | Q1 | 0.15 BSC. | |

Absolute Maximum Ratings at Ta = 25℃

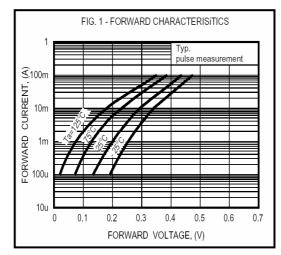
| Parameter | Symbol | Ratings | Unit | |
|---|------------------|------------|-------------------------|--|
| Junction Temperature | Tj | +125 | $^{\circ}$ C | |
| Storage Temperature | Tstg | -40 ~ +125 | $^{\circ}\! \mathbb{C}$ | |
| Maximum Recurrent Peak Reverse Voltage | V_{RRM} | 40 | V | |
| Maximum RMS Voltage | V_{RPS} | 28 | V | |
| Maximum DC Blocking Voltage | V _{DC} | 40 | V | |
| Peak Forward Surge Current at 8.3mSec single half sine-wave | I _{FSM} | 1.0 | A | |
| Typical Junction Capacitance between Terminal | CJ | 6.0 | pF | |
| Maximum Average Forward Rectified Current | lo | 0.1 | A | |
| Total Power Dissipation | PD | 225 | mW | |

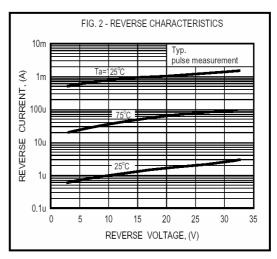
Characteristics at Ta = 25°C

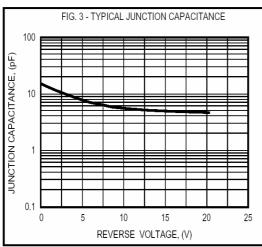
| Parameter | Symbol | Min. | Тур. | Max. | Unit | Test Conditions |
|---------------------------|---------------|------|------|------|------|-----------------|
| Reverse Breakdown Voltage | V(BR)R | 40 | - | - | ٧ | IR=50μA |
| Forward Voltage | VF(1) | - | 1 | 340 | mV | IF=10mA |
| Forward voilage | V F(2) | - | - | 550 | mV | IF=100mA |
| Reverse Leakage Current | lr | - | - | 30 | μΑ | VR=10V |

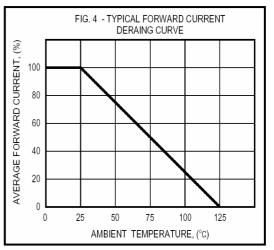
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Characteristics Curve









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