# **MORNSUN®**

Fixed input voltage, isolated & regulated single output





#### **FEATURES**

- Continuous Short Circuit Protection
- Operating temperature range: -40°C to +105°C
- Isolation voltage: 1K VDC
- Ultra Compact SMD package
- Internal surface mounted design
- International standard pin-out

#### Patent Protection RoHS

IB\_(X)T-1W & IB\_(X)T-W75 series is specially designed for applications where an isolated voltage is required in a distributed power supply system. It is suitable for

- 1. Where the voltage of the input power supply is stable (voltage variation: ±5%Vin).
- 2.Where isolation is necessary between input and output (isolation voltage ≤ 1000VDC).
- 3. Where has high requirement of line regulation, load regulation and the ripple & noise of the output voltage,

Part No.	Input Voltage (VDC)	Output		Efficiency	
	Nominal (Range)	Output Voltage (VDC)	Output Current (mA) (Max./Min.)	(%,Min./Typ.) @ Full Load	Max. Capacitive Load(µF)
IB0305T-W75		5	150/15	63/67	
IB0305XT-1W	3.3 (3.13~3.46)	5	200/20	60/64	
IB0505(X)T-W75		5	150/15	64/68	
IB0512(X)T-1W	5 (4.75~5.25)	12	83/9	65/69	220
IB0515T-1W	(4.70-0.20)	15	67/7	65/69	
IB1205(X)T-W75		5	150/15	63/67	
IB1212(X)T-1W	12 (11.4~12.6)	12	83/9	65/69	
IB1215T-1W	(11.4.12.0)	15	67/8	66/70	

Input Specifications						
Item	Operating Conditions	Min.	Тур.	Max.	Unit	
	3.3V input	_	500/40	_		
Input Current (full load / no-load)	5V input	-	300/30	_	mA	
	12V input	-	130/20	_		
	3.3V input	-0.7		5	VDC	
Surge Voltage (1sec. max.)	5V input	-0.7	_	9		
	12V input	-0.7	-	15		
Input Filter Capacitor filter			itor filter			

ltem	Operating Conditions	Min.	Тур.	Max.	Unit
Output Voltage Accuracy			See tolerance envelope graph (Fig. 1)		
Line Regulation Input voltage change: ±1%		_	_	±1.2	0/
Load Regulation	10%-100% load	_	12	-	%
Ripple*	001 #  -	_	10	20	
Noise*	20MHz bandwidth	_	50	100	mVp-p
Temperature Drift Coefficient	100% load	_		±0.03	<b>%/</b> ℃
Output Short Circuit Protection			Continuous,	self-recovery	,

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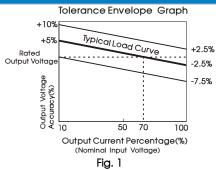
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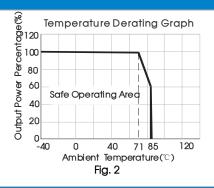
General Specifications						
Item	Operating Conditions	Min.	Тур.	Max.	Unit	
Isolation Voltage	Input-output, with the test time of 1 minute and the leak current lower than 1mA	1000		_	VDC	
Isolation Resistance	Input-output, Isolation voltage 500VDC	1000	-	_	MΩ	
Isolation Capacitance	Input-output, 100KHz/0.1V	-	40	_	pF	
Operating Temperature	Derating if the temperature ≥71°C (see Fig. 2)	-40		85		
Storage Temperature		-55		125		
Casing Temperature Rise	Ta=25℃	15		25	~ °C	
Pin Welding Resistance Temperature	Welding spot is 1.5mm away from the casing, 10 seconds	_	_	260	260	
Reflow Soldering Temperature			≤245°C, maxi r actual appli J-STD-020D.1.			
Storage Humidity	Non-condensing	-		95	%	
Switching Frequency	100% load, nominal input voltage		100	-	KHz	
MTBF	MIL-HDFK-217F@25°C	3500		_	K hours	

Physical Specifications			
Casing Material Black flame-retardant heat-proof epoxy resin (UL94-V0)			
Package Dimensions	15.24*11.20*6.50 mm		
Weight	1.7 g (Typ.)		
Cooling Method	Free air convection		

EMC Specifications					
EMI	Conducted disturbance	CISPR22/EN55022	CLASS A (see Fig. 4 for recommended circuit)		
EMS	Electrostatic discharge	IEC/EN61000-4-2	Contact ±6KV perf. Criteria B		

### Product Characteristic Curve



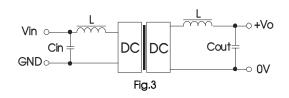


### Design Reference

#### 1. Typical application

If it is required to further reduce input and output ripple, a "LC" filter capacitor can be connected to the input and output terminals, see Fig.3.

It should also be noted that the inductance and the frequency of the "LC" filtering network should be staggered with the DC/DC frequency to avoid mutual interference. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor sees Table 1.



Recommended capacitive load value table (Table 1)

	•		
Vin	Cin	Vout	Cout
(VDC)	( µ F)	(VDC)	(µF)
3.3	4.7	5	4.7
5	4.7	12	1
12	2.2	15	1

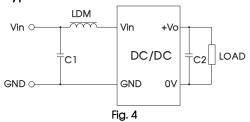
It is not recommended to connect any external capacitor when output power is less than 0.5W.

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#### 2. EMC typical recommended circuit



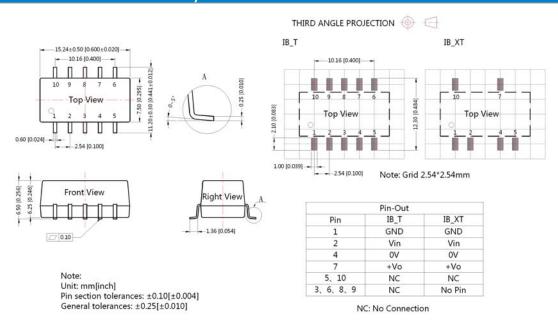
Input vol	tage (VDC)	3.3/5/12
	C1	4.7µF /50V
EMI	C2	Refer to the Cout in Fig.3
	LDM	6.8µH

#### 3. Output load requirements

To ensure the module work efficiently and reliably, during the operation, the min. output load should be no less than 10% of the full load. If the actual output power is low, please connect a resister to the output terminal in parallel, with a recommenced resistance which is 10% of the rated power, and derating is required during operation.

#### 4. For more information please find the application notes on www.mornsun-power.com

#### Dimensions and Recommended Layout



#### Notes:

- 1. Packing Information please refer to 'Product Packing Information'. Packing bag number: 58200019;
- 2. If the product is operated under the min. required load, the product performance cannot be guaranteed to comply with all performance indexes in this datasheet;
- The max. capacitive load should be tested within the input voltage range and under full load conditions;
- Unless otherwise specified, data in this datasheet should be tested under the conditions of Ta=25°C, humidity<75% when inputting nominal voltage and outputting rated load;
- 5. All index testing methods in this datasheet are based on our Company's corporate standards;
- The performance indexes of the product models listed in this manual are as above, but some indexes of non-standard model products will exceed the above-mentioned requirements, and please directly contact our technicians for specific information;
- 7. We can provide product customization service;
- 8. Specifications of this product are subject to changes without prior notice.

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