

Outline Drawing and Circuit Diagram

| Dim. | Inches | mm |
| :---: | :---: | :---: |
| A | 14.9 | 378.4 |
| B | 10.15 | 257.8 |
| C | 7.4 | 193.0 |
| D | 14.25 | 362.0 |
| E | 0.3 | 7.7 |
| F | 2.95 | 75.0 |
| G | 8.0 | 203.2 |
| H | 1.0 | 25.4 |
| J | 2.01 | 51.0 |


| Dim. | Inches | mm |
| :---: | :---: | :---: |
| K | 0.79 | 20.0 |
| L | 7.20 | 183.0 |
| M | 5.4 | 137.2 |
| N | 4.33 | 110.0 |
| P | 0.256 Dia. | 6.5 Dia. |
| Q | M6 Metric | M6 |
| R | 0.32 | 8.2 |
| S | 0.51 | 12.9 |
| T | 14.32 | 363.6 |



## Description:

The Powerex POW-R-PAK ${ }^{\text {TM }}$ is a configurable IGBT based power assembly that may be used as a converter, chopper, half or full bridge, or three phase inverter for motor control, power supply, UPS or other power conversion applications.

The power assembly is mounted on a forced air-cooled heatsink and features state-of-the-art Powerex IGBTs with low conduction and low switching losses for high efficiency operation. The POW-R-PAK ${ }^{\text {TM }}$ includes a low inductance laminated bus structure, optically isolated gate drive interfaces, isolated gate drive power supplies, and a DC-link capacitor bank. The control board provides a simple user interface along with built-in protection features including overvoltage, undervoltage lockout, overcurrent, overtemperature, and short circuit detection.

Depending on application characteristics, the POW-RPAK ${ }^{\text {TM }}$ is suitable for operation with DC bus voltages up to 800 VDC and switching frequencies below 20 kHz .

## Features:

$\square$ High performance IGBT inverter bridge
Integrated gate drive with fault monitoring and protection
$\square$ System status / troubleshooting LEDs to verify or monitor proper operation
$\square$ Isolated gate drive power supplies
$\square$ Low inductance laminated bus
$\square$ Output current measurement and feedback
$\square$ Superior short circuit detection \& shoot through prevention

Powerex, Inc., 173 Pavilion Lane, Youngwood, Pennsylvania 15697 (724) 925-7272 www.pwrx.com

## PP150T120-ND

3-Phase POW-R-PAK ${ }^{\text {TM }}$ IGBT Assembly
150 Amperes/1200 Volts

## Absolute Maximum Ratings, $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$ unless otherwise specified

Module

| Characteristics | Symbol | Rating | Units |
| :--- | :---: | :---: | :---: |
| IGBT Junction Temperature | $\mathrm{T}_{\mathrm{j}}$ | -40 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | $\mathrm{T}_{\text {stg }}$ | -40 to +65 | ${ }^{\circ} \mathrm{C}$ |
| Operating Temperature | $\mathrm{T}_{\mathrm{op}}$ | -20 to +60 | ${ }^{\circ} \mathrm{C}$ |
| Voltage Applied to DC Terminals | $\mathrm{V}_{\mathrm{CC}}$ | 900 | Volts |
| Isolation Voltage, Main Terminals to Heatsink | $\mathrm{V}_{\text {iso }}$ | 2500 | Volts |

## IGBT Part

| Characteristics | Symbol | Rating | Units |
| :--- | :---: | :---: | :---: |
| Collector Current $\left(\mathrm{DC}, \mathrm{T}_{\mathrm{C}^{\prime}}=83^{\circ} \mathrm{C}\right)$ | $\mathrm{I}_{\mathrm{C}}$ | 150 | Amperes |
| Peak Collector Current | $\mathrm{I}_{\mathrm{CM}}$ | 300 | Amperes |
| Emitter Current $\left(\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}\right)$ | $\mathrm{I}_{\mathrm{E}}$ | 150 | Amperes |
| Peak Emitter Current | $\mathrm{I}_{\mathrm{EM}}$ | 300 | Amperes |
| Maximum Collector Dissipation $\left(\mathrm{T}_{\mathrm{j}}<150^{\circ} \mathrm{C}\right.$ per Module $)$ | $\mathrm{P}_{\mathrm{C}}$ | 960 | Watts |

## Interface Board

| Characteristics | Symbol | Rating | Units |
| :--- | :---: | :---: | :---: |
| Unregulated +24V Power Supply Input | - | 30 | Volts |
| IGBT Command Signal Input Voltage | - | 20 | Volts |
| Fault Output Supply Voltage | - | 30 | Volts |
| Fault Output Current | - | 50 | mA |

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150 Amperes／1200 Volts

## Electrical Characteristics， $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$ unless otherwise specified

## IGBT Part

| Characteristics | Symbol | Test Conditions | Min． | Typ． | Max． | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Collector Cutoff Current | ICES | $\mathrm{V}_{\mathrm{CE}}=\mathrm{V}_{\mathrm{CES}}, \mathrm{V}_{\mathrm{GE}}=0 \mathrm{~V}$ | － | － | 1.0 | mA |
| Collector－Emitter Saturation Voltage | $\mathrm{V}_{\text {CE（sat）}}$ | $\mathrm{I}_{\mathrm{C}}=150 \mathrm{~A}, \mathrm{~T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$ | － | 2.1 | 3.0 | Volts |
|  |  | $\mathrm{I}_{\mathrm{C}}=150 \mathrm{~A}, \mathrm{~T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ | － | 2.4 | － | Volts |
| Emitter－Collector Voltage | $\mathrm{V}_{\mathrm{EC}}$ | $\mathrm{I}_{\mathrm{E}}=150 \mathrm{~A}$ | － | － | 3.8 | Volts |
| Turn－on Delay Time | $\mathrm{t}_{\mathrm{d} \text {（on）}}$ | $\begin{gathered} \mathrm{V}_{\mathrm{CC}}=600 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=150 \mathrm{~A}, \mathrm{~V}_{\mathrm{GE}}= \pm 15 \mathrm{~V}, \\ \mathrm{R}_{\mathrm{G}}=1.6 \Omega \text {, Inductive Load } \end{gathered}$ | － | － | 130 | Ns |
| Rise Time | $t_{r}$ |  | － | － | 100 | Ns |
| Turn－off Delay Time | $\mathrm{t}_{\mathrm{d} \text {（off）}}$ |  | － | － | 450 | Ns |
| Fall Time | $t_{f}$ |  | － | － | 350 | Ns |
| Diode Reverse Recovery Time | $\mathrm{trr}^{\text {r }}$ | $\mathrm{I}_{\mathrm{E}}=150 \mathrm{~A}$ | － | － | 150 | Ns |
| Diode Reverse Recovery Charge | $\mathrm{Q}_{\mathrm{rr}}$ | $\mathrm{I}_{\mathrm{E}}=150 \mathrm{~A}$ | － | 9.0 | － | $\mu \mathrm{C}$ |

## Interface Board

| Characteristics | Min． | Typ． | Max． | Units |
| :---: | :---: | :---: | :---: | :---: |
| Unregulated＋24V Power Supply Input | 20 | 24 | 30 | Volts |
| Power Supply Current Consumption | － | － | 800 | mA |
| IGBT Command Signal ON Threshold | 12 | 15 | － | Volts |
| IGBT Command Signal OFF Threshold | － | 0 | 2 | Volts |
| IGBT Command Signal Input Impedance | － | 10 | － | k ת |
| IGBT Command Signal Input Capacitance | － | 1 | － | nF |
| Dead Time | － | 3.0 | － | $\mu \mathrm{s}$ |

## Feedback Signal and Fault Characteristics

| Characteristics | Min． | Typ． | Max． |
| :--- | :---: | :---: | :---: |
| Output Over Current Trip | - | 250 | - |
| Amperes |  |  |  |
| Heatsink Over Temperature Trip | - | 95 | - |
| Bus Over Voltage Trip | - | 920 | - |
| Power Supply Under Voltage Trip | - | 18.9 | - |
| Fault Reset Time | - | 9 | - |
| Heatsink Temperature Feedback | - | $0.1 \mathrm{~V} /{ }^{\circ} \mathrm{C}$ | - |
| Output Current Feedback（Bipolar） | - | $\pm \mathrm{s}$ |  |
| DC Link Feedback | $-0.01 \mathrm{~V} / \mathrm{Amp}$ | - | Volts |

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## Electrical Characteristics, $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$ unless otherwise specified

## Other Electrical Component Specifications

| Characteristics | Min. | Typ. | Max. |
| :--- | :---: | :---: | :---: |
| Units |  |  |  |
| DC Link Capacitor Bank |  |  |  |
| Total Nominal Capacitance | - | 2300 | - |
| Nominal Ripple Current Rating per Capacitor $\left(@ 85^{\circ} \mathrm{C}, 120 \mathrm{~Hz}\right)$ | - | 11.6 | - |
| Nominal Total Voltage Rating | - | 1000 | - |
| Minimum Life @ Nominal Ripple Current $\left(80^{\circ} \mathrm{C}\right)$ | - | 10 | - |
| Output Current Sensor |  |  |  |
| Primary Current Measuring Range | - | $\pm 200$ | - |
| Accuracy (@ IPN, $\left.25^{\circ} \mathrm{C}\right)$ | - | $< \pm 1$ | - |
| Linearity Error | - | $< \pm 1$ | - |
| Response Time | - | $<5$ | - |
| Bandwidth $(-3 \mathrm{~dB})$ | DC | - | 25 |

Thermal Characteristics, $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$ unless otherwise specified

| Characteristics | Symbol | Test Conditions | Min. | Typ. | Max. |
| :--- | :---: | :---: | :---: | :---: | :---: |
| IGBT Thermal Resistance, Junction-to-Case | $\mathrm{R}_{\text {th(j-c) } \mathrm{Q}}$ | Per IGBT, $1 / 2$ Module | - | - | 0.13 |
| FWD Thermal Resistance, Junction-to-Case | $\mathrm{R}_{\mathrm{th}(\mathrm{j}-\mathrm{c})} \mathrm{D}$ | Per FWD, $1 / 2$ Module | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |  |  |
| Contact Thermal Resistance | $\mathrm{R}_{\text {th(c-f) }}$ | Per $1 / 2$ Module | - | - | 0.23 |
| ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |  |  |  |  |  |
| Heatsink Thermal Resistance | $\mathrm{R}_{\text {th(f-a })}$ | 286 CFM Airflow | - | 0.022 | - |

Mechanical Characteristics, $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$ unless otherwise specified

| Characteristics | Min. | Typ. | Max. |
| :--- | :---: | :---: | :---: |
| Units |  |  |  |
| Mounting Torque, Output Power Terminals | - | 75 | 90 |
| Mounting Torque, DC Bus Terminals | - | 130 | 150 |
| Weight | - | 39 | - |

## Relevant Standards

UL508C: Power Conversion Equipment
EN50178: Electronic Equipment for Use in Power Installations

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## PP150T120-ND

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## Interface Board Signal Defintions (Table 1)

| Pin | Signal Name | Description |
| :---: | :---: | :---: |
| 1 | Shield | Internally Conected to PGND |
| 2 | Gate 1 Neg. | 0-15V Signal Controlling, Lower IGBT, HIGH = IGBT on |
| 3 | Leg 1 Error ${ }^{1}$ | Open Collector Output, External Pull-up Resistor Required <br> LOW = No Error; HIGH = Phase A Over Current or Short Circuit OR Power Supply UV |
| 4 | Gate 1 Pos. | 0-15V Signal Controlling, Upper IGBT, HIGH = IGBT on |
| 5 | Gate 2 Neg. | 0-15V Signal Controlling, Lower IGBT, HIGH = IGBT on |
| 6 | Leg 2 Error ${ }^{1}$ | Open Collector Output, External Pull-up Resistor Required <br> LOW = No Error; HIGH = Phase A Over Current or Short Circuit OR Power Supply UV |
| 7 | Gate 2 Pos. | 0-15V Signal Controlling, Upper IGBT, HIGH = IGBT on |
| 8 | Gate 3 Neg. | 0-15V Signal Controlling, Lower IGBT, HIGH = IGBT on |
| 9 | Leg 3 Error ${ }^{1}$ | Open Collector Output, External Pull-up Resistor Required <br> LOW = No Error; HIGH = Phase A Over Current or Short Circuit OR Power Supply UV |
| 10 | Gate 3 Pos. | 0-15V Signal Controlling, Upper IGBT, HIGH = IGBT on |
| 11 | Over Temperature ${ }^{1}$ | Open Collector Output, External Pull-up Resistor Required LOW = No Error; HIGH = Heatsink OT |
| 12 | External Fault Reset | Active Low: Must be High for Operation; Low for 10microseconds to Reset Faults ${ }^{3}$ |
| 13 | DC Link Voltage | Analog Voltage Feedback of DC Link Voltage |
| 14 | 24 VDC Input Power | 20-30 VDC Input Power Supply |
| 15 | 24 VDC Input Power | 20-30 VDC Input Power Supply |
| 16 | N/C | No Connect - Do Not Ground |
| 17 | N/C | No Connect - Do Not Ground |
| 18 | PGND | Ground Reference for 24 VDC Power Supply |
| 19 | PGND | Ground Reference for 24 VDC Power Supply |
| 20 | Heatsink Temperature | Analog Voltage Representation of Heatsink Temperature |
| 21 | AGND ${ }^{2}$ | Tied to Pins 10 and 11 |
| 22 | IOUT Phase A | Analog Voltage Representation of Output Current |
| 23 | AGND ${ }^{2}$ | Tied to Pins 10 and 11 |
| 24 | Iout Phase B | Analog Voltage Representation of Output Current |
| 25 | AGND ${ }^{2}$ | Tied to Pins 10 and 11 |
| 25 | lout Phase C | Analog Voltage Representation of Output Current |

1. Open collectors can be pulled up to 30 V max. and sink 50 mA continuous.
2. AGND signals to be used for analog feedback signals (i.e. twisted pair with lout Phase A).
3. On the board is a jumper that enables fault reset by bringing all leg control signals low for 10 microseconds (default).

## Interface Board Connector

| Description | Symbol | Type | Manufacturer |
| :--- | :---: | :---: | :---: |
| Gate Drive Board Interface Header | P1 | $0.100^{\prime \prime} \times 0.100 "$ Latching Header, 26 Pin | 3 M\# 3429-6002 or Equivalent |
| Recommended Mating Socket | - | $0.100^{\prime \prime} \times 0.100 "$ IDC Socket, 26 Pin | $3 M \# 3499-7600$ or Equivalent |
| Recommended Strain Relief | - | Plastic Strain Relief | $3 M \# 3448-3026$ or Equivalent |

