S1C17003



Low Power 16-bit Single Chip Microcontroller

- Low power MCU (Operating voltage 1.65V ~, Power consumption SLEEP 1 uA, HALT 3.3 uA)
- High code density and high processing power 16bitRISC C17CPU, optimize to C, serial ICE
- 64KB ROM, 4KB RAM
- 10-bit ADC 4ch
- Small package: 48 pins WCSP

DESCRIPTIONS

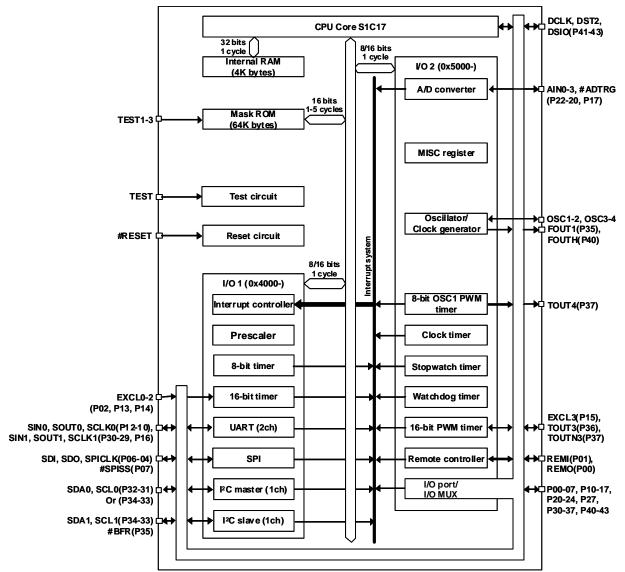
The S1C17003 is a 16-bit MCU featuring high-speed low-power operations, compact dimensions, wide address space and on-chip ICE. A/D converter is built in and sensor of various analog I/F can be connected. It is suitable for the application of health care product, sports watch and meter module etc. with sensor that is required a small size and micro display in the battery driven.

■ FEATURES

●CPU	Epson original 16-bit RISC CPU core S1C17 16-bit x 16-bit + 32-bit product-sum processor 16 bit ÷ 16bit division arithmetic unit	
OSC3 oscillator circuit	Crystal oscillator circuit or ceramic oscillator circuit, 20 MHz (max.)	
OSC1 oscillator circuit	Crystal oscillator circuit 32.768 kHz (typ.)	
Internal MASK ROM	64 Kbytes (for both instructions and data)	
Internal RAM	4 Kbytes	
A/D Converter	10 bit resolution	4ch
Input/output port	Max. 30-bit general purpose input/output port, 4-bit input only port	
Serial interface	SPI (master/slave)	1ch.
	I ² C (master)	1ch.
	l ² C (slave)	1ch.
	UART (460,800bps, IrDA1.0 compatible)	2ch.
_	Remote controller (REMC)	1ch.
Timer	8-bit timer (T8F)	2ch.
	16-bit timer (T16)	3ch.
	PWM timer (T16E)	1ch.
	Clock timer (CT)	1ch.
	Stopwatch timer (SWT) Watchdog timer (WDT)	1ch. 1ch.
	8-bit OSC1 PWM timer (T8OSC1)	1ch.
Interrupt	NMI, P Port Input interrupt	3ch.
Therapt	Serial Interface interrupt	5ch
	Timer interrupt	9ch.
Power supply voltage	•	
• I ewel supply voltage	LVDD(Core) : 1.65 to 1.95V	
	AVDD(I/O) : 2.7V to 3.6V	
Operating temperatures	-40°C to 85°C	
Current consumption	SLEEP mode: 1 µA (typ.) off/1.8V	
-	HALT mode: 3.3 µA (typ.) 32kHz/1.8V	
	When operating: 4.0 mA (typ.) 20MHz	z/1.8V
Shipping form TQFP12-64pin (7 mm x 7 mm x 1.2 mm, 0.4 mm pin pitch)		0.4 mm pin pitch)
	WCSP-48pin (3.124 mm x 3.124 mm x 0.78 mm, 0.4 mm ball pitch) Chip	

S1C17003

Block Diagram



NOTICE:

No part of this material may be reproduced or duplicated in any form or by any means without the written permission of Seiko Epson. Seiko Epson reserves the right to make changes to this material without notice. Seiko Epson does not assume any liability of any kind arising out of any inaccuracies contained in this material or due to its application or use in any product or circuit and, further, there is no representation that this material is applicable to products requiring high level reliability, such as, medical products. Moreover, no license to any intellectual property rights is granted by implication or otherwise, and there is no representation or warranty that anything made in accordance with this material will be free from any patent or copyright infringement of a third party. This material or portions thereof may contain technology or the subject relating to strategic products under the control of the Foreign Exchange and Foreign Trade Law of Japan and may require an export license from the Ministry of Economy, Trade and Industry or other approval from another government agency.

All brands or product names mentioned herein are trademarks and/or registered trademarks of their respective companies.

©Seiko Epson Corporation 2010, All rights reserved.

SEIKO EPSON CORPORATION

SEMICONDUCTOR OPERATIONS DIVISION

IC Sales Department IC International Sales Group 421-8 Hino, Hino-shi, Tokyo 191-8501, JAPAN Phone: +81-42-587-5814 FAX: +81-42-587-5117 Epson semiconductor website

http://www.epson.jp/device/semicon_e/

Document code: 411826701 First issue Oct, 2009 Revised Feb, 2010 in Japa