EURO QUARTZ

EQ5032 TCXO

9.6MHz to 30.0MHz

5.0 x 3.2mm SMD TCXO

- Miniature 5.0 x 3.2 x 1.5mm SMD package
- Frequency range: 9.6MHz to 30.0MHz
- Available with either CMOS or clipped sinewave output
- May be ordered as TCXO or Voltage-Controlled TCXO
- Supply voltage 3.3 Volts

DESCRIPTION

EQ5032 TCXOs are packaged in a miniature 4 pad ceramic, 5.0 x 3.2mm outline SMD package. The part is available as a TCXO or as a voltage-controlled TCXO (VCTCXO). Output may be specified as either clipped sinewave or CMOS. The part has excellent phase nose characteristics and provides a stable source of clock signals over a wide operating temperature range.

SPECIFICATION

SSB PHASE NOISE at 25°C

Product Code:	EQ5032
Frequency Range:	9.6MHz to 30.0MHz
Frequency Stability	
vs. Temperature Range:	±2.0 or ±2.5ppm
vs. Supply Voltage (±5%):	±0.3ppm maximum
vs. Load Change (±5%):	±0.3ppm maximum
vs. Ageing (at 25°C):	±1.0ppm maximum
Intitial Frequency Tolerance:	±1.5ppm maximum
Operating Temperature Range:	-30° to +80°C
Storage Temperature Range:	-40° to +90°C
Supply Voltage:	+2.4V min., +3.6V max.
Current Consumption	
CMOS:	6.0mA maximum
Clipped Sinewave: 2.0mA r	
Clipped Sinewave: 2.0mA r Output Voltage Level	
Output Voltage Level	naximum
Output Voltage Level CMOS:	80% Vdd minimum
Output Voltage Level CMOS: Clipped Sinewave:	80% Vdd minimum
Output Voltage Level CMOS: Clipped Sinewave: Output Load	80% Vdd minimum 0.8V p-p minimum
Output Voltage Level CMOS: Clipped Sinewave: Output Load CMOS:	naximum 80% Vdd minimum 0.8V p-p minimum 1kΩ//15pF
Output Voltage Level CMOS: Clipped Sinewave: Output Load CMOS: Clipped Sinewave:	naximum 80% Vdd minimum 0.8V p-p minimum 1kΩ//15pF
Output Voltage Level CMOS: Clipped Sinewave: Output Load CMOS: Clipped Sinewave: Dynamic Behaviour	naximum 80% Vdd minimum 0.8V p-p minimum 1kΩ//15pF 10kΩ//10pF
Output Voltage Level CMOS: Clipped Sinewave: Output Load CMOS: Clipped Sinewave: Dynamic Behaviour -20° to +80°C:	naximum 80% Vdd minimum 0.8V p-p minimum 1kΩ//15pF 10kΩ//10pF ±0.30ppm/°C
Output Voltage Level CMOS: Clipped Sinewave: Output Load CMOS: Clipped Sinewave: Dynamic Behaviour -20° to +80°C: -30° to -20°C:	aximum 80% Vdd minimum 0.8V p-p minimum 1kΩ//15pF 10kΩ//10pF ±0.30ppm/°C ±1.0ppm/°C

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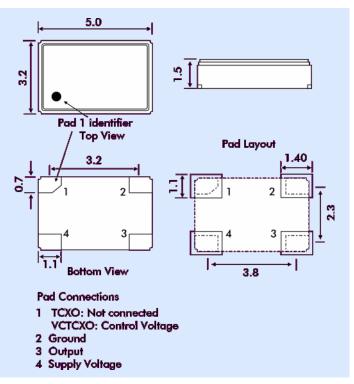
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ROHS

EQ5032 - OUTLINES AND DIMENSIONS

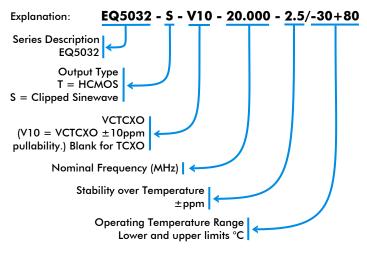


EQ5032 VOLTAGE CONTROL SPECIFICATION

Control Voltage:	Nominal 50% Vdd (0V min, Vdd max.)
Frequency Deviation:	±5.0ppm min., ±15ppm max.

PART NUMBERS

Example: EQ5032-S-V10-20.000-2.5/-30+80



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