SPECIFICATION FOR APPROVAL

()	Prel	iminaı	ry Sp	ecific	ation
---	---	------	--------	-------	--------	-------

(\vee) Final Specification

Title 15.4" WXGA TFT LCD

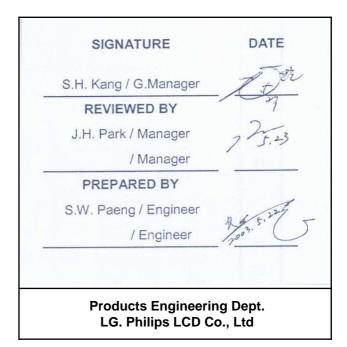
BUYER	COMPAL(TOSHIBA)		
MODEL			

SUPPLIER	LG.Philips LCD Co., Ltd.		
*MODEL	LP154W01		
Suffix	A1		

^{*}When you obtain standard approval, please use the above model name without suffix

DATE

Please return 1 copy for your confirmation with your signature and comments.





Contents

No	ITEM		
	COVER	1	
	CONTENTS	2	
	RECORD OF REVISIONS	3	
1	GENERAL DESCRIPTION	4	
2	ABSOLUTE MAXIMUM RATINGS	5	
3	ELECTRICAL SPECIFICATIONS		
3-1	ELECTRICAL CHARACTREISTICS	6	
3-2	INTERFACE CONNECTIONS	7	
3-3	SIGNAL TIMING SPECIFICATIONS	9	
3-4	SIGNAL TIMING WAVEFORMS	9	
3-5	COLOR INPUT DATA REFERNECE	10	
3-6	POWER SEQUENCE	11	
4	OPTICAL SFECIFICATIONS	12	
5	MECHANICAL CHARACTERISTICS	16	
6	RELIABLITY	20	
7	INTERNATIONAL STANDARDS		
7-1	SAFETY	21	
7-2	EMC	21	
8	PACKING		
8-1	DESIGNATION OF LOT MARK	22	
8-2	PACKING FORM	22	
9	PRECAUTIONS	23	
А	APPENDIX. Enhanced Extended Display Identification Data	25	
В	APPENDIX. Inspection Criteria	28	



RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	Note
0.0	JAN.07. 2003	-	First Draft	
1.0	May.23.2003	22	Designation of Lot mark and packing form revision	
		25,27	Add color coordinates in EDID data (Check sum is changed)	
			<u> </u>	

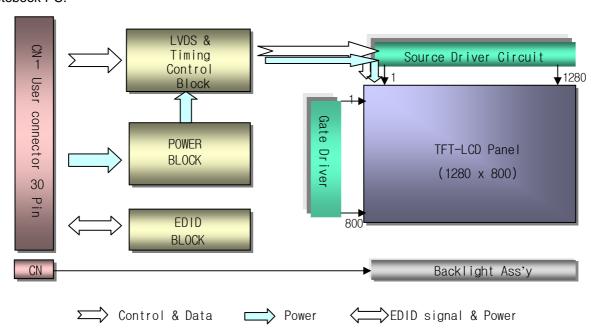


1. General Description

The LP154W01(A1) is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp (CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 15.4 inches diagonally measured active display area with WXGA resolution(900 vertical by 1440 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP154W01(A1) has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP154W01(A1) is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP154W01(A1)characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	15.4 inches diagonal
Outline Dimension	344.0 (H) × 222.0 (V) × 6.5(D, max) mm
Pixel Pitch	0.25875 mm × 0.25875 mm
Pixel Format	1280 horiz. By 800 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	185 cd/m²(Typ.) 5 point
Power Consumption	Total 5.66 Watt(Typ.) @ LCM circuit 1.52 Watt(Typ.), B/L input 4.14 Watt(Typ.)
Weight	590 g (Typ.)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer

Ver. 1.0 May. 23, 2003 4 / 29



2. Absolute Maximum Ratings

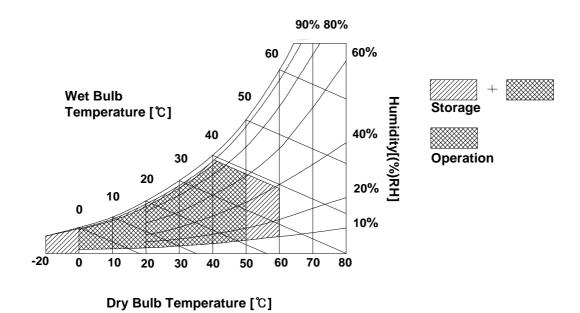
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes
i arameter	Symbol	Min	Max	Offics	Notes
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C
Operating Temperature	Тор	0	50	°C	1
Storage Temperature	Нѕт	-20	60	°C	1
Operating Ambient Humidity	Нор	10	90	%RH	1
Storage Humidity	Нѕт	10	90	%RH	1

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.



Ver. 1.0 May. 23, 2003 5 / 29



3. Electrical Specifications

3-1. Electrical Characteristics

The LP154W01(A1)requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Values Symbol Unit Parameter Notes Min Max Тур MODULE: Power Supply Input Voltage VCC 3.3 3.0 3.6 V_{DC} Power Supply Input Current 391 460 529 mΑ I_{CC} **Power Consumption** 1.52 Watt Рс 1.75 1 Differential Impedance 100 110 Ohm 2 Zm 90 LAMP : 670 690 810 Operating Voltage 3 V_{BL} V_{RMS} (6.5mA) (6.0mA) (3.5mA) 3.5 6.0 6.5 **Operating Current** $\mathsf{mA}_{\mathsf{RMS}}$ 4 I_{BL} **Power Consumption** 4.14 4.35 P_{BL} 9 **Operating Frequency** 60 80 kHz 45 7 f_{BL} Discharge Stabilization Time Min Ts 3 5 Life Time 10,000 Hrs 6 Established Starting Voltage 8 $V_{\rm RMS}$ at 25℃ Vs 1200 at 0 ℃ 1560 V_{RMS}

Table 2. ELECTRICAL CHARACTERISTICS

Note)

- 1. The specified current and power consumption are under the Vcc = 3.3V, $25^{\circ}C$, fv = 60Hz condition whereas full black pattern is displayed and fv is the frame frequency.
- 2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
- 3. The variance of the voltage is \pm 10%.
- 4. The typical operating current is for the typical surface luminance (L_{WH}) in optical characteristics.
- 5. Define the brightness of the lamp after being lighted for 5 minutes as 100%, Ts is the time required for the brightness of the center of the lamp to be not less than 95%.
- 6. The life time is determined as the time at which brightness of lamp is 50% compare to that of initial value at the typical lamp current.
- 7. The output of the inverter must have symmetrical (negative and positive) voltage waveform and symmetrical current waveform. (Asymmetrical ratio is less than 10%) Please do not use the inverter which has asymmetrical voltage and asymmetrical current and spike wave.

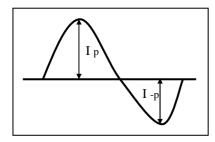
 Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
- 8. The voltage above VS should be applied to the lamps for more than 1 second for start-up. Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.
- 9. The lamp power consumption shown above does not include loss of external inverter. The applied lamp current is a typical one.

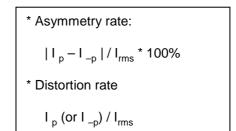
Ver. 1.0 May. 23, 2003 6 / 29



Note)

- Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following.
 It shall help increase the lamp lifetime and reduce leakage current.
 - a. The asymmetry rate of the inverter waveform should be less than 10%.
 - b. The distortion rate of the waveform should be within $\sqrt{2 \pm 10\%}$.
 - * Inverter output waveform had better be more similar to ideal sine wave.





Do not attach a conducting tape to lamp connecting wire.
If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.

Ver. 1.0 May. 23, 2003 7 / 29



3-2. Interface Connections

This LCD employs two interface connections, a 30 pin connector is used for the module electronics interface and the other connector is used for the integral backlight system.

The electronics interface connector is a model FIXB30SR-HF11 manufactured by JAE.

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	GND	Ground	
2	VCC	Power Supply, 3.3V Typ.	
3	VCC	Power Supply, 3.3V Typ.	
4	V EEDID	DDC 3.3V power	1, Interface chips
5	NC	Reserved for supplier test point	1.1 LCD: KZ4E010G12CFP(LCD Controller)
6	CIK EEDID	DDC Clock	including LVDS Receiver
7	DATA EEDID	DDC Data	(THINE, THC63LVD64A) 1.2 System: THC63LVD63A or equivalent
8	R _{IN} 0-	Negative LVDS differential data input	* Pin to Pin compatible with THINE LVDS
9	R _{IN} 0+	Positive LVDS differential data input	
10	GND	Ground	2. Connector 2.1 LCD : FI-XB30SR-HF11, JAE or
11	R _{IN} 1-	Negative LVDS differential data input	its compatibles
12	R _{IN} 1+	Positive LVDS differential data input	2.2 Mating : FI-X30M or equivalent.
13	GND	Ground	2.3 Connector pin arrangement 30 1
14	R _{IN} 2-	Negative LVDS differential data input	l η Π ή
15	R _{IN} 2+	Positive LVDS differential data input	
16	GND	Ground	
17	CLKIN-	Negative LVDS differential clock input	[LCD Module Rear View]
18	CLKIN+	Negative LVDS differential clock input	
19	GND	Ground	
20	NC	No connect	
21	NC	No connect	
22	NC	No connect	
23	NC	No connect	
24	NC	No connect	
25	NC	No connect	
26	NC	No connect	
27	NC	No connect	
28	NC	No connect	
29	NC	No connect	
30	NC	No connect	

The backlight interface connector is a model BHSR-02VS-1, manufactured by JST. The mating connector part number is SM02B-BHSS-1 or equivalent.

Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION (J3)

			. ,
Pin	Symbol	Description	Notes
1	HV	Power supply for lamp (High voltage side)	1
2	LV	Power supply for lamp (Low voltage side)	1

Notes: 1. The high voltage side terminal is colored pink and the low voltage side terminal is white.

Ver. 1.0	May, 23, 2003	8 / 29
V CI. 1.U	IVIAY. 23, 2003	o, _ o



3-3. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 6. TIMING TABLE

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	fclk	67	68.9	72	MHz	
Hsync	Period	tHP	1380	1408	1428		
	Width	twn	16	32	-	tclk	
	Active	twha	1280	1280	1280		
Vsync	Period	tvp	808	816	840		
	Width	tw∨	2	4	-	tHP	
	Active	twva	800	800	800		
Data	Horizontal back porch	tHBP	68	75	-	tour	
Enable	Horizontal front porch	tHFP	16	21	-	tCLK	
	Vertical back porch	tvbp	5	8	-	tup	
	Vertical front porch	tvfp	1	4	-	tHP	

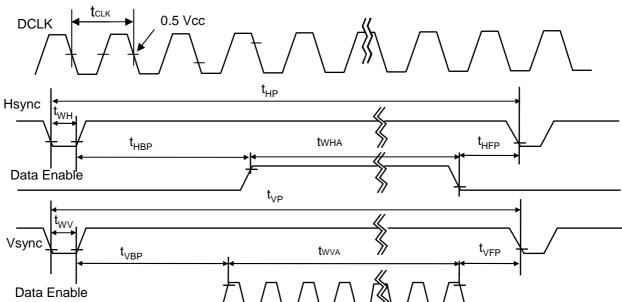
3-4. Signal Timing Waveforms

Data Enable, Hsync, Vsync

High: 0.7VCC

Low: 0.3VCC

Condition: VCC =3.3V



Ver. 1.0 May. 23, 2003 9 / 29



3-5. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

									Inp	out Co	olor D	ata							
	Color			RE	Đ					GRE	EN					BL	UE		
`	50101	MSE	3				LSB		3				LSB	MSE	3				LSB
		R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	В3	B 2	B 1	B 0
	Black	0	0			0	0	0	0			0	0	0	0			0	0
	Red	1	1	.1	. 1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1		
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED																			
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN																			
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																			
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	 1	 1	 1	1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	 1		 1	1	1
	- (/							<u> </u>											

Ver. 1.0 May. 23, 2003



3-6. Power Sequence

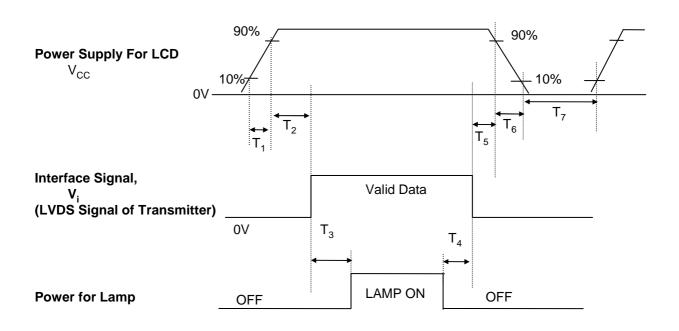


Table 8. POWER SEQUENCE TABLE

Parameter		Value		Units
	Min.	Тур.	Max.	
T ₁	-	-	10	(ms)
T_2	0	-	50	(ms)
T_3	200	-	-	(ms)
T ₄	200	-	-	(ms)
T_{5}	0	-	50	(ms)
T ₆	0	-	10	(ms)
T ₇	400	-	-	(ms)

Note)

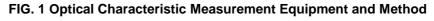
- 1. Please avoid floating state of interface signal at invalid period.
- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 3. Lamp power must be turn on after power supply for LCD and interface signal are valid.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.



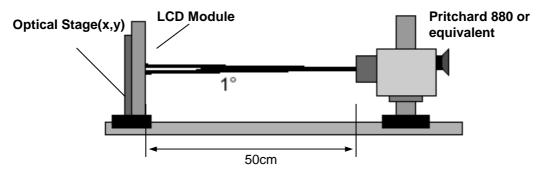


Table 9. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, f_{V} =60Hz, f_{CLK} = 68.9MHz, lout = 6.0mA

Parameter	Cumbal		Values		Units	Notes
Parameter	Symbol	Min	Тур	MAx	Units	Notes
Contrast Ratio	CR	250	300	-		1
Surface Luminance, white	L_WH	150	185	-	cd/m ²	2
Luminance Variation	$\delta_{ \text{WHITE}}$	-	-	1.6		2
Response Time						3
Rise Time+Decay Time	$Tr_{R+}Tr_{D}$	-	25	35	ms	
Color Coordinates]					±0.03
RED	RX	0.555	0.585	0.615		
	RY	0.295	0.325	0.355		
GREEN	GX	0.290	0.320	0.350]	
	GY	0.515	0.545	0.575]	
BLUE	ВХ	0.120	0.150	0.180		
	BY	0.105	0.135	0.165		
WHITE	WX	0.283	0.313	0.343		
	WY	0.299	0.329	0.359		
Viewing Angle						5
x axis, right(Φ=0°)	Θr	60	-	-	degree	
x axis, left (Φ=180°)	Θl	60	-	-	degree	
y axis, up (Φ=90°)	Θu	40	-	-	degree	
y axis, down (Φ=270°)	Θd	50	-	-	degree	
Gray Scale						6

Ver. 1.0 May. 23, 2003



Note)

1. Contrast Ratio(CR) is defined mathematically as Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

- 2. Surface luminance is the 5point (1~5) average across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 2. When I_{BL} = 6.0mA, L_{WH} =185cd/m²(typ.)
- 3. Luminance % uniformity is measured for 13 point For more information see FIG 2. δ WHITE = Maximum(LN1,LN2, LN13) ÷ Minimum(LN1,LN2, LN13)
- 4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

* f_{\/}=60Hz

Gray Level	Luminance [%] (Typ)
L0	0.18
L7	0.60
L15	5.01
L23	12.97
L31	26.20
L39	40.15
L47	56.17
L55	80.22
L63	100



FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

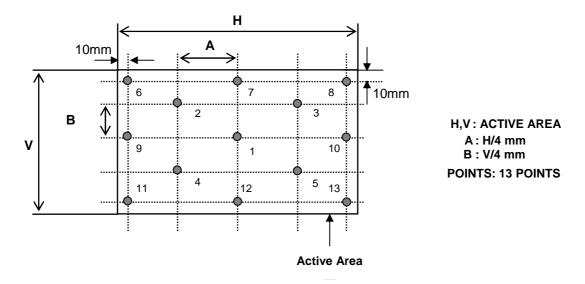


FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

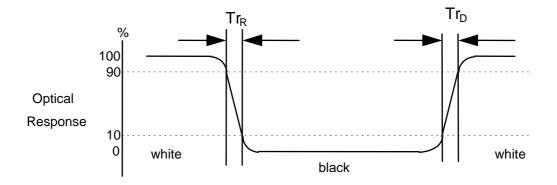
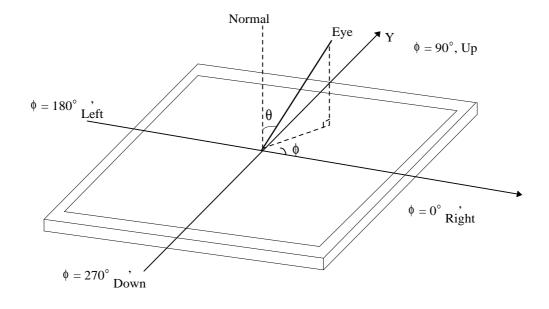




FIG. 4 Viewing angle

<Dimension of viewing angle range>





5. Mechanical Characteristics

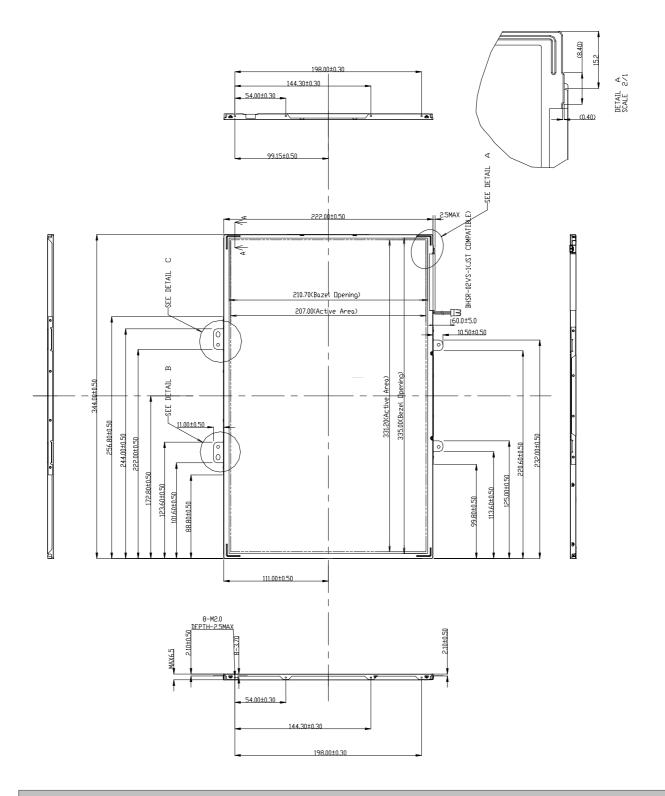
The contents provide general mechanical characteristics for the model LP154W01(A1). In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	344.0 ± 0.5mm			
Outline Dimension	Vertical	222.0 ± 0.5mm			
	Depth	6.2 ^{mm} (Typ), 6.5 ^{mm} (Max)			
Bezel Area	Horizontal	335.0 ± 0.5mm			
bezei Alea	Vertical	210.7 ± 0.5mm			
Active Display Area	Horizontal	331.2 mm			
Active Display Area	Vertical	207.0 mm			
Weight	605g (MAX)				
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer				



<FRONT VIEW>

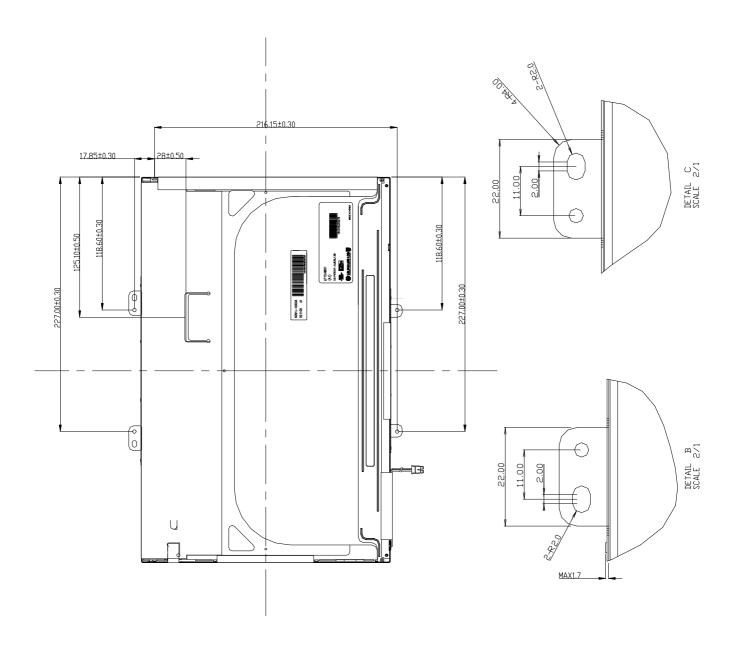
Note) Unit:[mm], General tolerance: \pm 0.5mm





<REAR VIEW>

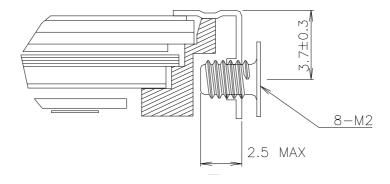
Note) Unit:[mm], General tolerance: ± 0.5mm





[DETAIL DESCRIPTION OF SIDE MOUNTING SCREW]

*Screw Torque (8 point): Max. 2Kgf.Cm



Ver. 1.0 May. 23, 2003



6. Reliability

Environment test condition

No.	Test Item	Conditions					
1	High temperature storage test	Ta= 60°C, 240h					
2	Low temperature storage test	Ta= -20°C, 240h					
3	High temperature operation test	Ta= 50°C, 50%RH, 240h					
4	Low temperature operation test	Ta= 0°C, 240h					
5	Vibration test (non-operating)	Sine wave, 5 ~ 150Hz, 1.5G, 0.37oct/min 3 axis, 30min/axis					
6	Shock test (non-operating)	- No functional or cosmetic defects following a shock to all 6 sides delivering at least 200 G in a half sine pulse no longer than 2 ms to the display module - No functional defects following a shock delivering at least 260 g in a half sine pulse no longer than 2 ms to each of 6 sides. Each of the 6 sides will be shock tested with one each display, for a total of 6 displays					
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr					

[{] Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.



7. International Standards

7-1. Safety

a) UL 60950, Third Edition, Underwriters Laboratories, Inc., Dated Dec. 11, 2000.

Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.

b) CAN/CSA C22.2, No. 60950, Third Edition, Canadian Standards Association, Dec. 1, 2000.

Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.

c) EN 60950 : 2000, Third Edition

IEC 60950: 1999, Third Edition

European Committee for Electrotechnical Standardization(CENELEC)

EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A B C D E F G	H I J K L M
---------------	-------------

A,B,C: SIZE(INCH)

D: YEAR E: MONTH

F : PANEL CODE G : FACTORY CODE H : ASSEMBLY CODE I,J,K,L,M : SERIAL NO.

Note

1. YEAR

Year	97	98	99	2000	2001	2002	2003	2004	2005	2006	2007
Mark	7	8	9	0	1	2	3	4	5	6	7

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

3. PANEL CODE

Panel Code	P1 Factory	P2 Factory	P3 Factory	P4 Factory	P5 Factory	Hydis Panel
Mark	1	2	3	4	5	Н

4. FACTORY CODE

Factory Code	LPL Gumi	LPL Nanjing
Mark	K	С

5. SERIAL NO.

Year	1 ~ 99999	100000 ~
Mark	00001 ~ 99999	A0001 ~ A9999, , Z9999

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 10 pcs

b) Box Size: 437mm X 369mm X 339mm



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm\ 200mV(Over\ and\ under\ shoot\ voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

Ver. 1.0 May. 23, 2003 23 / 29



9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

APPEN	DIA F	A. Ennanced Extended Display Identification	Data	(EEDID	") 1/3
Byte#	Byte#	Field Name and Comments	Value	Value	
(decimal)	(HEX)	Fleid Name and Comments	(HEX)	(binary)	
0	00	Header	0 0	0000 0000	
1	01		FF	1111 1111	
2	02		FF	1111 1111	
3	03		FF	1111 1111	Header
4	04		FF	1111 1111	
5	05		FF	1111 1111	
6 7	06 07		F F 0 0	1111 1111 0000 0000	
8	08	EISA manufacturer code = LGP	3 0	0011 0000	
9	09	EloA manuacturer code Edi	F 0	1111 0000	
10	0A	Product code =	0 0	0000 0000	
11	0B	(Hex, LSB first)	0 0	0000 0000	
12	OC	32-bit serial number	0 0	0000 0000	Vender/
13	0D	oz-bit serial number	0 0	0000 0000	Product ID
					Product ID
14	0E		0 0	0000 0000	
15	0F		0 0	0000 0000	
16	10	Week of manufacture	0 0	0000 0000	
17	11	Year of manufacture = 2003	0 D	0000 1101	
18	12	EDID Structure version # = 1	0 1	0000 0001	EDID Version/
19	13	EDID Revision # = 3	0 3	0000 0011	Revision
<u>20</u> 21	14 15	Video input definition = Digital I/p,non TMDS CRGB Max H image size(cm) = 33.12cm(33)	8 0 2 1	1000 0000 0010 0001	Diaplay
22	16	Max V image size(cm) = 33.12cm(33)	1 5	0001 0101	Display Parameter
23	17	Display gamma = 2.20	7 8	0111 1000	i aiainetei
24	18	Feature support(DPMS) = Active off, RGB Color	0 A	0000 1010	
25	19	Red/Green low Bits	CE	1100 1110	
26	1A	Blue/White Low Bits	6 0	0110 0000	
27	1B	Red X Rx = 0.585	9 5	1001 0101	
28	1C	Red Y Ry = 0.325	5 3	0101 0011	
29	1D	Green X Gx = 0.320	5 1	0101 0001	Color
30	1E	Green Y Gy = 0.545	8 B	1000 1011	Characteristic
31	1F	Blue X Bx = 0.150	2 6	0010 0110	
32	20	Blue Y By = 0.135	2 2	0010 0010	
33 34	21 22	White X	5 0 5 4	0101 0000 0101 0100	
35	23	Established Timing I	0 0	0000 0000	Established
36	24	Established Timing II	0 0	0000 0000	Timings
37	25	Manufacturer's Timings	0 0	0000 0000	Tillings
38	26	Standard Timing Identification 1 was not used	0 1	0000 0001	
39	27	Standard Timing Identification 1 was not used	0 1	0000 0001	
40	28	Standard Timing Identification 1 was not used	0 1	0000 0001	
41	29	Standard Timing Identification 2 was not used	0 1	0000 0001	
42	2A	Standard Timing Identification 3 was not used	0 1		
43	2B	Standard Timing Identification 3 was not used		0000 0001	Ctom -
44	2C	Standard Timing Identification 4 was not used	0 1	0000 0001	Standard
45	2D	Standard Timing Identification 4 was not used	0 1	0000 0001	Timing ID
46	2E	Standard Timing Identification 5 was not used	0 1	0000 0001	
47	2F	Standard Timing Identification 5 was not used	0 1	0000 0001	
48	30	Standard Timing Identification 6 was not used	0 1	0000 0001	
49	31	Standard Timing Identification 6 was not used	0 1	0000 0001	
50	32	Standard Timing Identification 7 was not used	0 1	0000 0001	
51	33	Standard Timing Identification 7 was not used	0 1	0000 0001	
52	34	Standard Timing Identification 8 was not used	0 1	0000 0001	
53	35	Standard Timing Identification 8 was not used	0 1	0000 0001	



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

AFFEIN		A. Ennanced Extended Display Identification	<u>vala</u>	(EEDID	") <i>21</i> 3
Byte#	Byte#	Field Name and Comments	Value	Value	
(decimal)	(HEX)	Had rate and Willens	(HEX)	(binary)	
54	36	Detailed Timing Descriptor #1	ΕA	1110 1010	
55	37	1280 X 800 @60Hz mode: pixel clock = 68.9MHz	1 A	0001 1010	
56	38	Horizontal Active = 1280 pixels	0 0	∞ ∞	
57	39	Horizontal Blanking = 128 pixels	8 0	1000 0000	
58	3A	Horizontal Active: Horizontal Blanking=1280: 128	5 0	0101 0000	
59	3B	Vertical Avtive = 800 lines	2 0	0010 0000	
60	3C	Vertical Blanking = 16 lines	1 0	0001 0000	Detailed
61	3D	Vertical Active: Vertical Blanking=800:16	3 0	0011 0000	Timing
62		Horizontal Sync. Offset = 21 pixels	1 5	0001 0101	Description
63		Horizontal Sync Pulse Width = 32 pixels	2 0	0010 0000	<i>#</i> 1
64	40	Vertical Sync Offset = 4 lines, Sync Width = 4 lines	4 4	0100 0100	
65		Horizontal Vertical Sync Offset/Width upper 2bits = 0	0 0	∞ ∞	
66	42	Horizontal Image Size = 331mm	4 B	0100 1011	
67	43	Vertical Image Size = 207mm	C F	1100 1111	
- 68	44	Horizontal & Vertical Image Size	1 0	0001 0000	
69	45	Horizontal Border=0	0 0	∞ ∞	
70	46	Vertical Border = 0	0 0	0000 0000	
71	47	Non-interlaced, Normal display, no stereo, Digital separate sync, H/V pol negatives	1 9	0001 1001	
72	48	Flag	0 0	∞ ∞	
73	49	Flag	0 0	∞ ∞	
74	4A	Flag	0 0	∞ ∞	
75	4B	Data Type Tag: Descriptor Defined by Manufacturer	0 F	0000 1111	
76	4C	Flag	0 0	∞ ∞	
	4D_	Value = HSPW _{min} /2 (pixel clks)	0 0	∞ ∞	
78	4E	Value = HSPW _{max} /2 (pixel clks)	0 0	∞ ∞	Detailed
79	4F	Value=Thbp _{min} /2 (pixel clks)	0 0	∞ ∞	Timing
80	50	Value = Thbp _{max} /2 (pixel clks)	0 0	<u> </u>	Description
81	51	Value = VSPW _{min} /2 (line pulses)	0 0	∞ ∞	#2
82	52	Value = VSPW _{max} /2 (line pulses)	0 0	<u> </u>	
83	53	Value = Tvbp _{min} /2 (line pulses)	0 0	<u> </u>	
84	55	Value = Tvbp _{max} /2 (line pulses)	0 0	<u> </u>	
85	55	Thp _{min} = valueX2+ Ha _{oixel clks} (pixel clks) = 50	3 2	0011 0010	
86	56	$Thp_{max} = valueX2 + Ha_{oixel, clks} (pixel clks) = 74$	4 A	0100 1010	
87	57	Tvp _{min} = valueX2+VAl _{ines} (line pulses) = 4	0 4	0000 0100	
88	58	Tvp _{max} = valueX2+VAl _{ines} (line pulses) = 20	1 4	0001 0100	
89	59	Module "A" Revision (Example: 00, 01, 02, 03, etc.)	0 0	0000 0000	
90	5A	Detailed Timing Descriptor #3	0 0	0000 0000	
91	58		0 0	0000 0000	
92	5C	Deta Tipo Topic I halofood	0 0	0000 0000	
93	5D 	Data Type Tag: Undefined	1 0	0001 0000	
94	5E		0 0	0000 0000	
95	5 -		0 0		Date 91
96	60		0 0	0000 0000	Detailed Timin a
97	<u>61</u>		0 0	0000 0000	Timing
98	<u>62</u>		0 0	0000 0000	Description
99	63		0 0	0000 0000	#3
100	64		0 0	0000 0000	
101	<u>65</u>		0 0	0000 0000	
102	66		0 0	0000 0000	
103	67		0 0	0000 0000	
104	<u>68</u>		0 0	0000 0000	
105	69		0 0	0000 0000	
106	6A		0 0	0000 0000	
107	æ		0 0	∞	



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

Byte#	Byte#		\/a	lue	Value	,
<u> </u>		Field Name and Comments	_			
(decimal)	(HEX)		(HE	-X)	(binary)	
108	6C	Detailed Timing Descriptor #4	0	0	0000 0000	
109	6D		0	0	0000 0000	
110	6E		0	0	0000 0000	
111	6F	Data Type Tag: Undefined	1	0	0001 0000	
112	70		0	0	0000 0000	
113	71		0	0	0000 0000	
114	72		0	0	0000 0000	Detailed
115	73		0	0	0000 0000	Timing
116	74		0	0	0000 0000	Description
117	75		0	0	0000 0000	#4
118	76		0	0	0000 0000	
119	77		0	0	0000 0000	
120	78		0	0	0000 0000	
121	79		0	0	0000 0000	
122	7A		0	0	0000 0000	
123	7B		0	0	0000 0000	
124	7C		0	0	0000 0000	
125	7D		0	0	0000 0000	
126	7E	Extension flag = 00	0	0	0000 0000	Extension Flag
127	7F	Checksum	F	С	1111 1100	Checksum



APPENDIX B. Inspection Criteria 1/2

1. Dot

1.1. Bright Dot

Dots(sub-pixels) which appeared brightly in the screen when the LCM displayed with dark pattern.

- R or B 1 dot TBD Max
- G 1 dot TBD Max
- Adjacent 2 dots(R, B) TBD Max
- Adjacent 2 dots(G, vertical) TBD Max
- Total amount of Bright dots TBD Max
- Minimum Distance between bright dots TBD mm
- Total bright dot in screen center TBD Max
Size of Window: 160mm x 120mm

1.2. Dark Dot

Dots(sub-pixels) which appeared darkly in the screen when the LCM displayed with bright pattern.

-	1 dot	TBD Max
-	Adjacent 2 dots	TBD Max
-	Total amount of Dark dot	TBD Max
-	Minimum Distance between dark dots	TBD mm

- 1.3. Total amount of Dot Defects ----- TBD Max(Combination)
- Note) a. Every dot herein means Sub-Pixel(Each Red, Green, or Blue Color)
 - b. Bright & Dark dots are larger than half sub-pixel.(Dots smaller than half sub-pixel are not counted as a defect dots.)



APPENDIX B. Inspection Criteria 2/2

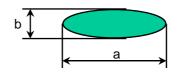
2. Polarizer

Item	s	Accept Criteria
Scratches	Linear	$W \le 0.1, L \le 5.0, N \le 3$
Dent	Circular	$D \le 0.5, N \le 4$

Note)

a. Áverage Diameter

$$D = \frac{a+b}{2}$$



W:Width

L : Length

D : Average diameter

- b. Linear: a > 2b, Circular: $a \le 2b$
- c. Extraneous substances which can be wiped out, like Finger Print, Particles, are not considered as a defect.
- d. Defects which is on the Black Matrix(outside of Active Area) are not considered as a defect.

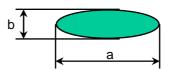
3. Foreign Material

Item	s	Accept Criteria
Familian Matarial	Linear	$W \le 0.07, L \le 1.0, N \le 4$
Foreign Material	Circular	D ≤ 0.5, N ≤ 4

Note)

a. Áverage Diameter

$$D = \frac{a+b}{2}$$



W:Width

L : Length

D : Average diameter

b. Linear: a > 2b, Circular: $a \le 2b$

4. Line(s)

All kinds of line defects such as vertical, horizontal or cross are not allowed.

5. Bezel Appearance

Scratches, minor bents, stains, particles on the Bezel frame are not considered as a defect.

6. Others

Issues which is not defined in this criteria shall be discussed with both parties, Customer and Supplier, for better solution.