



### N-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> max	<b>Ι</b> <sub>D</sub> T <sub>A</sub> = 25°C
001/	$0.4\Omega @ V_{GS} = 4.5V$	1A
20V	$0.7\Omega @ V_{GS} = 1.8V$	0.8A

## **Description and Applications**

This new generation MOSFET has been designed to minimize the onstate resistance ( $R_{DS(on)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Power management functions

## **Features and Benefits**

- Low On-Resistance
- Very Low Gate Threshold Voltage V<sub>GS(TH)</sub>, 1.0V max
- Low Input Capacitance
- Fast Switching Speed
- Ultra-Small Surfaced Mount Package
- Ultra-low package profile, 0.4mm maximum package height
- ESD Protected Gate
- Lead, Halogen, and Antimony Free, RoHS Compliant (Note 1)
- Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

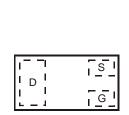
## **Mechanical Data**

- Case: X2-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.001 grams (approximate)

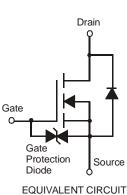


X2-DFN1006-3

Bottom View



Top View Internal Schematic



### Ordering Information (Note 3)

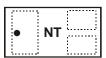
Part Number	Case	Packaging
DMN2500UFB4-7	X2-DFN1006-3	3,000/Tape & Reel
DMN2500UFB4-7B	X2-DFN1006-3	10,000/Tape & Reel

Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. No purposely added lead. Halogen and Antimony free 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.

3. For packaging details, go to our website at http://www.diodes.com.

## **Marking Information**





Top View Dot Denotes Drain Side

### DMN2500UFB4-7B



Top View Bar Denotes Gate and Source Side NT = Product Type Marking Code



## Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V <sub>DSS</sub>	20	V		
Gate-Source Voltage		V <sub>GSS</sub>	±6	V	
	Steady State	$T_{A} = 25^{\circ}C$ $T_{A} = 70^{\circ}C$	I <sub>D</sub>	810 640	mA
Continuous Drain Current (Note 4) $V_{GS}$ = 4.5V	t<10s	T <sub>A</sub> = 25°C T <sub>A</sub> = 70°C	I <sub>D</sub>	950 750	mA
	Steady State	$T_A = 25^{\circ}C$ $T_A = 70^{\circ}C$	ID	1000 800	mA
Continuous Drain Current (Note 5) $V_{GS}$ = 4.5V	t<10s	$T_{A} = 25^{\circ}C$ $T_{A} = 70^{\circ}C$	ID	1200 1000	mA
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I <sub>DM</sub>	4	A		
Maximum Body Diode continuous Current			ls	850	mA

# Thermal Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units	
Total Bower Dissinction (Note 4)	$T_A = 25^{\circ}C$	Р	0.46	W
Total Power Dissipation (Note 4)	T <sub>A</sub> = 70°C	PD	0.29	
Thermal Resistance, Junction to Ambient (Note 4)	Steady state	Р	279	°C/W
Thermal Resistance, Junction to Ambient (Note 4)	t<10s	$R_{ extsf{ heta}JA}$	210	°C/W
Total Power Dissipation (Note 5)	$T_A = 25^{\circ}C$	<b>D</b> -	0.95	W
Total Fower Dissipation (Note 5)	$T_A = 70^{\circ}C$	PD	0.6	
Thermal Desistance Junction to Ambient (Note 5)	Steady state	D	134	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{ ext{ heta}JA}$	100	°C/W
Operating and Storage Temperature Range	T <sub>J.</sub> T <sub>STG</sub>	-55 to +150	°C	

# Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T <sub>J</sub> = 25°C	I <sub>DSS</sub>	-	-	100	nA	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS	-	-	±1.0	μΑ	$V_{GS} = \pm 4.5 V$ , $V_{DS} = 0 V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.5	-	1.0	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	
			0.3	0.4	Ω	$V_{GS} = 4.5V, I_D = 600mA$	
Static Drain-Source On-Resistance	R <sub>DS (ON)</sub>	-	0.4	0.5		$V_{GS} = 2.5V, I_D = 500mA$	
			0.5	0.7		$V_{GS} = 1.8V, I_D = 350mA$	
Forward Transfer Admittance	Y <sub>fs</sub>	-	1.4	-	S	$V_{DS} = 10V, I_D = 400mA$	
Diode Forward Voltage	V <sub>SD</sub>		0.7	1.2	V	$V_{GS} = 0V, I_{S} = 150mA$	
DYNAMIC CHARACTERISTICS (Note 7)	-						
Input Capacitance	Ciss	-	60.67	-	pF		
Output Capacitance	Coss	-	9.68	-	pF	−V <sub>DS</sub> =16V, V <sub>GS</sub> = 0V, −f = 1.0MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	-	5.37	-	pF	1 = 1.000112	
Gate resistance	Rg	-	93	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge	Qg	-	736.6	-	рС		
Gate-Source Charge	Q <sub>gs</sub>	-	93.6	-	рС	$V_{GS} = 4.5V, V_{DS} = 10V,$	
Gate-Drain Charge	Q <sub>gd</sub>	-	116.6	-	рС	$I_D = 250 \text{mA}$	
Turn-On Delay Time	t <sub>D(on)</sub>	-	5.1	-	ns		
Turn-On Rise Time	tr	-	7.4	-	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$	
Turn-Off Delay Time	t <sub>D(off)</sub>	-	26.7	-	ns	R <sub>L</sub> = 47Ω, R <sub>G</sub> = 10Ω, I <sub>D</sub> = 200mA	
Turn-Off Fall Time	t <sub>f</sub>	-	12.3	-	ns		

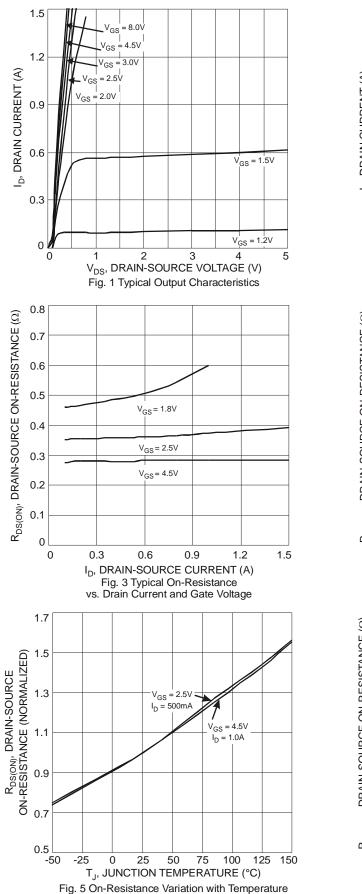
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:

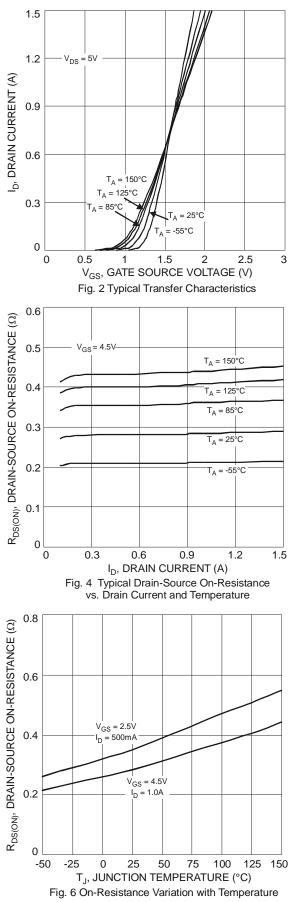
6. Short duration pulse test used to minimize self-heating effect.

7. Guaranteed by design. Not subject to product testing.

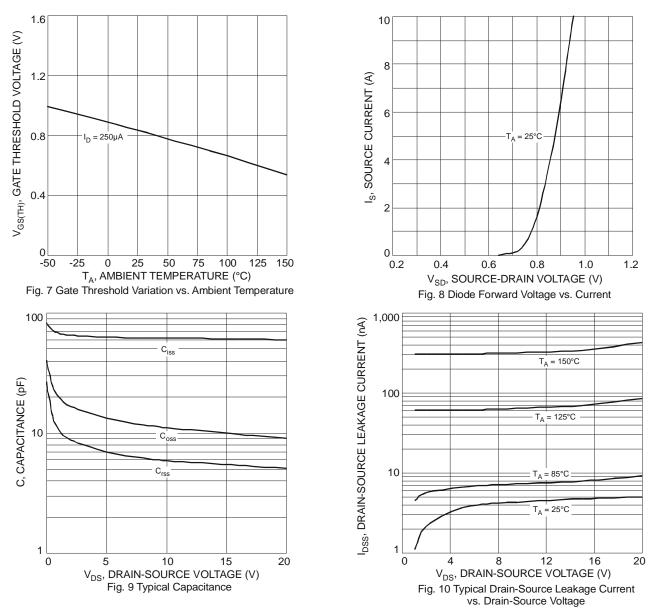


## **DMN2500UFB4**

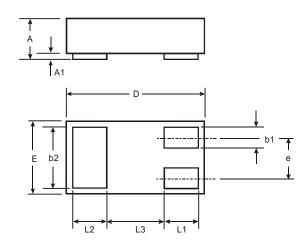








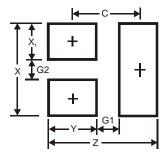
## **Package Outline Dimensions**



X2-DFN1006-3					
Dim	Min	Max	Тур		
Α		0.40			
A1	0	0.05	0.02		
b1	0.10	0.20	0.15		
b2	0.45	0.55	0.50		
D	0.95	1.05	1.00		
Е	0.55	0.65	0.60		
е	_	_	0.35		
L1	0.20	0.30	0.25		
L2	0.20	0.30	0.25		
L3		_	0.40		
All Dimensions in mm					



## Suggested Pad Layout



Dimensions	Value (in mm)
Z	1.1
G1	0.3
G2	0.2
Х	0.7
X1	0.25
Y	0.4
С	0.7

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