TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ86AFS

2-Input EXCLUSIVE OR Gate

Features

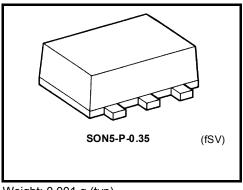
• High output current : ±24mA (min) at V_{CC} = 3.0V

• Super high speed operation : t_{pd} = 2.6ns (typ.)

at $V_{CC} = 5V$, 50pF

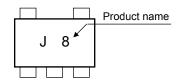
• Operating voltage range : V_{CC} = 1.6 to 5.5V

• 5.5-V tolerant inputs.



Weight: 0.001 g (typ)

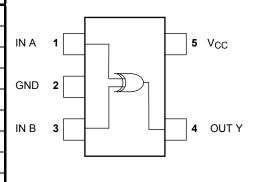
Marking



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	−0.5 to 6	V
DC input voltage	V_{IN}	-0.5 to 6	>
DC output voltage	V _{OUT}	–0.5 to V _{CC} +0.5	٧
Input diode current	I _{IK}	-20	mA
Output diode current	lok	±20 (Note1)	mA
DC output current	lout	±50	mA
DC VCC/ground current	Icc	±50	mA
Power dissipation	PD	50	mW
Storage temperature	T _{stg}	-65 to 150	°C
·		•	

Pin Assignment (top view)



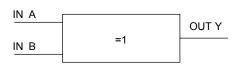
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: $V_{OUT} < GND, V_{OUT} > V_{CC}$

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IEC Logic Symbol



Truth Table

Α	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	1.65 to 5.5	V
Supply voltage	VCC	1.5 to 5.5 (Note 2)	V
Input voltage	V _{IN}	0 to 5.5	٧
Output voltage	V _{OUT}	0 to V _{CC}	٧
Operating temperature	T _{opr}	-40 to 85	°C
Input rise time fall time		0 to 20 ($V_{CC} = 1.80 \text{ V} \pm 0.15\text{V}$, 2.5 V $\pm 0.2 \text{ V}$)	
	dt/dv	0 to 10 (V _{CC} = 3.3 V \pm 0.3 V)	ns/V
		0 to 5 ($V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$)	

Note 2: Data retention only

Electrical Characteristics

DC Characteristics

Characteristics Symbol		Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
				V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
High-level input		_		1.65 to 1.95	V _{CC} × 0.75			V _{CC} × 0.75	_	
voltage	2.3 to 5.5			V _{CC} × 0.7		_	V _{CC} × 0.7	_	V	
Low-level input V _{IL}			1.65 to 1.95	_		V _{CC} ×0.25	_	V _{CC} ×0.25	V	
	V _{IL}	_		2.3 to 5.5	_	_	V _{CC} × 0.3	_	V _{CC} × 0.3	
				1.65	1.55	1.65	_	1.55	_	-
			I _{OH} = -100 μA	2.3	2.2	2.3	_	2.2	_	
			ΙΟΗ = -100 μΑ	3.0	2.9	3.0		2.9	_	
				4.5	4.4	4.5		4.4	_	
High-level Output voltage	V _{OH}	V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -4 \text{ mA}$	1.65	1.29	1.52		1.29	_	V
			$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15		1.9	_	
			I _{OH} = -16 mA	3.0	2.4	2.8	_	2.4	_	
			I _{OH} = -24 mA	3.0	2.3	2.68	_	2.3	_	
			$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8	_	
			I _{OL} = 100 μA	1.65	_	0	0.1	_	0.1	
				2.3	_	0	0.1	_	0.1	
Low-level VOL				3.0	_	0	0.1	_	0.1	
				4.5	_	0	0.1	_	0.1	
	V _{OL}	$V_{IN} = V_{IH}$ or V_{IL}	I _{OL} = 4 mA	1.65	_	0.08	0.24	_	0.24	
			I _{OL} = 8 mA	2.3	_	0.1	0.3	_	0.3	
			I _{OL} = 16 mA	3.0	_	0.15	0.4	_	0.4	
			I _{OL} = 24 mA	3.0	_	0.22	0.55	_	0.55	
			$I_{OL} = 32 \text{ mA}$	4.5	_	0.22	0.55	_	0.55	
Input leakage current	I _{IN}	$V_{IN} = 5.5 \text{ V or GND}$		0 to 5.5	_	_	±1	_	±10	μА
Quiescent supply current	Icc	V _{IN} = 5.5V or GND		5.5	_	_	2	_	20	μА

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AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics Symbol		Test Condition		Ta = 25°C		Ta = -40 to 85°C		Unit	
		rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	^t pLH ^t pHL	C_L = 15 pF, R_L = 1 $M\Omega$	1.8 ± 0.15	1.0	6.4	11.5	1.0	12.0	- ns
			2.5 ± 0.2	0.8	3.8	8.0	0.8	8.5	
			3.3 ± 0.3	0.5	3.0	5.7	0.5	6.0	
			5.0 ± 0.5	0.5	2.4	5.0	0.5	5.4	
		$C_L = 50 \text{ pF}, R_L = 500 \Omega$	3.3 ± 0.3	1.2	3.5	6.2	1.2	6.5	
			5.0 ± 0.5	0.8	2.6	5.4	0.8	5.8	
Input capacitance	C _{IN}		0 to 5.5	_	4		_	_	pF
Power dissipation capacitance C _{Pl}	Cpp	(Note 3)	3.3	_	21	_	_	_	- pF
	CPD	(140te 3)	5.5	_	24		_	_	

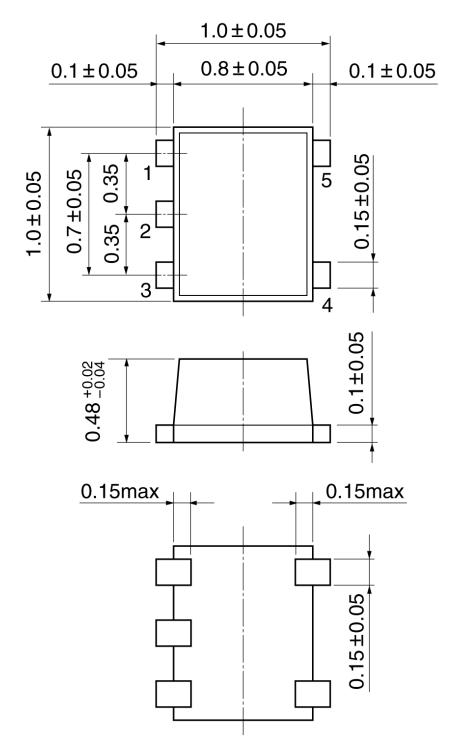
Note3: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

$$I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

Package Dimensions

SON5-P-0.35 Unit: mm



Weight: 0.001 g (typ.)

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