

MOSFETs Silicon N-Channel MOS (U-MOSVII)

TPCA8075

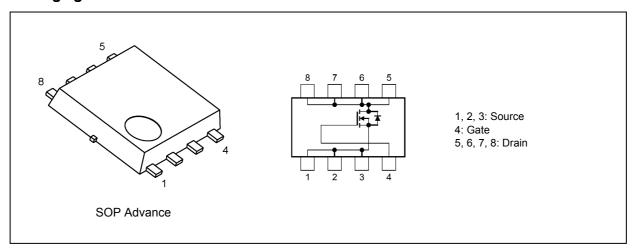
1. Applications

- · Notebook PCs
- · Mobile Equipments

2. Features

- (1) Small footprint due to a small and thin package
- (2) Low drain-source on-resistance: $R_{DS(ON)} = 1.9 \text{ m}\Omega$ (typ.) ($V_{GS} = 10 \text{ V}$)
- (3) Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 33 \text{ V)}$
- (4) Enhancement mode: V_{th} = 1.3 to 2.3 V (V_{DS} = 10 V, I_D = 1.0 mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) (T_a = 25°C unless otherwise specified)

Characteris	Symbol	Rating	Unit		
Drain-source voltage			V _{DSS}	33	V
Gate-source voltage			V _{GSS}	±20	1
Drain current (DC)		(Note 1)	I _D	48	Α
Drain current (pulsed)		(Note 1)	I _{DP}	144	1
Power dissipation	(T _c = 25 °C)		P _D	63	W
Power dissipation	(t = 10 s)	(Note 2)	P _D	2.8	W
Power dissipation	(t = 10 s)	(Note 3)	P _D	1.6	W
Single-pulse avalanche energy		(Note 4)	E _{AS}	261	mJ
Avalanche current			I _{AR}	48	Α
Channel temperature			T _{ch}	150	℃
Storage temperature			T _{stg}	-55 to 150	1

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production



5. Thermal Characteristics

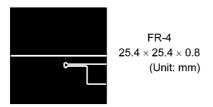
Characte	Symbol	Max	Unit		
Channel-to-case thermal resistance	(T _c = 25 °C)		R _{th(ch-c)}	1.98	°C/W
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 2)	R _{th(ch-a)}	44.6	°C/W
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 3)	R _{th(ch-a)}	78.1	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: Device mounted on a glass-epoxy board (a), Figure 5.1

Note 3: Device mounted on a glass-epoxy board (b), Figure 5.2

Note 4: V_{DD} = 24 V, T_{ch} = 25°C (initial), L = 0.1 mH, R_G = 1 Ω , I_{AR} = 48 A



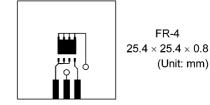


Fig. 5.1 Device Mounted on a Glass-Epoxy Board (a)

Fig. 5.2 Device Mounted on a Glass-Epoxy Board (b)

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.



6. Electrical Characteristics (T_a = 25°C unless otherwise specified)

6.1. Static Characteristics

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±0.1	μА
Drain cut-off current	I _{DSS}	V _{DS} = 33 V, V _{GS} = 0 V	_	_	10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	33			V
	V _{(BR)DSX}	I _D = 10 mA, V _{GS} = -20 V	18			
Gate threshold voltage	V_{th}	V _{DS} = 10 V, I _D = 1.0 mA	1.3	_	2.3	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 4.5 V, I _D = 24 A	_	2.4	3.1	mΩ
		V _{GS} = 10 V, I _D = 24 A	_	1.9	2.4	

6.2. Dynamic Characteristics

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	5200	_	pF
Reverse transfer capacitance	C _{rss}		_	300	_	
Output capacitance	C _{oss}		_	970	_	
Switching time (rise time)	t _r	See Figure 6.2.1.	_	5	_	ns
Switching time (turn-on time)	t _{on}		_	17	_	
Switching time (fall time)	t _f]	_	7	_	
Switching time (turn-off time)	t _{off}		_	49		

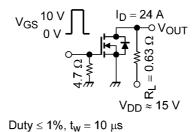


Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 48 \text{ A}$	_	70		nC
Gate-source charge 1	Q _{gs1}		_	14		
Gate-drain charge	Q_{gd}		_	12		

6.4. Source-Drain Characteristics

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (pulsed)	(Note 5)	I _{DRP}	_	_	_	144	Α
Diode forward voltage		V_{DSF}	I _{DR} = 48 A, V _{GS} = 0 V	_	_	-1.2	V

Note 5: Ensure that the channel temperature does not exceed 150°C.



7. Marking

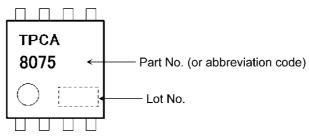


Fig. 7.1 Marking

Rev.2.0

8. Characteristics Curves (Note)

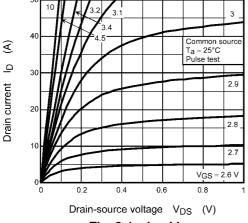
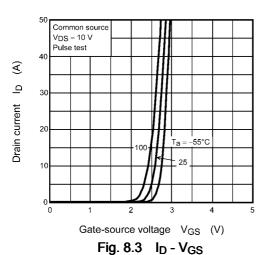


Fig. 8.1 I_D - V_{DS}



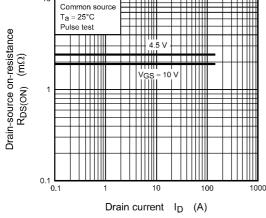


Fig. 8.5 R_{DS(ON)} - I_D

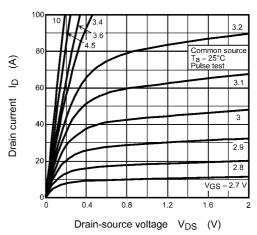


Fig. 8.2 I_D - V_{DS}

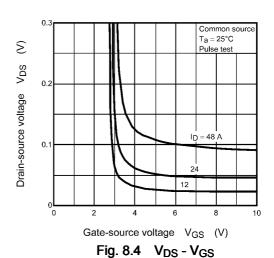


Fig. 8.6 R_{DS(ON)} - T_a

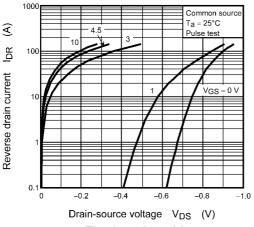


Fig. 8.7 IDR - VDS

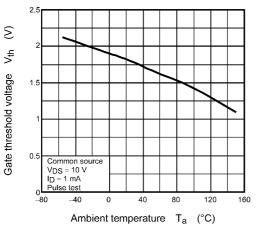


Fig. 8.9 V_{th} - T_a

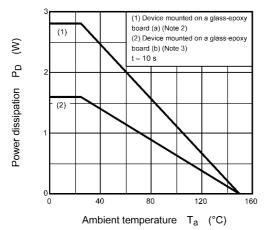


Fig. 8.11 P_D - T_a (Guaranteed Maximum)

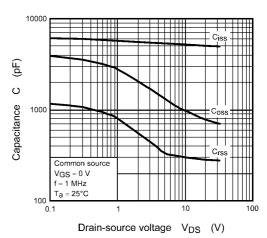


Fig. 8.8 Capacitance - V_{DS}

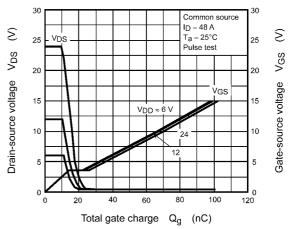


Fig. 8.10 Dynamic Input/Output Characteristics

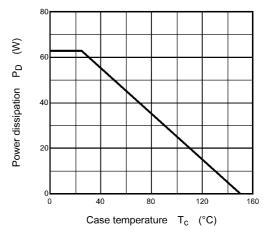


Fig. 8.12 P_D - T_c (Guaranteed Maximum)

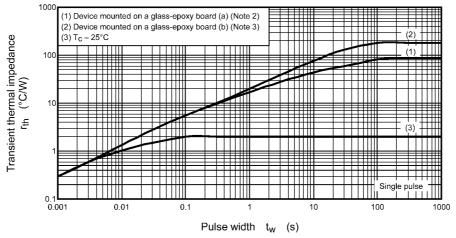


Fig. 8.13 r_{th} - t_w (Guaranteed Maximum)

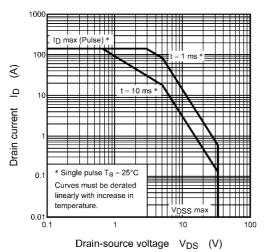


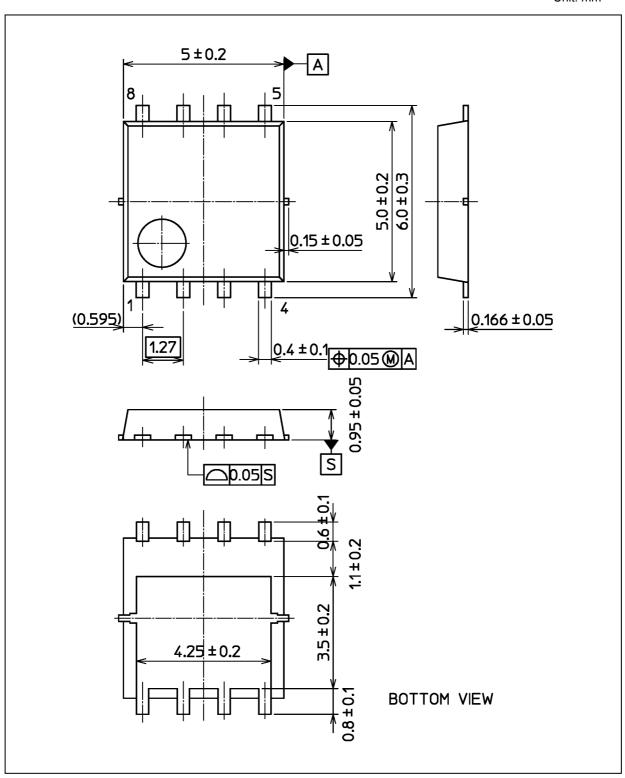
Fig. 8.14 Safe Operating Area (Guaranteed Maximum)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 0.069 g (typ.)

Package Name(s)	
TOSHIBA: 2-5Q1S	
Nickname: SOP Advance	



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