Advance Information

N-Channel Power MOSFET 60 V, 96 A, 8 m Ω

Features

- Low R_{DS(on)}
- High Current Capability
- 100% Avalanche Tested
- These Devices are Pb-Free, Halogen Free and are RoHS Compliant

MAXIMUM RATINGS (T_J = 25°C Unless otherwise specified)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V _{DSS}	60	V
Gate-to-Source Voltage - Continuous			V _{GS}	± 20	V
Gate-to-Source Voltage - Nonrepetitive (T _P < 10 µs)			V _{GS}	30	V
Continuous Drain	Steady	T _C = 25°C	I _D	96	Α
Current	State	T _C = 100°C		68	
Power Dissipation	Steady State	T _C = 25°C	P _D	150	W
Pulsed Drain Current	t _p = 10 μs		I _{DM}	383	Α
Operating and Storage Temperature Range			T _J , T _{stg}	-55 to +175	°C
Source Current (Body Diode)			IS	96	Α
Single Pulse Drain-to-Source Avalanche Energy (L = 0.1 mH, $I_{L(pk)}$ = 53 A)			E _{AS}	140	mJ
Lead Temperature for Soldering Purposes (1/8" from Case for 10 Seconds)		TL	260	°C	

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Case (Drain) Steady State	$R_{\theta JC}$	1.0	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$	36	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

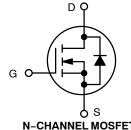
1. Surface mounted on FR4 board using 1 sq in pad size, (Cu Area 1.127 sq in [2 oz] including traces).



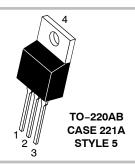
ON Semiconductor®

http://onsemi.com

V _{(BR)DSS}	R _{DS(on)} MAX	I _D MAX
60 V	8.0 mΩ @ 10 V	96 A



N-CHANNEL MOSFET



MARKING DIAGRAMS **& PIN ASSIGNMENTS**

Drain 5863NG **AYWW** Source 2 Drain

G = Pb-Free Device Α = Assembly Location

WW = Work Week

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

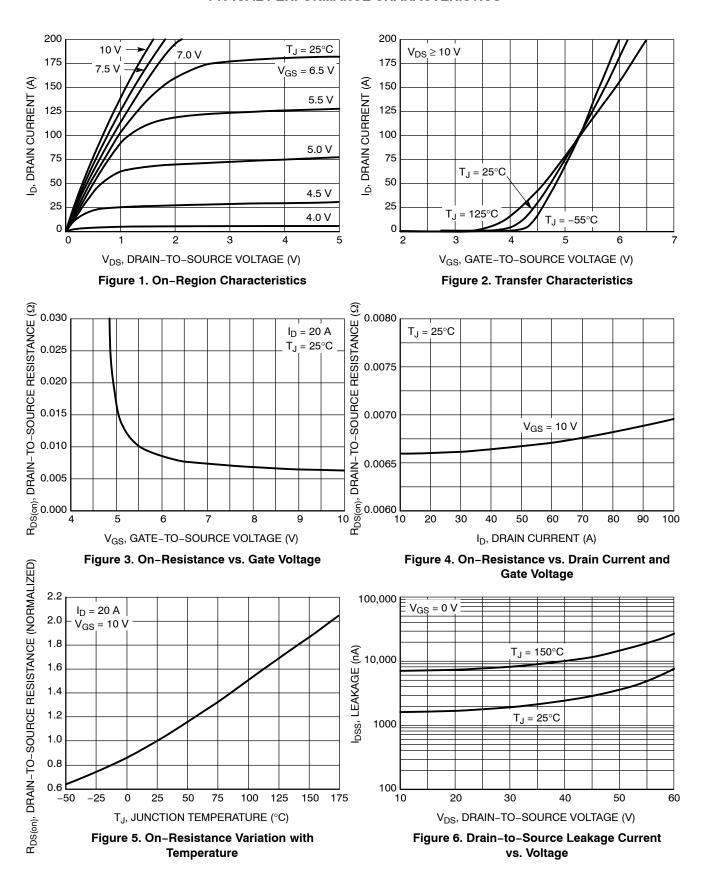
This document contains information on a new product. Specifications and information herein are subject to change without notice.

ELECTRICAL CHARACTERISTICS (T_J = 25°C Unless otherwise specified)

Characteristics	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS	•	•		1	•		•
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{DS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$		60			٧
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	I _D = 250 μA, ref to 25°C			48		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V	T _J = 25°C			1.0	μΑ
		V _{DS} = 60 V	T _J = 125°C			50	1
Gate-Body Leakage Current	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$				±100	nA
ON CHARACTERISTICS (Note 2)							
Gate Threshold Voltage	V _{GS(th)}	$V_{GS} = V_{DS}$	I _D = 250 μA	2.0		4.0	V
Negative Threshold Temperature Coefficient	$V_{GS(th)}/T_J$				9.3		mV/°C
Drain-to-Source On-Resistance	R _{DS(on)}	V _{GS} = 10 \	/, I _D = 20 A		6.6	8.0	mΩ
Forward Transconductance	9FS	V _{DS} = 15 \	/, I _D = 30 A		12		S
CHARGES, CAPACITANCES & GATE RESISTA	ANCE						
Input Capacitance	C _{iss}				3170		pF
Output Capacitance	C _{oss}	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1 MHz			350		
Transfer Capacitance	C _{rss}		2		230		1
Total Gate Charge	Q _{G(TOT)}				55		nC
Threshold Gate Charge	Q _{G(TH)}	V _{GS} = 10 V, V _{DS} = 48 V, I _D = 48 A			3.4		1
Gate-to-Source Charge	Q _{GS}				14.5		1
Gate-to-Drain Charge	Q_{GD}				19		1
Gate Resistance	R_{G}				0.4		Ω
SWITCHING CHARACTERISTICS, V _{GS} = 10 V	(Note 3)						
Turn-On Delay Time	t _{d(on)}				10		ns
Rise Time	t _r	V _{GS} = 10 V,	V _{DD} = 48 V,		34		1
Turn-Off Delay Time	t _{d(off)}	$I_D = 48 \text{ A}, R_G = 2.5 \Omega$			25]
Fall Time	t _f				9.0		1
DRAIN-SOURCE DIODE CHARACTERISTICS	•			•	•	-	•
Forward Diode Voltage	VGS - UV	V _{GS} = 0 V	T _J = 25°C		0.96	1.5	V_{dc}
		T _J = 150°C		0.85		1	
Reverse Recovery Time	t _{rr}	V _{GS} = 0 V _{dc} , I _S = 48 A _{dc} , dI _S /dt = 100 A/μs			28		ns
Charge Time	ta				17		1
Discharge Time	t _b				11		1
Reverse Recovery Stored Charge	Q_{RR}				20		nC

Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperatures.

TYPICAL PERFORMANCE CHARACTERISTICS



TYPICAL PERFORMANCE CHARACTERISTICS

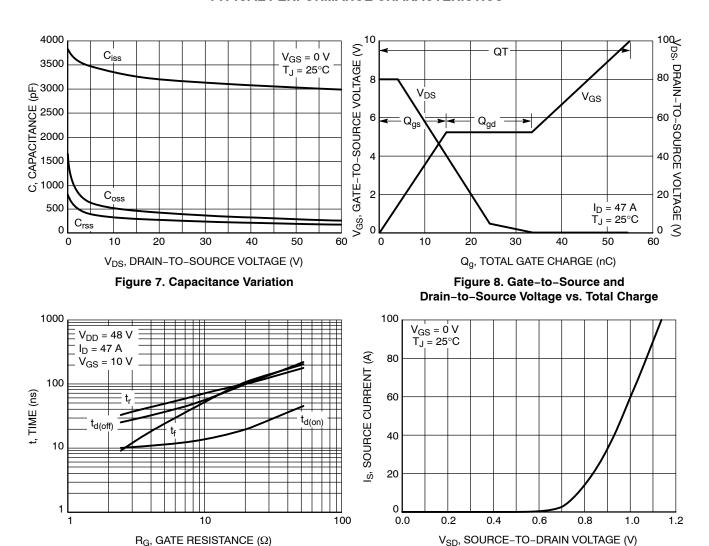


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

Figure 10. Diode Forward Voltage vs. Current

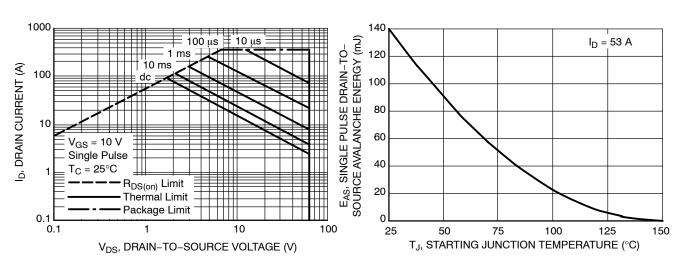


Figure 11. Maximum Rated Forward Biased Safe Operating Area

Figure 12. Maximum Avalanche Energy vs.
Starting Junction Temperature

TYPICAL PERFORMANCE CHARACTERISTICS

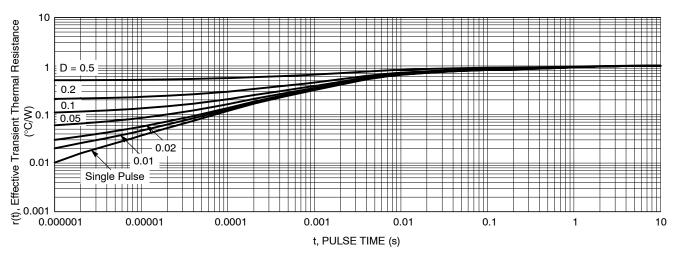


Figure 13. Thermal Response

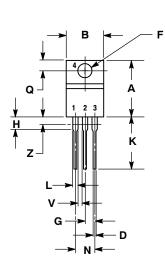
ORDERING INFORMATION

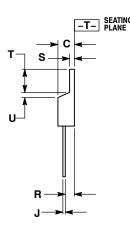
Device	Package	Shipping [†]
NTP5863N	TO-220AB (Pb-Free)	50 Units / Rail

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

TO-220 CASE 221A-09 **ISSUE AF**





NOTES

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.

- DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.161	3.61	4.09
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.014	0.025	0.36	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080	-	2.04

STYLE 5: PIN 1.

GATE

- 2. DRAIN
- 3. SOURCE DRAIN

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