

## DESCRIPTION

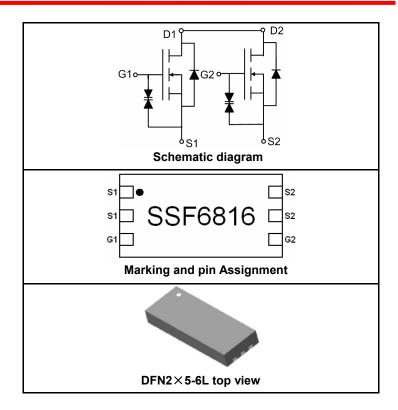
The SSF6816 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V while retaining a 12V  $V_{GS(MAX)}$  rating. It is ESD protected. This device is suitable for use as a uni-directional or bi-directional load switch, facilitated by its common-drain configuration.

## **GENERAL FEATURES**

•  $V_{DS} = 30V, I_D = 8A$   $R_{DS(ON)} < 30m\Omega @ V_{GS} = 2.5V$   $R_{DS(ON)} < 24m\Omega @ V_{GS} = 3.1V$   $R_{DS(ON)} < 22m\Omega @ V_{GS} = 4.0V$   $R_{DS(ON)} < 20m\Omega @ V_{GS} = 4.5V$  $R_{DS(ON)} < 17m\Omega @ V_{GS} = 10V$ 

ESD Rating: 2000V HBM

- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package



#### PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
6816	SSF6816	DFN2×5-6L			3000 units

#### ABSOLUTE MAXIMUM RATINGS(TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	30	V
Gate-Source Voltage	Vgs	±12	V
Drain Current Continuouo@ Current Duland (Nate 1)	I <sub>D</sub>	8	А
Drain Current-Continuous@ Current-Pulsed (Note 1)	I <sub>DM</sub>	45	А
Maximum Power Dissipation	PD	1.7	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 150	°C

## THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient (Note 2)R <sub>0JA</sub> 40°C/W	Thermal Resistance, Junction-to-Ambient (Note 2)	rθja		°C/W
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## ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V Ι <sub>D</sub> =250μΑ	30			V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V			1	μA	

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Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±10V, $V_{DS}$ =0V			10	uA
Gate-Source Breakdown Voltage	BV <sub>GSO</sub>	$V_{DS}$ =0V, I <sub>G</sub> =±250uA	±12			V
ON CHARACTERISTICS (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250µA	0.6	1	1.5	V
Drain-Source On-State Resistance		V <sub>GS</sub> =10V, I <sub>D</sub> =8A		14	17	mΩ
		$V_{GS}$ =4.5V, I <sub>D</sub> =6A		17	20	
	R <sub>DS(ON)</sub>	$V_{GS}$ =4.0V, I <sub>D</sub> =4A		18	22	
		$V_{GS}$ =3.1V, $I_{D}$ =4A		20	24	
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =3A		23	30	
Forward Transconductance	<b>g</b> fs	$V_{DS}$ =5V,I <sub>D</sub> =8A		17		S
DYNAMIC CHARACTERISTICS (Note4)						
Input Capacitance	C <sub>lss</sub>			870		PF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =15V,V <sub>GS</sub> =0V, _ F=1.0MHz		130		PF
Reverse Transfer Capacitance	C <sub>rss</sub>			100		PF
Gate resistance	Rg	V <sub>DS</sub> =0V,V <sub>GS</sub> =0V, F=1.0MHz		1.5		Ω
SWITCHING CHARACTERISTICS (Note 4	)					
Turn-on Delay Time	t <sub>d(on)</sub>			4		nS
Turn-on Rise Time	t <sub>r</sub>	V <sub>DD</sub> =15V,V <sub>GS</sub> =10V,		10		nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_{GEN}=3\Omega$ , $R_{L}=1.25\Omega$		28		nS
Turn-Off Fall Time	t <sub>f</sub>			7		nS
Total Gate Charge	Qg	V <sub>DS</sub> =15V,I <sub>D</sub> =8A,V <sub>GS</sub> =4.5V		10.5		nC
Gate-Source Charge	Q <sub>gs</sub>			1.9		nC
Gate-Drain Charge	Q <sub>gd</sub>			4.1		nC
DRAIN-SOURCE DIODE CHARACTERIST	ICS					
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =1A		0.76	0.9	V
Diode Forward Current (Note 2)	I <sub>S</sub>				4.5	А

## NOTES:

Repetitive Rating: Pulse width limited by maximum junction temperature.
Surface Mounted on FR4 Board, t ≤ 10 sec.
Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
Guaranteed by design, not subject to production testing.



# **TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**

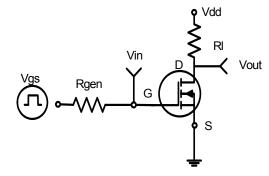


Figure 1: Switching Test Circuit

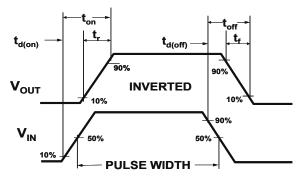


Figure 2:Switching Waveforms

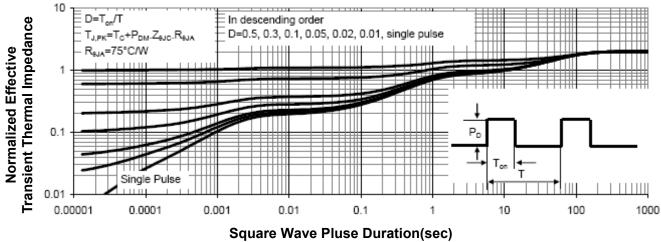


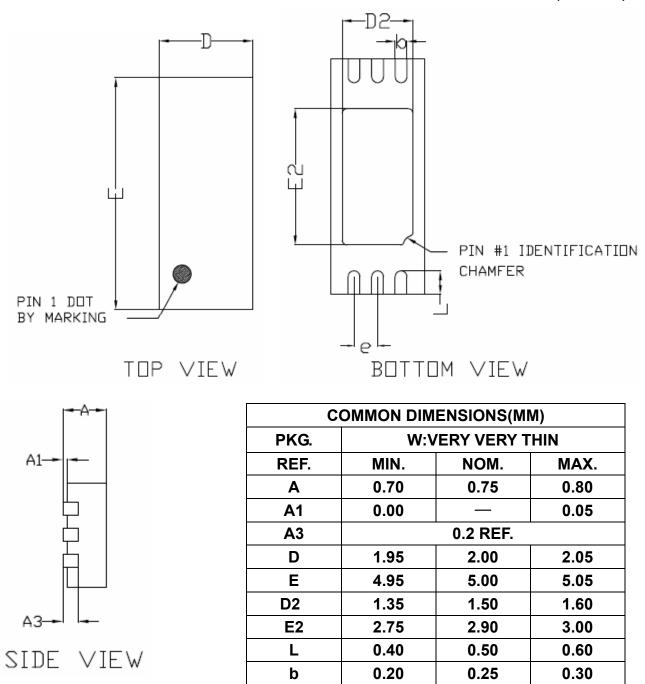
Figure 3: Normalized Maximum Transient Thermal Impedance

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# **DFN2×5-6L PACKAGE INFORMATION**





#### NOTES

1. All dimensions are in millimeters.

2. Tolerance ±0.10mm (4 mil) unless otherwise specified

3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.

4. Dimension L is measured in gauge plane.

5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

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0.5 BCS.



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