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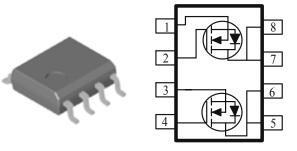
AO4826/MC4826

N-Channel 60-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low r_{DS(on)} provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SOIC-8 saves board space
- Fast switching speed
- High performance trench technology

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)} m(\Omega) \qquad I_D(A)$		
60	$35 @ V_{GS} = 10V$	±6.4	
	$45 @ V_{GS} = 4.5V$	±5.6	



ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Units	
Drain-Source Voltage		V _{DS}	60	v	
Gate-Source Voltage		V _{GS}	±20	v	
Continues Durin Connect ^a	$T_A=25^{\circ}C$	т_	±6.4		
Continuous Drain Current ^a	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	ID	±5.2	A	
Pulsed Drain Current ^b		I _{DM}	±40		
Continuous Source Current (Diode Conduction) ^a		Is	2	Α	
	$T_A=25^{\circ}C$	P. 2.1		W	
Power Dissipation ^a	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	гD	1.3	vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Maximum	Units	
	t <= 10 sec	$R_{\theta JA}$	62.5	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		110	°C/W	

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

b. Pulse width limited by maximum junction temperature

SPECIFICATIONS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)							
Development			Limits			II.4	
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Threshold Voltage	VGS(th)	$V_{\rm DS} = V_{\rm GS}, I_{\rm D} = 250 \mathrm{uA}$	1				
Gate-Body Leakage	Igss	$V_{DS} = 0 V, V_{GS} = 20 V$			±100	nA	
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 60 V, V_{GS} = 0 V$			1		
Zero Gate Voltage Dialii Current	IDSS	$V_{DS} = 60 V, V_{GS} = 0 V, T_J = 55^{\circ}C$			10	uA	
On-State Drain Current ^A	ID(on)	$V_{DS} = 5 V, V_{GS} = 10 V$	20			Α	
		$V_{GS} = 10 \text{ V}, I_D = 6.4 \text{ A}$			35		
Drain-Source On-Resistance ^A	fDS(on)	$V_{GS} = 4.5 \text{ V}, I_D = 5.6 \text{ A}$			45	mΩ	
Forward Tranconductance ^A	g _{fs}	$V_{DS} = 15 \text{ V}, I_D = 6.4 \text{ A}$		11		S	
Diode Forward Voltage	Vsd	$I_S = 2.0 A, V_{GS} = 0 V$			1.2	V	
Dynamic ^b							
Total Gate Charge	Qg	$V_{DS} = 30 V, V_{GS} = 4.5 V,$		12.5		nC	
Gate-Source Charge	Qgs	$V_{DS} = 50 V, V_{GS} = 4.5 V,$ $I_D = 6.4 A$		2.4			
Gate-Drain Charge	Qgd	ID = 0.4 A		2.6			
Switching						•	
Turn-On Delay Time	td(on)			11		nS	
Rise Time	tr	$V_{\rm DD}$ = 30 V, $R_{\rm L}$ = 30 Ω , ${\rm Id}$ = 1 A,		8			
Turn-Off Delay Time	td(off)	$V_{GEN} = 10 V$		19			
Fall-Time	tf	7		6			

Notes

- a. Pulse test: $PW \le 300$ us duty cycle $\le 2\%$.
- b. Guaranteed by design, not subject to production testing.

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