MOSFETs Silicon N-channel MOS (U-MOSVII-H)

# TK100S04N1L

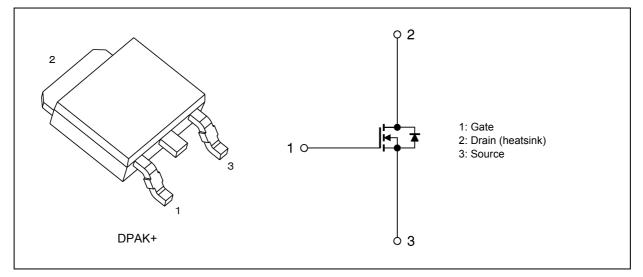
## 1. Applications

- Automotive
- Motor Drivers
- Switching Voltage Regulators

### 2. Features

- (1) Low drain-source on-resistance:  $R_{DS(ON)}$  = 1.9 m $\Omega$  (typ.) (V<sub>GS</sub> = 10 V)
- (2) Low leakage current:  $I_{\rm DSS}$  = 10  $\mu A$  (max) (V\_{\rm DS} = 40 V)
- (3) Enhancement mode:  $V_{th}$  = 1.5 to 2.5 V (V\_{DS} = 10 V,  $I_{D}$  = 0.5 mA)

# 3. Packaging and Internal Circuit



# 4. Absolute Maximum Ratings (Note) ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics			Symbol	Rating	Unit
Drain-source voltage			V <sub>DSS</sub>	40	V
Gate-source voltage			V <sub>GSS</sub>	±20	1
Drain current (DC)		(Note 1)	Ι <sub>D</sub>	100	A
Drain current (pulsed)		(Note 1)	I <sub>DP</sub>	200	1
Power dissipation	(T <sub>c</sub> = 25°C)		PD	100	W
Single-pulse avalanche energy		(Note 2)	E <sub>AS</sub>	114	mJ
Avalanche current			I <sub>AR</sub>	100	A
Channel temperature		(Note 3)	T <sub>ch</sub>	175	°C
Storage temperature		(Note 3)	T <sub>stg</sub>	-55 to 175	7

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### 5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R <sub>th(ch-c)</sub>	1.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 175°C.

Note 2: V\_DD = 32 V, T\_ch = 25°C (initial), L = 8.8  $\mu$ H, R<sub>G</sub> = 1  $\Omega$ , I<sub>AR</sub> = 100 A

Note 3: The definitions of the absolute maximum channel and storage temperatures are based on AEC-Q101.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

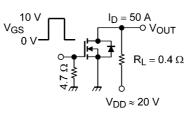
#### 6. Electrical Characteristics

### 6.1. Static Characteristics (T<sub>a</sub> = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±20 V, $V_{DS}$ = 0 V	_	_	±1	μA
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V	_	—	10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	40	_	_	V
Drain-source breakdown voltage	V <sub>(BR)DSX</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = -20 V	20	—	—	
Gate threshold voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.5 mA	1.5	_	2.5	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 50 A	_	2.8	4.5	mΩ
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 50 A	_	1.9	2.3	

# 6.2. Dynamic Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	—	5490	_	pF
Reverse transfer capacitance	C <sub>rss</sub>	]	_	220	_	
Output capacitance	C <sub>oss</sub>	]	_	3000		
Switching time (rise time)	tr	See Figure 6.2.1.	—	8		ns
Switching time (turn-on time)	t <sub>on</sub>	1		23		
Switching time (fall time)	t <sub>f</sub>	1		19		
Switching time (turn-off time)	t <sub>off</sub>	]	_	80	—	



Duty  $\leq$  1%, t<sub>w</sub> = 10  $\mu$ s

Fig. 6.2.1 Switching Time Test Circuit

#### 6.3. Gate Charge Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD}\approx 32 \text{ V}, \text{ V}_{GS} \text{ = } 10 \text{ V}, \text{ I}_{D} \text{ = } 100 \text{ A}$	_	76	—	nC
Gate-source charge 1	Q <sub>gs1</sub>		_	18	_	
Gate-drain charge	Q <sub>gd</sub>			11	_	

# 6.4. Source-Drain Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 4)	I <sub>DR</sub>	—	_	_	100	А
Reverse drain current (pulsed)	(Note 4)	I <sub>DRP</sub>		_	_	200	
Diode forward voltage		V <sub>DSF</sub>	I <sub>DR</sub> = 100 A, V <sub>GS</sub> = 0 V	_	_	-1.2	V
Reverse recovery time		t <sub>rr</sub>	I <sub>DR</sub> = 100 A, V <sub>GS</sub> = 0 V		90		ns
Reverse recovery charge		Q <sub>rr</sub>	-dI <sub>DR</sub> /dt = 50 A/μs	_	90	_	nC

Note 4: Ensure that the channel temperature does not exceed 175°C.

# 7. Marking (Note)

TOSHIBA

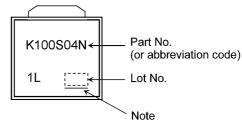
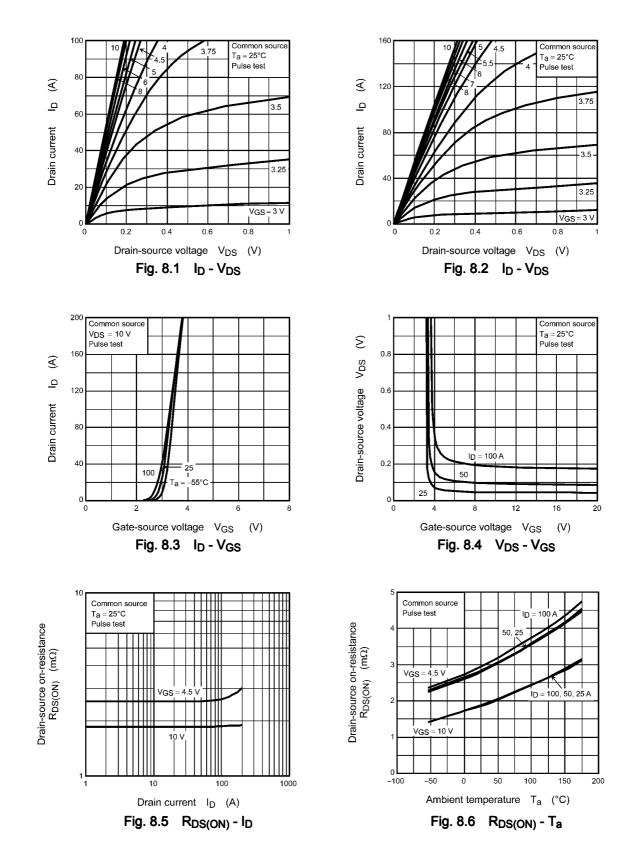


Fig. 7.1 Marking

 Note: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.
The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

# 8. Characteristics Curves (Note)



50

Case temperature

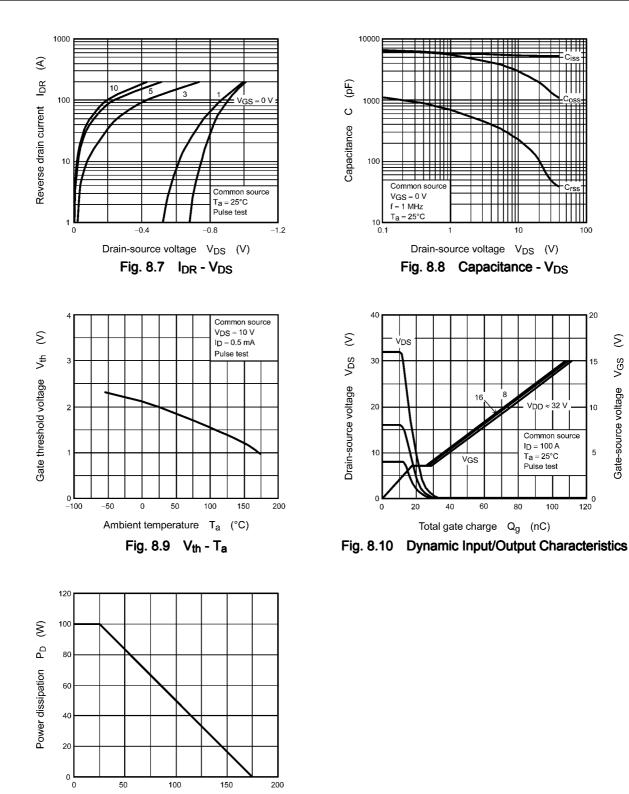
100

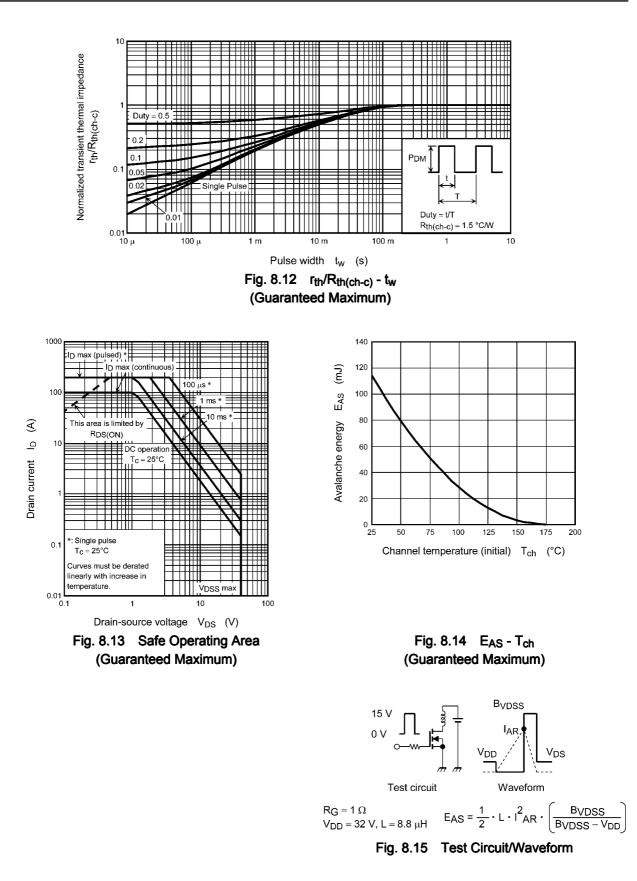
Fig. 8.11 P<sub>D</sub> - T<sub>c</sub> (Guaranteed Maximum)

150

 $T_c$  (°C)

200



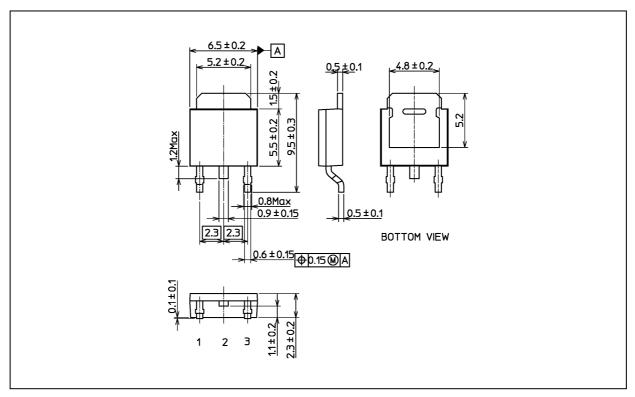


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



### Package Dimensions

Unit: mm



Weight: 0.36 g (typ.)

	Package Name(s)
TOSHIBA: 2-7M1A	
Nickname: DPAK+	

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