TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSIV)

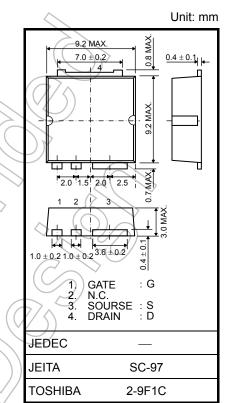
TK80X04K3

Switching Regulator, DC-DC Converter Applications Motor Drive Applications

- Low drain-source ON-resistance: $R_{DS (ON)} = 2.7 \text{ m}\Omega (typ.)$
- High forward transfer admittance: |Y_{fs}| = 150 S (typ.)
- Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 40 \ V)$
- Enhancement mode: V_{th} = 3.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

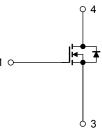
Characteristics		Symbol	Rating	Unit	$\langle \rangle$	
Drain-source voltage		V _{DSS}	40	V		
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	40	Y		
Gate-source voltage			V _{GSS}	±20	> v	
Drain current	DC	(Note 1)	۱ _D	80	А	
	Pulse	(Note 1)	I _{DP}	320	A	
Drain power dissipation (Tc = 25° C)			P _D <	125	W	
Single pulse avalanche energy (Note 2)			EAS	123	Em	
Avalanche current			IAR	80	Α	\sim
Repetitive avalanche energy (Note 3)			EAR	12.5	mJ	
Channel temperature (Note 4)			Tch	175	3°C	
Storage temperature range (Note 4)			∕∕T _{stg}	-55 to 175	°C	



Weight: 0.74 g (typ.)

Thermal Characteristics

Characteristics	Symbol Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)} 1.2	°C/W



Note 1: Ensure that the channel temperature does not exceed 175°C.

Note 2: $V_{DD} = 25 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), $L = 20 \mu\text{H}$, $I_{AR} = 80 \text{ A}$, $R_G = 25 \Omega$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

- Note 4: The definitions of the absolute maximum channel temperature and storage temperatures are based on AEC-Q101.
- Note 5: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.

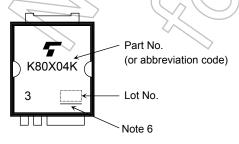
Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cu	rrent	I _{GSS}	$V_{GS} = \pm 20$ V, $V_{DS} = 0$ V		_	±10	μA	
Drain cut-off curr	rent	I _{DSS}	$V_{DS} = 40 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	— — 10		10	μA	
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	40		_	V	
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	20		_	V	
Gate threshold voltage		V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	3.0)}	4.0	V	
Drain-source ON-resistance		R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 40 \text{ A}$	77	2.7	3.5	mΩ	
Forward transfer admittance		Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 40 \text{ A}$	75	150	_	S	
Input capacitance		C _{iss}			4600	_		
Reverse transfer capacitance		C _{rss}	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	_	850	_	pF	
Output capacitance		C _{oss}			1150	1		
Switching time	Rise time	tr	$V_{GS} \stackrel{10}{_{0}V} \int I_{D} = 40 \text{ A}$	- (28			
	Turn-on time	t _{on}			60) –	20	
	Fall time	t _f			35	_	ns	
	Turn-off time	t _{off}	Duty $\leq 1\%$, t _w = 10 µs	D	90	_		
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \approx 32 V, V_{GS} \neq 10 V,$) —	100	_		
Gate-source charge		Qgs	$V_{DD} \approx 32 \text{ V}, \text{ V}_{GS} = 10 \text{ V},$ $V_{D} = 80 \text{ A}$		60		nC	
Gate-drain ("miller") charge		Q _{gd}			40	_		

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	(5)	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current	(Note 1)		(()) -	_	_	80	А
Pulse drain reverse current	(Note 1)	IDRP	_	_	_	320	А
Forward voltage (diode)	,,	VDSF	I _{DR} = 80 A, V _{GS} = 0 V	_	_	-1.5	V
Reverse recovery time		trr	I _{DR} = 80 A, V _{GS} = 0 V,	_	80	—	ns
Reverse recovery charge	\wedge	Q _{rr}	dl _{DR} /dt = 30 A/µs	_	60	_	nC

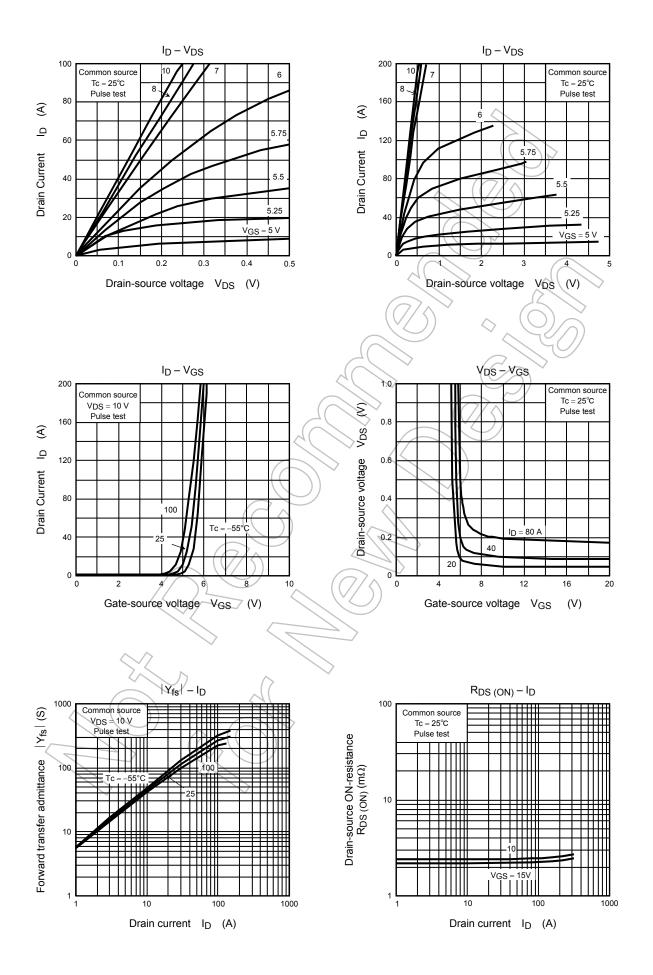
Marking

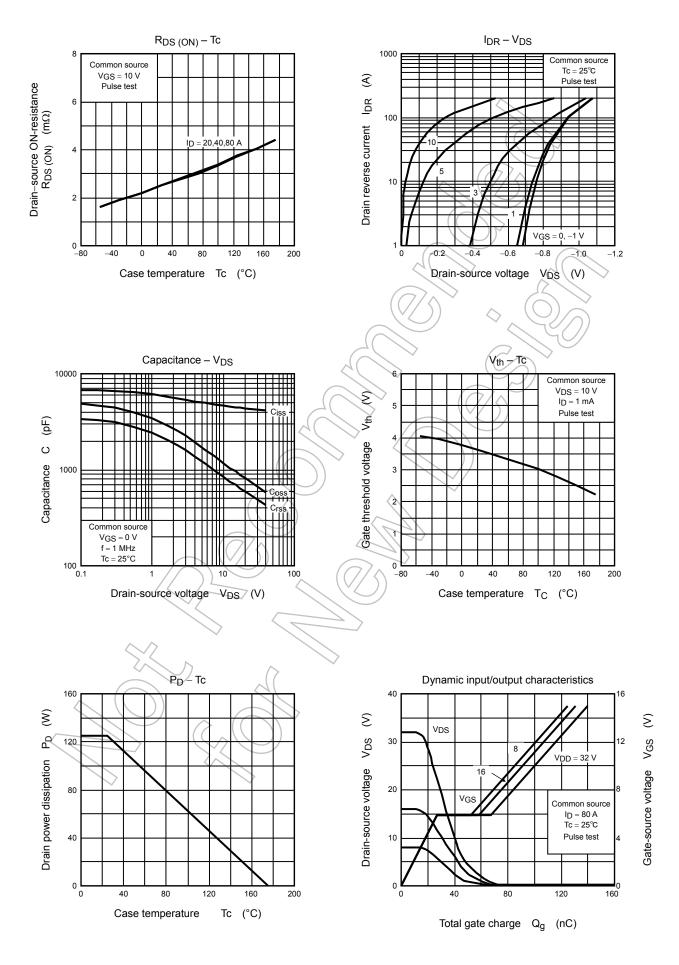


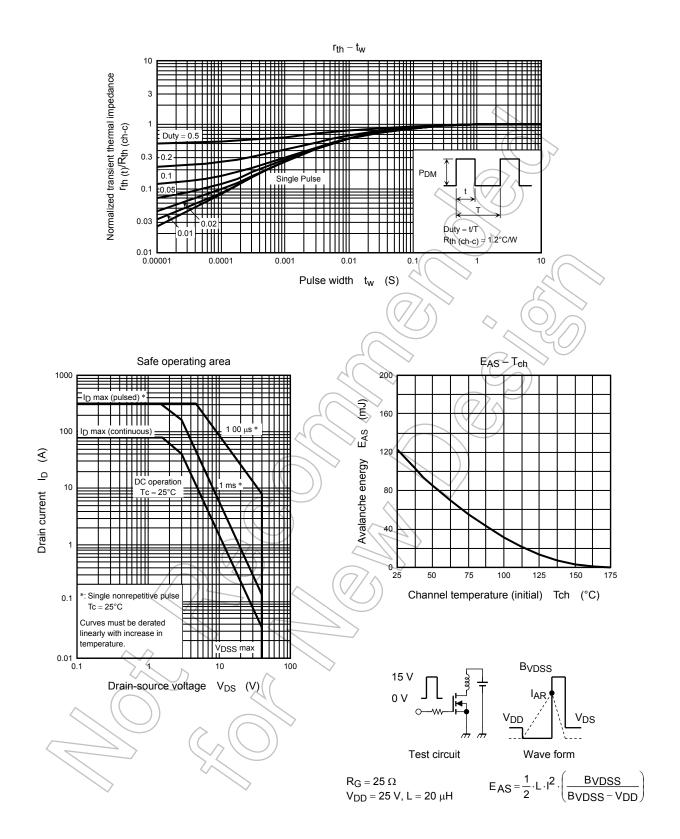
Note 6: A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment

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