TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOSVI-H)

TPCP8008-H

High-Efficiency DC-DC Converter Applications Notebook PC Applications Portable Equipment Applications

- · Small footprint due to a small and thin package
- · High-speed switching
- Small gate charge: Q_{SW} = 3.8 nC (typ.)
- · Low drain-source ON-resistance:

 $R_{DS (ON)} = 16 \text{ m}\Omega \text{ (typ.) (V}_{GS} = 4.5 \text{ V)}$

- High forward transfer admittance: |Y_{fs}| = 26 S (typ.)
- Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 30 \text{ V)}$
- Enhancement mode: $V_{th} = 1.3 \text{ to } 2.3 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 0.1 \text{ mA})$

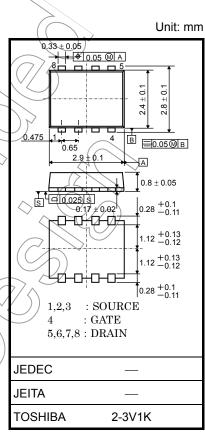
Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Symbol Rating	
Drain-source voltage		V_{DSS}	30	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR} <	30	y
Gate-source voltage		V _{GSS}	±20	
Drain current	DC (Note 1)	ID((8	A
Diaili Cullent	Pulsed (Note 1)	P P	32	^ `
Drain power dissipation (t = 5 s) (Note 2a)		PD	1.68	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
Drain power dissipation (t = 5 s) (Note 2b)		PD	0.84	w
Single-pulse avalance	ne energy (Note 3)	EAS	42	mJ
Avalanche current		IAR	8	Α
Repetitive avalanche energy (Tc = 25°C) (Note 4)		E _{AR}	0.11	mJ
Channel temperature		Tch	150	°C
Storage temperature range		T _{stg}	–55 to 150	°C

Note: For Notes 1 to 5, refer to the next page.

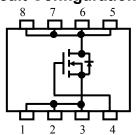
Using continuously under heavy toads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.

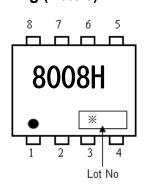


Weight: 0.017g (typ.)

Circuit Configuration

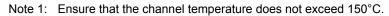


Marking (Note 5)



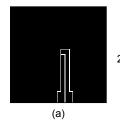
Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to ambient $(t = 5 \text{ s})$ (Note 2a)	R _{th (ch-a)}	74.4	°C/W
Thermal resistance, channel to ambient (t = 5 s) (Note 2b)	R _{th (ch-a)}	148.8	°C/W

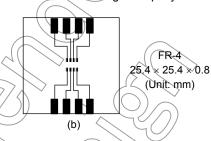


Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)



FR-4 25.4 × 25.4 × 0.8 (Unit: mm)



Note 3: $V_{DD} = 24 \text{ V}$, $T_{ch} = 25 \text{ °C}$ (initial), $L = 500 \mu\text{H}$, $R_G = 1 \Omega$, $I_{AR} = 8 \text{ A}$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

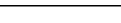
Note 5: * Weekly code: (Three digits)

Week of manufacture

(01 for the first week of the year, continuing up to 52 or 53)

Year of manufacture

(The last digit of the year)



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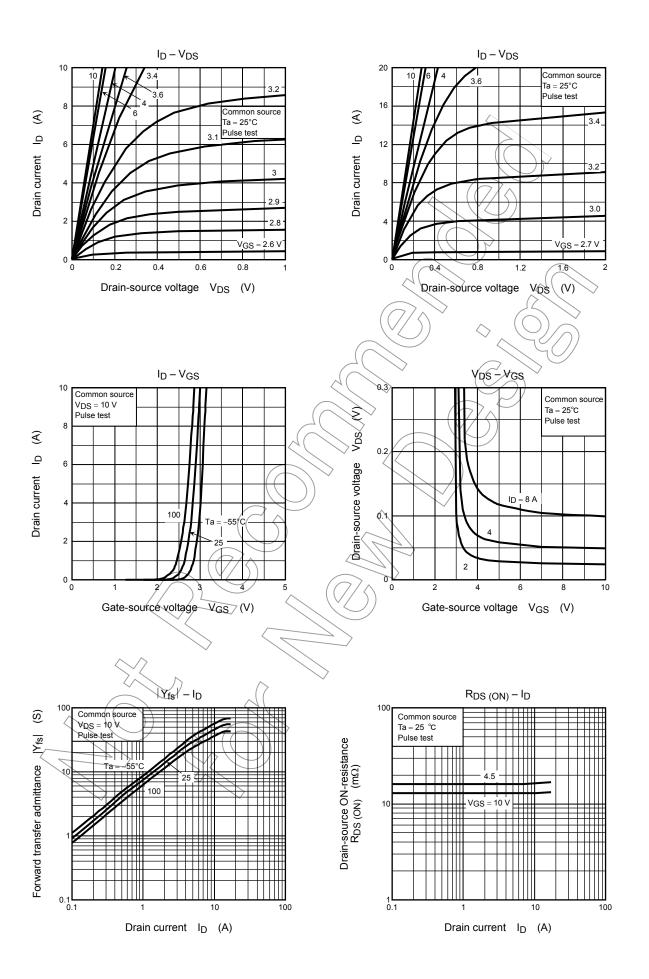
Electrical Characteristics (Ta = 25°C)

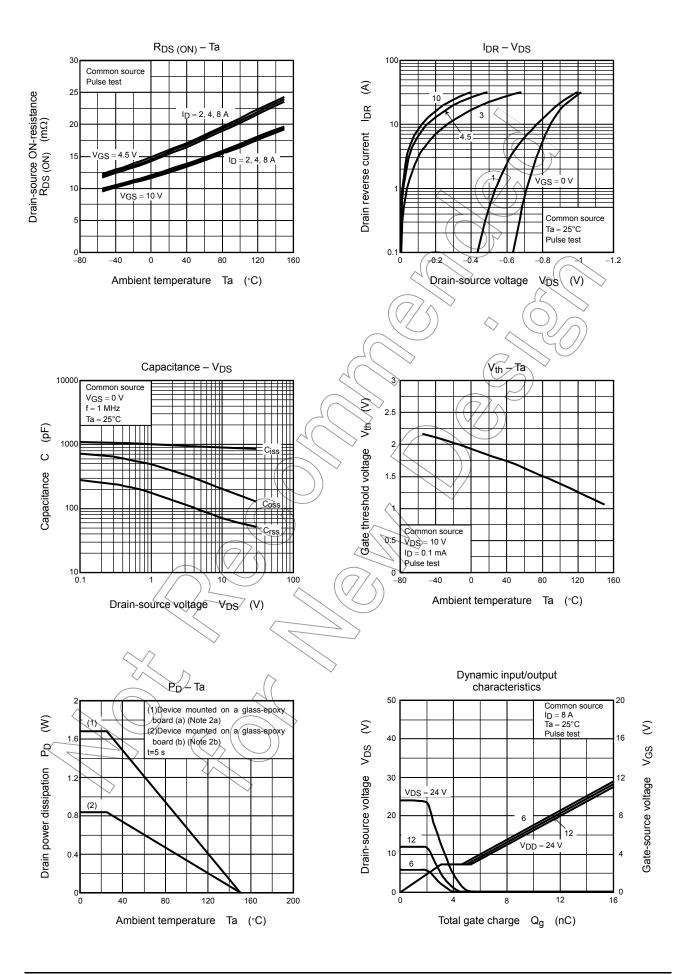
Ch	aracteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
Drain cutoff curre	nt	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	10	μА
Drain-source breakdown voltage		V _{(BR) DSS}	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V
		V _{(BR) DSX}	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	_	_	V
Gate threshold vo	oltage	V _{th}	V _{DS} = 10 V, I _D = 0.1 mA	1.3) / _	2.3	V
Drain-source ON-resistance		-	V _{GS} = 4.5 V, I _D = 4 A	<u> </u>	16	23	- mΩ
		R _{DS} (ON)	V _{GS} = 10 V, I _D = 4 A))	13	20	
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 4 A	13	26	_	S
Input capacitance		C _{iss}		_	900	1170	
Reverse transfer	capacitance	C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	65	104	pF
Output capacitan	ce	C _{oss}			200	\nearrow	
Gate resistance		rg	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	-6	3.0	4.0	Ω
Switching time	Rise time	t _r	10 V T ID = 4 A	7	2.4) _	
	Turn-on time	t _{on}	V _{GS} 10 V I _D = 4 A V _{OUT} G	7(5)	8.6	_	ne
	Fall time	t _f	27.4 W % S S S S S S S S S S S S S S S S S S		13	_	ns
	Turn-off time	t _{off}	Duty ≤ 1%, t _W = 10 μs	_	31	_	
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 8 \text{ A}$	_	14.7	_	
			$V_{DD} \approx 24 \text{ V}, V_{GS} = 5 \text{ V}, V_{D} \neq 8 \text{ A}$	_	8.0	_	
Gate-source char	ge 1 (Q _{gs1}			3.1		nC
Gate-drain ("Miller") charge		Q _{gd}	$V_{DD} \approx 24 \text{ V, } V_{GS} = 10 \text{ V, } I_D = 8 \text{ A}$	_	2.3	_	
Gate switch charg	ge ((7/	Q _{SW}		_	3.8	_	

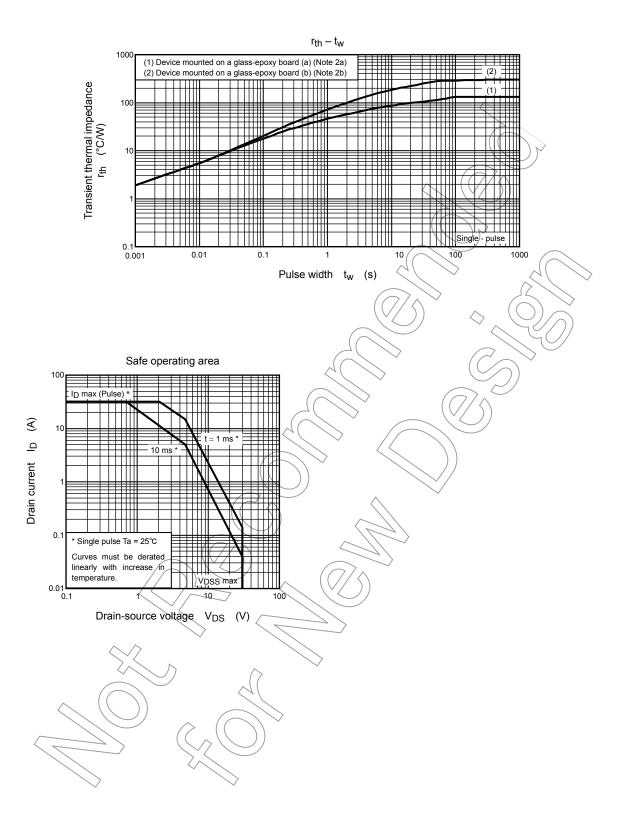
Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Peak forward current Pulse (Note 1)	IFP	> -	_	_	32	Α
Forward voltage (diode)	VDSF	I _{DR} = 8 A, V _{GS} = 0 V			-1.2	V

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