

## UNISONIC TECHNOLOGIES CO., LTD

MGBR10S45 Preliminary DIODE

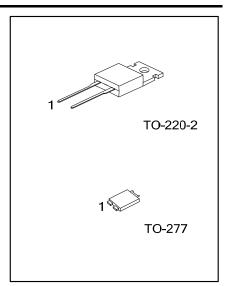
# MOS GATED BARRIER RECTIFIER

#### **■** DESCRIPTION

The UTC MGBR10S45 is a surface mount mos gated barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

#### ■ FEATURES

- \* Super low forward voltage drop
- \* High switching speed



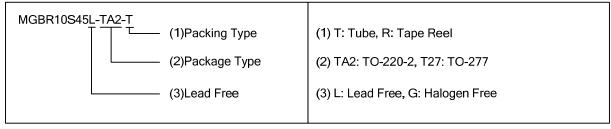
#### ■ SYMBOL

TO-220-2	TO-277
2 — 1 K	1. A °———— 2. K

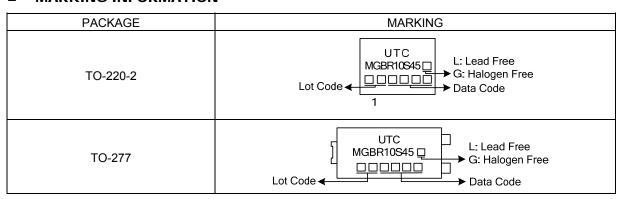
#### ■ ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Doolsing	
Lead Free	Halogen Free	Package	1	2	3	Packing	
MGBR10S45L-TA2-T	MGBR10S45G-TA2-T	TO-220-2	K	Α	-	Tube	
MGBR10S45L-T27-R	MGBR10S45G-T27-R	TO-277	Α	K	Α	Tape Reel	

Note: Pin Assignment: A: Anode K: Common Cathode



## **■ MARKING INFORMATION**



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## ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub>=25°C, unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER	SYMBOL	RATINGS	UNIT
DC Blocking Voltage	$V_{RM}$	45	V
Working Peak Reverse Voltage	$V_{RWM}$	45	V
Peak Repetitive Reverse Voltage	$V_{RRM}$	45	V
RMS Reverse Voltage	$V_{R(RMS)}$	32	V
Average Rectified Output Current T <sub>C</sub> =140°C	Ιο	10	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	150	Α
Repetitive Peak Avalanche Power (1µs, 25°C)	$P_{ARM}$	5000	W
Operating Junction Temperature	TJ	-65~+150	°C
Storage Temperature	T <sub>STG</sub>	-65~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### **■ THERMAL CHARACTERISTICS**

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient	TO-220-2	0	60	°C/W	
	TO-277	TO-277 θ <sub>JA</sub>	73 (Note 3)		
Junction to Case	TO-220-2	0	2	°C/W	
	TO-277	$\theta_{JC}$	13 (Note 3)		

## ■ **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> =25°C, unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS		TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I <sub>R</sub> =0.45mA	45			V
Forward Voltage Drop	I V <sub>EM</sub>	I <sub>F</sub> =10A, T <sub>J</sub> =25°C			0.48	V
		I <sub>F</sub> =10A, T <sub>J</sub> =125°C			0.43	V
Leakage Current (Note 1)	I DM	V <sub>R</sub> =45V, T <sub>J</sub> =25°C		50	500	μΑ
		V <sub>R</sub> =45V, T <sub>J</sub> =125°C		12	40	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

- 2. Thermal resistance junction to case mounted on heatsink.
- 3. Mounted on an FR4 PCB, single-sided copper, with 100cm<sup>2</sup> copper pad area.

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