

# BYD33DGP thru BYD33MGP

New Product

atented\*

Glass-plastic encapsulation

technique is covered by Patent No. 3,996,602, brazed-lead assembly by Patent No. 3,930,306

**Mechanical Data** 

Vishay General Semiconductor

DO-204AL (DO-41)

Case: DO-204AL, molded epoxy over glass body

Terminals: Matte tin plated leads, solderable per

E3 suffix for commercial grade, HE3 suffix for high

Epoxy meets UL-94V-0 Flammability rating

Polarity: Color band denotes cathode end

J-STD-002B and JESD22-B102D

reliability grade (AEC Q101 qualified)

# **Avalanche Glass Passivated Junction Fast Switching Rectifier**

#### **Major Ratings and Characteristics**

I <sub>F(AV)</sub>	1.0 A				
V <sub>RRM</sub>	200 V to 1000 V				
I <sub>FSM</sub>	30 A				
E <sub>RSM</sub>	10 mJ, 7 mJ				
t <sub>rr</sub>	150 ns, 250 ns, 300 ns				
I <sub>R</sub>	5.0 µA				
T <sub>j</sub> max.	175 °C				

#### Features

- Cavity-free glass-passivated junction
- Avalanche surge capability guaranteed
- Fast reverse recovery time
- Low switching losses, high efficiency
- Low leakage current, typical I<sub>R</sub> less than 0.1  $\mu$ A
- High forward surge capability
- Meets environmental standard MIL-S-19500
- Solder Dip 260 °C, 40 seconds

## **Typical Applications**

For use in high frequency rectification of switching power supplies, inverters, converters and freewheel ing applications for consumer, automotive and Telecommunication

#### **Maximum Ratings**

 $T_A = 25 \ ^{\circ}C$  unless otherwise specified

Parameter	Symbol	BYD33DGP	BYD33GGP	BYD33JGP	BYD33KGP	BYD33MGP	Unit
Device Marking Code		33DGP	33GGP	33JGP	33KGP	33MGP	V
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	200	400	600	800	1000	V
Maximum DC blocking voltage	V <sub>DC</sub>	200	400	600	800	1000	V
Maximum average forward rectified currentI F(AV)1.00.375 " (9.5 mm) lead length at $T_A = 55 \ ^{\circ}C$ 1.0						А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	30					A
Non-repetitive peak reverseD-Javalanche energyK-M	E <sub>RSM</sub>	10 7					mJ
Maximum full load reverse current, full cycle average 0.375" (9.5 mm) lead length $T_A = 55 \ ^\circ C$	I <sub>R(AV)</sub>	I <sub>R(AV)</sub> 100					μA
Operating junction and storage temperature range	T <sub>J</sub> ,T <sub>STG</sub>	- 65 to + 175					°C



# **BYD33DGP thru BYD33MGP**

### **Vishay General Semiconductor**



#### **Electrical Characteristics**

 $T_A = 25$  °C unless otherwise specified

Parameter	Test condition	Symbol	BYD33DGP	BYD33GGP	BYD33JGP	BYD33KGP	BYD33MGP	Unit
Maximum instantaneous forward voltage	at 1.0 A <sup>(1)</sup>	V <sub>F</sub>	1.3					V
Maximum DC reverse current at rated DC blocking voltage	T <sub>A</sub> = 25 °C T <sub>A</sub> = 150 °C	I <sub>R</sub>	5.0 200					μA
Maximum reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1.0 A, I <sub>rr</sub> = 0.25 A	t <sub>rr</sub>	1	50	250	3	00	ns
Typical junction capacitance	at 4.0 V, 1 MHz	CJ	15					pF

Notes:

(1) Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

## **Thermal Characteristics**

 $T_A = 25 \ ^{\circ}C$  unless otherwise specified

Parameter	Symbol	BYD33DGP	BYD33GGP	BYD33JGP	BYD33KGP	BYD33MGP	Unit
Typical thermal resistance <sup>(1)</sup>	$R_{\thetaJA}$	55					°C/W

Notes:

(1) Thermal resistance from junction to ambient at 0.375" (9.5 mm) lead length, P.C.B. mounted

#### **Ratings and Characteristics Curves**

(T<sub>A</sub> = 25 °C unless otherwise specified)

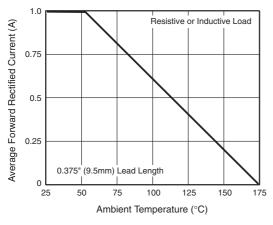


Figure 1. Forward Current Derating Curve

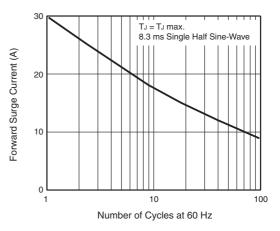


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current



# BYD33DGP thru BYD33MGP

## **Vishay General Semiconductor**

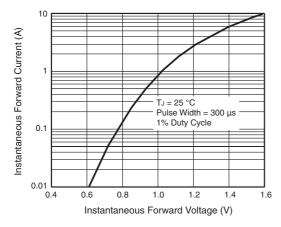


Figure 3. Typical Instantaneous Forward Characteristics

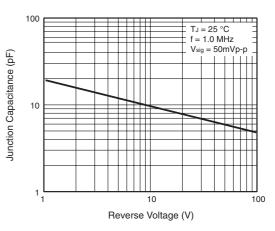


Figure 5. Typical Junction Capacitance

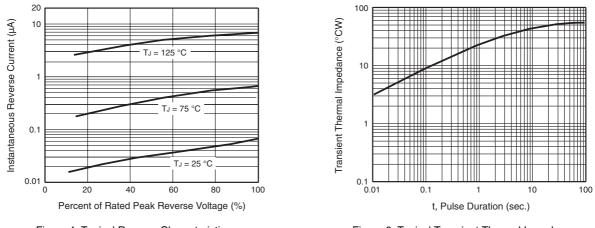
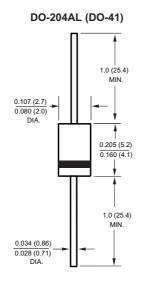


Figure 4. Typical Reverse Characteristics

Figure 6. Typical Transient Thermal Impedance

## Package outline dimensions in inches (millimeters)





Vishay

# Notice

Specifications of the products displayed herein are subject to change without notice. Vishay Intertechnology, Inc., or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Information contained herein is intended to provide a product description only. No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Vishay's terms and conditions of sale for such products, Vishay assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of Vishay products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Vishay for any damages resulting from such improper use or sale.