





SOT-23 Formed SMD Package

BCW66F, BCW66G BCW66H

GENERAL PURPOSE TRANSISTOR

N-P-N transistor

Marking

 $BCW \ 66F = EF$

BCW 66G = EG

BCW 66H = EH

PACKAGE OUTLINE DETAILS
ALL DIMENSIONS IN mm

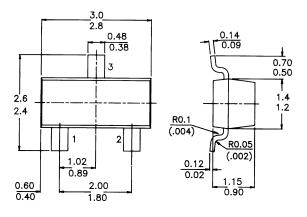


1 = BASE

2 = EMITTER

3 = COLLECTOR





ABSOLUTE MAXIMUM RATINGS		В	CW66F	66G	66H	
Collector-base voltage (open emitter)	$-V_{CBO}$	max.	<i>75</i>	<i>75</i>	<i>75</i>	V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	45	45	45	V
Emitter-base voltage (open collector)	$-V_{EBO}$	max.	5	5	5	V
Collector current (d.c.)	$-I_C$	max.	800	800	800	mA
Total power dissipation at $T_{amb} = 25^{\circ}C$	P_{tot}	max	225	225	225	mW
D.C. current gain						
$-I_C = 100 \text{ mA}; -V_{CE} = 10 \text{ V}$	h_{FE}	min.	35	<i>50</i>	<i>80</i>	
$-I_C = 10 \text{ mA}; V_{CE} = 1 \text{ V}$		min.	<i>75</i>	110	180	
$-I_C = 100 \text{ mA}; \ V_{CE} = 1 \ V$		min.	100	160	250	
		max.	<i>250</i>	400	<i>630</i>	
$-I_C = 500 \text{ mA}; V_{CE} = 2 \text{ V}$		min.	35	60	100	

BCW66F, BCW66G BCW66H

RATINGS (at $T_A = 25^{\circ}C$ unless otherwise specified)									
Limiting values	•	BCW	66F	66G	66H				
Collector-base voltage (open emitter)	$-V_{CBO}$	max.	75	75	75	V			
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	45	45	45	V			
Emitter-base voltage (open collector)	$-V_{EBO}$	max.	5	5	5	V			
Collector current (d.c.)	$-I_C$	max.	800	800	800	mA			
Total power dissipation at $T_{amb} = 25^{\circ}C$	P_{tot}	max	225	225	225	mW			
Storage temperature	T_{stg}		−55 to +150			° C			
	Ü								
THERMAL CHARACTERISTICS									
$T_j = P (R_{th j-t} + R_{th s-a}) + T_{amb}$									
Thermal resistance									
from junction to ambient	$R_{th\ j-a}$		<i>556</i>	556	<i>556</i>	°C/mW			
CHARACTERISTICS (at $T_A = 25$ °C unless	othomuico	anagifia	(d)						
Collector-emitter breakdown voltage	ouiei wise	specifie	u)						
$-I_C = 10 \text{ mA}; I_B = 0$	-V _{(BR)CE}	omin	45	45	45	V			
Collector–emitter breakdown voltage	- v (BR)CE	Omm.	40	40	40	V			
$-I_C = 10 \text{ mA}; V_{EB} = 0$	-V _{(BR)CE}	a min	75	75	75	V			
Emitter-base breakdown voltage	- v (BR)CE	S IIIII.	73	73	73	V			
$-I_E = 10 \text{ mA}; I_C = 0$	-V _{(BR)EB}	o min	5	5	5	V			
Collector cut-off current	- v (BR)EB	Jiiii.	J	J	J	V			
$-V_{CE} = 45 \text{ V}; I_C = 0 \text{ V}$	-I _{CES}	max.	20	20	20	nΑ			
Emitter cut-off current	-1CES	шал.	20	20	20	ш			
$V_{EB} = 4 \text{ V}; I_C = 0$	I_{EBO}	max.	20	20	20	nΑ			
Output capacitance at $f = 1$ MHz	¹EBO	max.	20	20	20	ш			
$I_E = 0; -V_{CB} = 10 \text{ V}$	$C_{\mathcal{C}}$	max.	12	12	12	pF			
Input capacitance at $f = 1$ MHz	c_c	max.	12	12	12	pr			
$I_C = 0; -V_{EB} = 0.5 \text{ V}$	C_{e}	max.	80	80	80	nF			
$IC = 0$, $-v_{EB} = 0.3$ v	c_e	шал.	00	00	00	ρr			
Saturation voltages									
$-I_C = 500 \text{ mA}; -I_B = 50 \text{ mA}$	-V _{CEsat}	max.	0.7	0.7	0.7	V			
$-I_C = 100 \text{ mA}; -I_B = 10 \text{ mA}$	-V _{CEsat}	typ.	0.3	0.3	0.3	V			
$-I_C = 500 \text{ mA}; -I_B = 50 \text{ mA}$	-V _{BEsat}	max.	2	2	2	V			
Noise figure at $R_S = 1 \text{ kW}$	DEBU								
$-I_C = 0.2 \text{ mA; } -V_{CE} = 5 \text{ V}$									
f = 1 KHz, BW = 200 Hz	NF	max.	10	10	10	dB			
Current Gain-Bandwidth Product									
$I_C = 20 \text{ mA}, \ V_{CE} = 10 \ V, \ f = 100 \ MHz$		min.	100	100	100	MHz			

Notes

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered Trademark of Continental Device India Limited

C-120 Naraina Industrial Area, New Delhi 110 028, India.

Telephone + 91-11-579 6150 Fax + 91-11-579 9569, 579 5290
e-mail sales@cdil.com www.cdil.com