

Silicon Carbide PiN Diode Chip

Features

- 10 kV blocking
- 250 °C operating temperature
- Fast turn off characteristics
- Soft reverse recovery characteristics
- Ultra-Fast high temperature switching

Advantages

- Industry's lowest conduction losses
- Reduced stacking
- · Reduced system complexity/Increased reliability



Applications

- Voltage Multiplier
- Ignition/Trigger Circuits
- Oil/Downhole
- Lighting
- Defense

Maximum Ratings at T_j = 250 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values	Unit
Repetitive peak reverse voltage	V _{RRM}		10	kV
Continuous forward current	I _F	T _C ≤ 150 °C	2	А
RMS forward current	I _{F(RMS)}	T _C ≤ 150 °C	1	А
Operating and storage temperature	T _j , T _{stg}		-55 to 250	°C

Electrical Characteristics at T_j = 250 °C, unless otherwise specified

Devemeter	Symbol	Conditions	Values		11:0:4	
Parameter		Conditions	min.	typ.	max.	Unit
Diode forward voltage	V _F	I _F = 2 A, T _j = 25 °C		4.4	4.8	V
		I _F = 2 A, T _j = 225 °C		4.1	4.5	v
Reverse current	I _R	V _R = 10 kV, T _j = 25 °C		0.1	3	
		V _R = 10 kV, T _j = 225 °C			50	μA
Total reverse recovery charge	Qrr	$V_{\rm R} = 1000 V$ $I_{\rm F} \le I_{\rm F,MAX}$ $I_{\rm F} = 1.5 A$		558		nC
Switching time	ts	$\begin{array}{c} \text{dI}_{\text{F}}/\text{dt} = 70 \text{ A}/\mu\text{s} \\ \text{T}_{\text{j}} = 225 ^{\circ}\text{C} \end{array} \qquad \begin{array}{c} \text{V}_{\text{R}} = 1.0 ^{\circ}\text{A} \\ \text{V}_{\text{R}} = 1000 ^{\circ}\text{V} \\ \text{I}_{\text{F}} = 1.5 ^{\circ}\text{A} \end{array}$		< 236		ns
		V _R = 1 V, f = 1 MHz, T _i = 25 °C		20		
Total capacitance	С	V _R = 400 V, f = 1 MHz, T _i = 25 °C		5		pF
		V _R = 1000 V, f = 1 MHz, T _j = 25 °C		4		-
Total capacitive charge	Qc	V _R = 1000 V, f = 1 MHz, T _j = 25 °C		5.34		nC

*For chip size and metallization, please refer to the mechanical datasheet (must have a non-disclosure agreement with GeneSiC Semiconductor).

Electrical Datasheet*

GA01PNS100-CAU

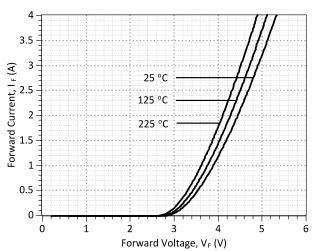


Figure 1: Typical Forward Characteristics

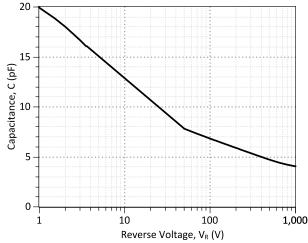
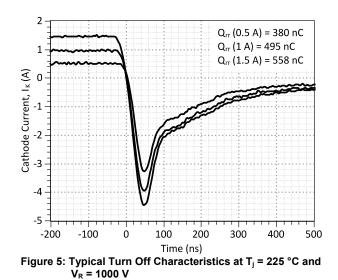


Figure 3: Typical Junction Capacitance vs Reverse Voltage Characteristics



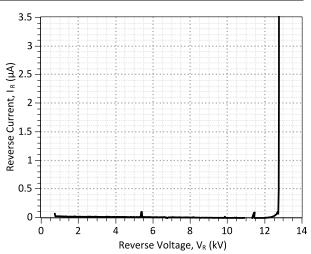
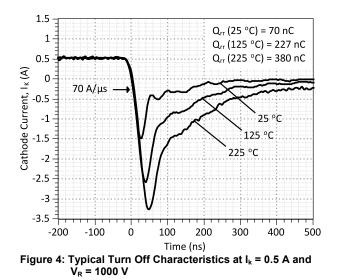


Figure 2: Typical Reverse Characteristics



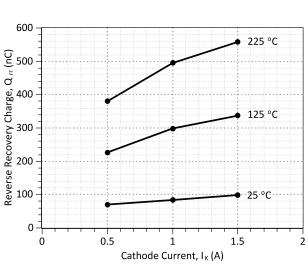


Figure 6: Reverse Recovery Charge vs Cathode Current

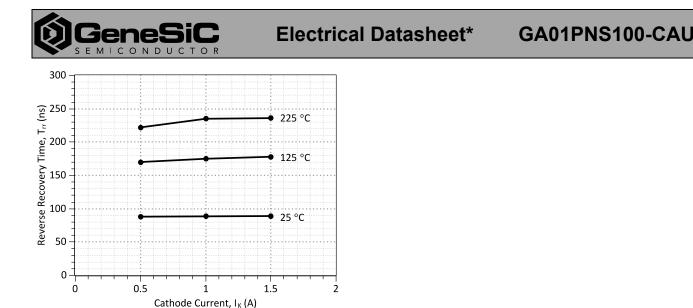


Figure 7: Reverse Recovery Time vs Cathode Current

Revision History						
Date	Revision	Comments	Supersedes			
2012/08/15	0	Initial release				

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