

Digital Transistors (Built-in Resistors)

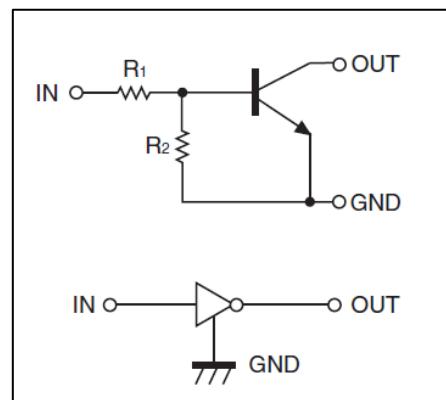
DTC113ZM/DTC113ZE/DTC113ZUA DTC113ZKA /DTC113ZCA/DTC113ZSA

DIGITAL TRANSISTOR (NPN)

FEATURES

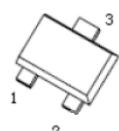
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy

• Equivalent Circuit



PIN CONNECTIONS and MARKING

DTC113ZM

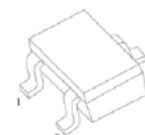


MARKING: E21

SOT-723

1. IN
2. GND
3. OUT

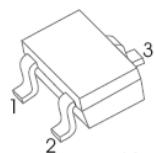
DTC113ZE



SOT-523

1. IN
2. GND
3. OUT

DTC113ZUA

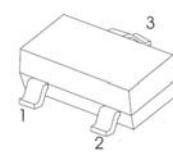


SOT-323

1. IN
2. GND
3. OUT

MARKING: E21

DTC113ZKA

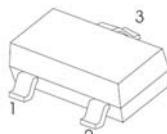


SOT-23-3L

1. IN
2. GND
3. OUT

MARKING: E21

DTC113ZCA



SOT-23

1. IN
2. GND
3. OUT

MARKING: E21

DTC113ZSA



TO-92S

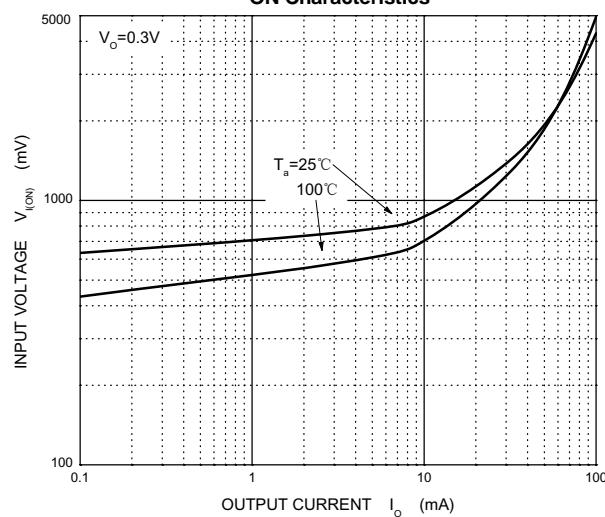
1. GND
2. OUT
3. IN

MAXIMUM RATINGS(Ta=25°C unless otherwise noted)

Symbol	Parameter	Limits(DTC113Z□)						Unit
		M	E	UA	CA	KA	SA	
V _{CC}	Supply Voltage	50						V
V _{IN}	Input Voltage	-5~+10						V
I _O	Output Current	100						mA
P _D	Power Dissipation	100	150	200	200	200	300	mW
T _j	Junction Temperature	150						°C
T _{stg}	Storage Temperature	-55~+150						°C

ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Input voltage	V _{I(off)}	V _{CC} =5V,I _O =100μA	0.3			V
	V _{I(on)}	V _O =0.3V,I _O =20mA			3	V
Output voltage	V _{O(on)}	I _O /I _I =10mA/0.5mA			0.3	V
Input current	I _I	V _I =5V			7.2	mA
Output current	I _{O(off)}	V _{CC} =50V,V _I =0			0.5	μA
DC current gain	G _I	V _O =5V,I _O =5mA	33			
Input resistance	R ₁		0.7	1	1.3	kΩ
Resistance ratio	R _{2/R₁}		8	10	12	
Transition frequency	f _T	V _O =10V ,I _O =5mA,f=100MHz		250		MHz

ON Characteristics**OFF Characteristics**