

March 2013

# FGH80N60FD2 600 V Field Stop IGBT

## **Features**

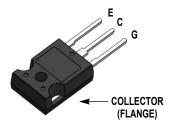
- · High Current Capability
- Low Saturation Coltage:  $V_{CE(sat)}$  = 1.8 V @  $I_C$  = 40 A
- · High Input Impedance
- · Fast Switching
- · RoHS Compliant

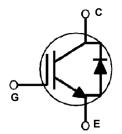
## **Applications**

· Induction Heating, PFC

# **General Description**

Using novel field stop IGBT technology, Fairchild  $^{\otimes}$  s field stop IGBTs offer the optimum performance for induction heating and PFC applications where low conduction and switching losses are essential.





# **Absolute Maximum Ratings**

Symbol	Description		Ratings	Unit
V <sub>CES</sub>	Collector-Emitter Voltage		600	V
$V_{GES}$	Gate-Emitter Voltage		± 20	V
I <sub>C</sub>	Collector Current	@ T <sub>C</sub> = 25°C	80	Α
	Collector Current	@ T <sub>C</sub> = 100°C	40	Α
I <sub>CM (1)</sub>	Pulsed Collector Current	@ T <sub>C</sub> = 25°C	160	Α
P <sub>D</sub>	Maximum Power Dissipation	@ T <sub>C</sub> = 25°C	290	W
. 0	Maximum Power Dissipation	@ T <sub>C</sub> = 100°C	116	W
T <sub>J</sub>	Operating Junction Temperature		-55 to +150	°C
T <sub>stg</sub>	Storage Temperature Range		-55 to +150	°C
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C

## Notes

(1) Repetitive rating: Pulse width limited by max. junction temperature

## **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction-to-Case		0.43	°C/W
$R_{\theta JC}(Diode)$	Thermal Resistance, Junction-to-Case		1.45	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		40	°C/W

# **Package Marking and Ordering Information**

			Packaging		Max Qty
Device Marking	Device	Package	Туре	Qty per Tube	per Box
FGH80N60FD2	FGH80N60FD2TU	TO-247	Tube	30ea	-

# Electrical Characteristics of the IGBT $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	teristics					
BV <sub>CES</sub>	Collector-Emitter Breakdown Voltage	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 250 uA	600			V
ΔBV <sub>CES</sub> / ΔΤ <sub>J</sub>	Temperature Coefficient of Breakdown Voltage	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 250 uA		0.6		V/°C
I <sub>CES</sub>	Collector Cut-Off Current	V <sub>CE</sub> = V <sub>CES</sub> , V <sub>GE</sub> = 0 V			250	uA
I <sub>GES</sub>	G-E Leakage Current	V <sub>GE</sub> = V <sub>GES</sub> , V <sub>CE</sub> = 0 V			±400	nA
On Charac	teristics		·			
V <sub>GE(th)</sub>	G-E Threshold Voltage	I <sub>C</sub> = 250 uA, V <sub>CE</sub> = V <sub>GE</sub>	4.5	5.5	7.0	V
()		I <sub>C</sub> = 40 A, V <sub>GE</sub> = 15 V		1.8	2.4	V
V <sub>CE(sat)</sub>	Collector to Emitter Saturation Voltage	I <sub>C</sub> = 40 A, V <sub>GE</sub> = 15 V, T <sub>C</sub> = 125°C		2.05		V
Dynamic C	haracteristics					
C <sub>ies</sub>	Input Capacitance			2110		pF
C <sub>oes</sub>	Output Capacitance	$V_{CE} = 30 \text{ V}, V_{GE} = 0 \text{ V},$ f = 1  MHz		200		pF
C <sub>res</sub>	Reverse Transfer Capacitance	- 1 - 1 IVITIZ		60		pF
Switching	Characteristics					
$t_{d(on)}$	Turn-On Delay Time			21		ns
t <sub>r</sub>	Rise Time			56		ns
$t_{d(off)}$	Turn-Off Delay Time	$V_{CC} = 400 \text{ V}, I_{C} = 40 \text{ A},$ $R_{G} = 10 \Omega, V_{GE} = 15 \text{ V},$		126		ns
t <sub>f</sub>	Fall Time			50	100	ns
E <sub>on</sub>	Turn-On Switching Loss	Inductive Load, T <sub>C</sub> = 25°C		1	1.5	mJ
E <sub>off</sub>	Turn-Off Switching Loss			0.52	0.78	mJ
E <sub>ts</sub>	Total Switching Loss			1.52	2.28	mJ
t <sub>d(on)</sub>	Turn-On Delay Time			20		ns
t <sub>r</sub>	Rise Time			54		ns
t <sub>d(off)</sub>	Turn-Off Delay Time	V <sub>CC</sub> = 400 V, I <sub>C</sub> = 40 A,		131		ns
t <sub>f</sub>	Fall Time	$R_G = 10 \Omega$ , $V_{GE} = 15 V$ ,		70		ns
E <sub>on</sub>	Turn-On Switching Loss	Inductive Load, T <sub>C</sub> = 125°C		1.1		mJ
E <sub>off</sub>	Turn-Off Switching Loss			0.78		mJ
E <sub>ts</sub>	Total Switching Loss			1.88		mJ
Qg	Total Gate Charge			120		nC
Q <sub>ge</sub>	Gate-Emitter Charge	V <sub>CE</sub> = 400 V, I <sub>C</sub> = 40 A, V <sub>GE</sub> = 15 V		14		nC
Q <sub>gc</sub>	Gate-Collector Charge	] *GE = 13 *		58		nC

# Electrical Characteristics of the Diode $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Condition	าร	Min.	Тур.	Max	Unit
V <sub>FM</sub>	Diode Forward Voltage	I <sub>F</sub> = 15 A	T <sub>C</sub> = 25°C	-	1.2	1.5	V
FIM	Diodo i orwara voltago		T <sub>C</sub> = 125°C	-	1.0	-	
t <sub>rr</sub>	Diode Reverse Recovery Time		T <sub>C</sub> = 25°C	-	61	-	ns
1	"	l <sub>ES</sub> = 15 A, dl <sub>ES</sub> / dt = 200 A/μs	T <sub>C</sub> = 125°C	-	125	-	
Irr	Diode Reverse Recovery Current		T <sub>C</sub> = 25°C	-	4.8	-	ns
'''			T <sub>C</sub> = 125°C	-	8.4	-	
Q <sub>rr</sub>	Diode Reverse Recovery Charge		T <sub>C</sub> = 25°C	-	146	-	nC
	2.535 . to 5.55 . to 60 voly officing		T <sub>C</sub> = 125°C	-	525	-	

# **Typical Performance Characteristics**

**Figure 1. Typical Output Characteristics** 

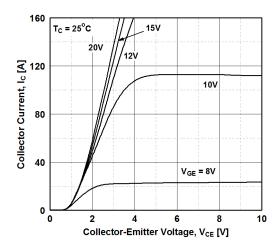


Figure 3. Typical Saturation Voltage Characteritics

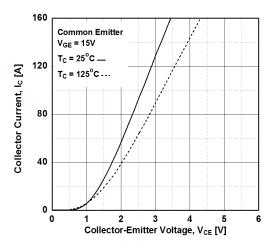


Figure 5. Saturation Voltage vs. Case

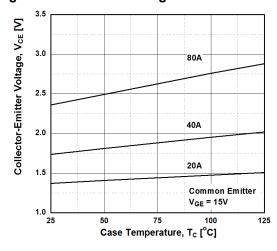


Figure 2. Typical Saturation Voltage Characteristics

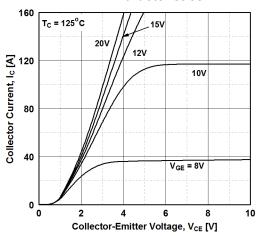


Figure 4. Transfer Characteristics

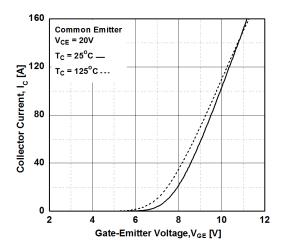
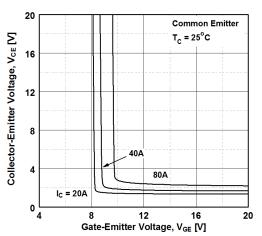


Figure 6. Saturation Voltage vs. Vge



# Typical Performance Characteristics (Continued)

Figure 7. Saturation Voltage vs. Vge

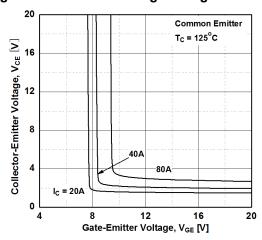


Figure 8. Capacitance Characteristics

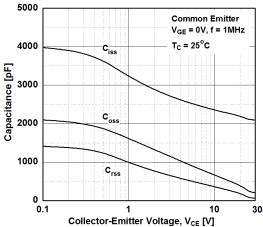


Figure 9. Gate Charge Characteristics

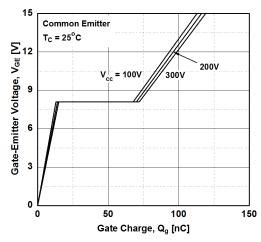


Figure 10. SOA Characteeristics

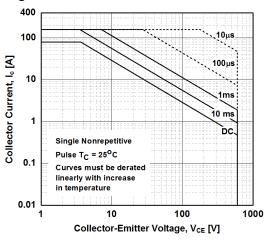


Figure 11. Turn-Off Switching SOA Characteristics

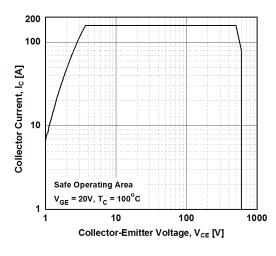
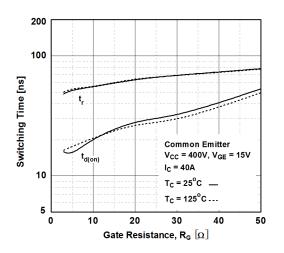


Figure 12. Turn-On Characteristics vs.
Gate Resistance



# Typical Performance Characteristics (Continued)

Figure 13. Turn-Off Characteristics vs. Gate Resistance

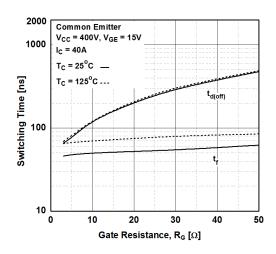


Figure 15. Turn-Off Characteristics vs. Collector Current

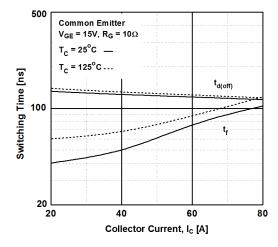


Figure 14. Turn-On Characteristics vs. Collector Current

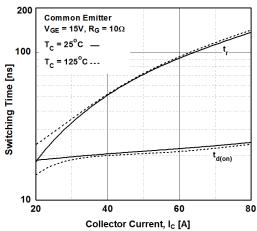


Figure 16. Switching Loss vs Gate Resistance

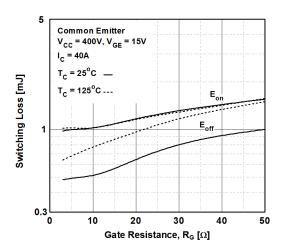
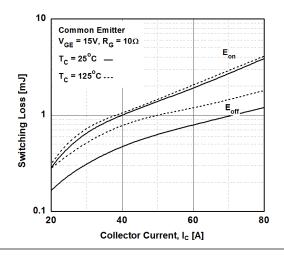


Figure 17. Switching Loss vs Collector Current



# **Typical Performance Characteristics** (Continued)

Figure 18. Transient Thermal Impedance of IGBT

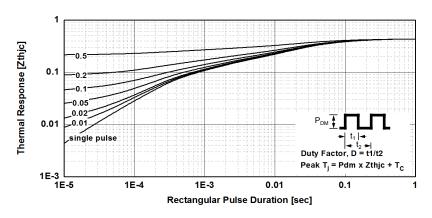


Figure 19. Typical Forward Voltage Drop

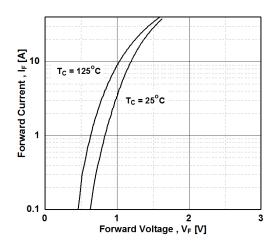


Figure 20. Stored Charge

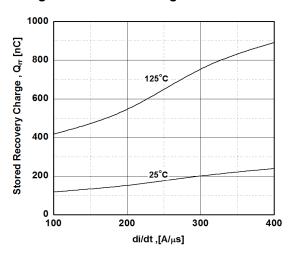


Figure 21. Reverse Recovery Time

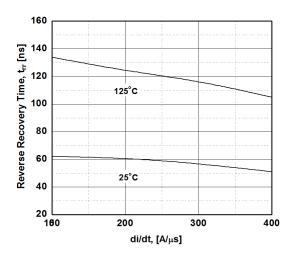
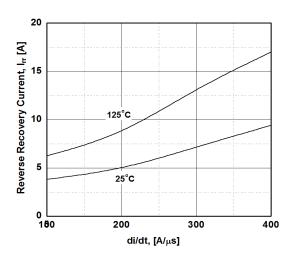
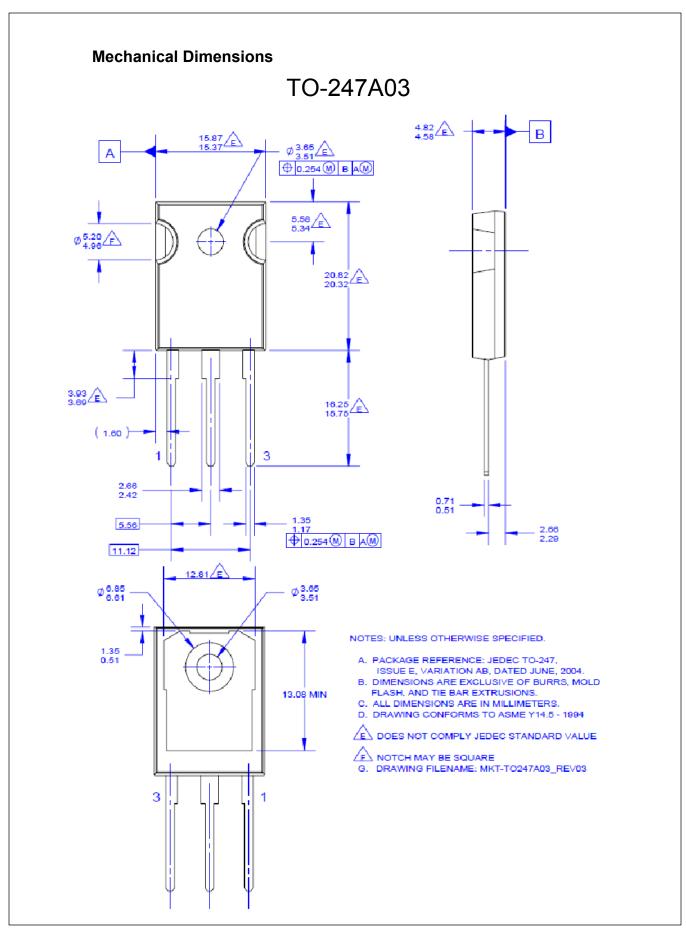


Figure 22. Reverse Recovery Current









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