

MGBR20V60C Preliminary DIODE

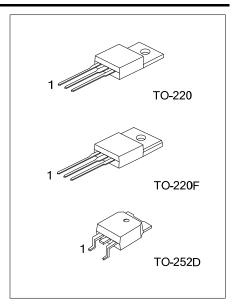
DUAL MOS GATED BARRIER RECTIFIERS

DESCRIPTION

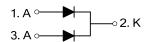
The UTC **MGBR20V60C** is a dual mos gated barrier rectifiers, it uses UT C's advanc ed techn ology to prov ide customers with hi gh current capability, low forward voltage and high switching speed, etc.

■ FEATURES

- * Very low forward voltage
- * High switching speed
- * High current capability

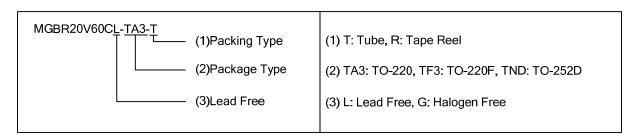


■ SYMBOL



■ ORDERING INFORMATION

Ordering Number		Doolsono	Pin Assignment			Dealing		
Lead Free	Halogen Free		Package	1	2	3	Packing	
MGBR20V60CL-TA3-T M	GBR20V6	0CG-TA3-T	TO-220	Α	K	Α	Tube	
MGBR20V60CL-TF3-T M	GBR20V6	0CG-TF3-T	TO-220F	Α	K	Α	Tube	
MGBR20V60CL-TND-T N	IGBR20V6	0CG-TND-T	TO-252D	Α	K	Α	Tube	
MGBR20V60CL-TND-R N	IGBR20V6	0CG-TND-R	TO-252D	Α	K	Α	Tape Reel	



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■ **ABSOLUTE MAXIMUM RATINGS** (T_A=25°C unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
DC Blocking Voltage		V_{RM}	60	V
Working Peak Reverse Voltage		V_{RWM}	60	V
Peak Repetitive Reverse Voltage		V_{RRM}	60	V
Average Rectified Forward Current	Per Leg		10	Α
	Total	I _O	20	Α
Peak Forward Surge Current		I _{FSM}	180	Α
Operating Junction Temperature		T _J	-65~+150	°C
Storage Temperature		T _{STG}	-65~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT	
Landing to Applicat	TO-220/TO-220F	0	62.5	°C/\\/	
Junction to Ambient	TO-252D	$ heta_{JA}$	2.5	°C/W	
lunation to Coop	TO-220F	0	3.31	°C/W	
Junction to Case	TO-220/TO-252D	θιс	2		

■ **ELECTRICAL CHARACTERISTICS** (T_A =25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I _R =0.60mA 60				V
Instantaneous Forward Voltage	V _{FM}	I _F =5A, T _J =25°C		0.44		V
		I _F =5A, T _J =125°C		0.39		V
		I _F =10A, T _J =25°C			0.59	V
		I _F =10A, T _J =125°C			0.54	V
Instantaneous Reverse Current (Note 1)	I DM	V _{RM} =60V, T _J =25°C			500	μΑ
		V _{RM} =60V, T _J =125°C			20	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

^{2.} Thermal resistance junction to case mounted on heatsink.

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