

#### Continental Device India Limited

An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company





### NPN SILICON HIGH SPEED SWITHCHING TRANSISTORS

P2N2369



TO-92 Plastic Package

#### LOW POWER FOR HIGH SPEED SWITCHING APPLICATIONS

ABSOLUTE MAXIMUM RATINGS (Ta=25°C unless specified otherwise)

SYMBOL	VALUE	UNIT
V <sub>CEO</sub>	15	V
V <sub>CBO</sub>	40	V
V <sub>CES</sub>	40	V
V <sub>EBO</sub>	4.5	V
I <sub>CM</sub>	500	mA
P <sub>D</sub>	625	mW
$T_{j},T_{stg}$	-55 to +150	°C
R <sub>th(j-a)</sub>	200	°C/W
	V <sub>CEO</sub> V <sub>CBO</sub> V <sub>CES</sub> V <sub>EBO</sub> I <sub>CM</sub> P <sub>D</sub> T <sub>j</sub> , T <sub>stg</sub>	V <sub>CEO</sub> 15 V <sub>CBO</sub> 40 V <sub>CES</sub> 40 V <sub>EBO</sub> 4.5 I <sub>CM</sub> 500 P <sub>D</sub> 625 T <sub>j</sub> , T <sub>stg</sub> -55 to +150



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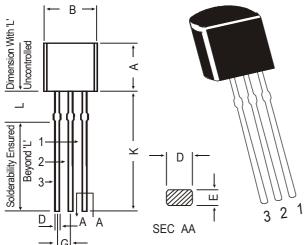
## ELECTRICAL CHARACTERISTICS (Ta=25°C unless specified otherwise)

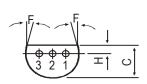
DESCRIPTION	CVMDOL	TECT CONDITION	VA	LINUT		
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	MAX	UNIT	
Collector Emitter Breakdown Voltage	BV <sub>CEO</sub>	$I_C=10$ mA, $I_B=0$	15		V	
Collector Emitter Breakdown Voltage	BV <sub>CES</sub>	$I_C=10\mu A, V_{BE}=0$	40		V	
Collector Base Breakdown Voltage	BV <sub>CBO</sub>	$I_{C}=10\mu A, I_{E}=0$	40		V	
Emitter Base Breakdown Voltage	BV <sub>EBO</sub>	$I_E=10\mu A, I_C=0$	4.5		V	
Collector Leakage Current	I <sub>CBO</sub>	$V_{CB}$ =20V, $I_E$ =0		400	nA	
Collector Leakage Current	I <sub>CBO</sub>	V <sub>CB</sub> =20V, Ta=125°C		30	μΑ	
Collector Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =10mA, I <sub>B</sub> =1mA		0.25	V	
Base Emitter Saturation Voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =10mA, I <sub>B</sub> =1mA	0.7	0.85	V	
DC Current Gain	h <sub>FE</sub>	I <sub>C</sub> =10mA, V <sub>CE</sub> =1V	40	120		
		I <sub>C</sub> =100mA, V <sub>CE</sub> =2V*	20			
		I <sub>C</sub> =10mA, V <sub>CE</sub> =1V,	20			
		Ta= -55°C				
DYNAMIC CHARACTERISTICS						
Output Capacitance	C <sub>c</sub>	I <sub>E</sub> =0, V <sub>CB</sub> =5V		4	pF	
		f=140KHz				
Small Signal Current Gain	h <sub>fe</sub>	V <sub>CE</sub> =10V,I <sub>C</sub> =10mA	5		MHz	
Sinan Signal Current Gain	I ''fe I	f=100MH <sub>Z</sub>	3		IVII IZ	
SWITCHING CHARCTERISTICS						
Turn on Time	t <sub>on</sub>	I <sub>C</sub> =10mA, I <sub>B1</sub> =3mA,		12	ns	
		V <sub>CC</sub> =3V				
Turn off Time	t <sub>off</sub>	I <sub>C</sub> =10mA, I <sub>B1</sub> =3mA,		18	ns	
		V <sub>CC</sub> =3V,I <sub>B2</sub> =1.5mA				
Storage Time	t <sub>s</sub>	I <sub>C</sub> =10mA, I <sub>B1</sub> =10mA= I <sub>B2</sub>		13	ns	

<sup>\*</sup>Pulse Condition: Length  $\leq$  300ms, Duty Cycle  $\leq$  2%.

### **TO-92 Plastic Package**

#### **TO-92 Transistors on Tape and Ammo Pack TO-92 Transistors on Tape and Ammo Pack**



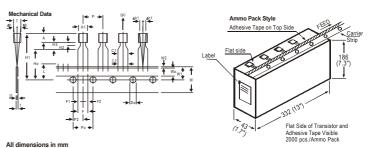


#### PIN CONFIGURATION

- 1. EMITTER
- 2. BASE
- 3. COLLECTOR

DIM	MIN.	MAX.				
Α	4.32	5.33				
В	4.45	5.20				
С	3.18	4.19				
D	0.41	0.55				
Е	0.35	0.50				
F	5 DEG					
G	1.14	1.40				
Н	1.14	1.53				
K	12.70	_				
L	1.982	2.082				

All diminsions in mm.



		SPECIFICATION			ION	
ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL.	REMARKS
BODY WIDTH	A1	4.0		4.8		
BODY HEIGHT	A	4.8		5.2		
BODY THICKNESS	T	3.9		4.2		
PITCH OF COMPONENT	Р		12.7		± 1.0	
FEED HOLE PITCH	Po		12.7		± 0.3	CUMULATIVE PITCH ERROR 1.0 mm/20 PITCH
FEED HOLE CENTRE TO						
COMPONENT CENTRE	P2		6.35		± 0.4	TO BE MEASURED AT BOTTOM OF CLINCH
DISTANCE BETWEEN OUTER						
LEADS	F		5.08		+ 0.6 - 0.2	
COMPONENT ALIGNMENT SIDE VIEW	Δh		0	1.0	- 0.2	AT TOP OF BODY
COMPONENT ALIGNMENT FRONT VIEW	∆h1		0	1.3		AT TOP OF BODY
TAPE WIDTH	w		18		± 0.5	
HOLD-DOWN TAPE WIDTH	Wo		6		+02	
HOLE POSITION	W1		9		+ 0.7	
			-		- 0.5	
HOLD-DOWN TAPE POSITION	W2		0.5		± 0.2	
LEAD WIRE CLINCH HEIGHT	Но		16		± 0.5	
COMPONENT HEIGHT	H1			23.25		
LENGTH OF SNIPPED LEADS	L			11.0		
FEED HOLE DIAMETER	Do		4		± 0.2	
TOTAL TAPE THICKNESS	t			1.2		t1 0.3-0.6
LEAD - TO - LEAD DISTANCE	F1, F2		2.54		+ 0.4	
STAND OFF	H2	0.45		1.45	- 0.1	
CLINCH HEIGHT	H2 H3	0.45		3.0		
LEAD PARALLELISM				0.22		
	C1 - C2	6N		0.22		
PULL - OUT FORCE	(P)	Nσ				

- NOTES

  1. Maximum alignment deviation between leads will not to be greater than 0.2mm.

  2. Maximum non-cumulative variation between tape feed holes shall not exceed 1 mm in 20 pitches.

  3. Holddown tape will not exceed beyond the edge(s) of carrier tape and there shall be no exposure of adhesive.

  4. There will be no more than three (3) consecutive missing components in a tape.

  5. A tape traiter, having at least three feed holes are provided after the last component in a tape.

  6. Splices should not interfere with the sprocket feed holes.

# **Packing Detail**

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight /Qty	Size	Qty	Size	Qty	Gr Wt
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs

Notes P2N2369

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#### **Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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