





SOT-23 Formed SMD Package

BCX71G BCX71H BCX71J BCX71K

SILICON PLANAR EPITAXIAL TRANSISTORS

P-N-P silicon transistors

Marking

BCX71G = BG

BCX71H = BH

BCX71J = BJ

BCX71K = BK

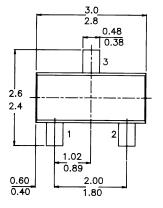
Pin configuration

1 = BASE

2 = EMITTER

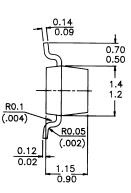
3 = COLLECTOR





PACKAGE OUTLINE DETAILS

ALL DIMENSIONS IN mm



ABSOLUTE MAXIMUM RATINGS

Collector-emitter voltage $(V_{RF} = 0)$	$-V_{CES}$	max.	45 V
Collector-emitter voltage (open base)	-V _{CE0}	max.	45 V
Collector current (d.c.)	$-I_C$	max.	200 mA
Total power dissipation	P_{tot}	max.	<i>250</i> mW
Junction temperature	T_i	max.	150 ° C
Transition frequency at $f = 100 \text{ MHz}$	J		
$-V_{CE} = 5 V$; $-I_{C} = 10 \text{ mA}$	f_T	typ.	180 MHz
Noise figure at $f = 1$ kHz			
$-V_{CE} = 5V; -I_{C} = 200 \text{mA}$	F	typ.	2 dB

RATINGS (at $T_A = 25^{\circ}C$ unless otherwise specified)

Limiting values

Collector-emitter voltage ($V_{BE} = 0$) $-V_{CES}$ max. 45 V Collector-emitter voltage (open base) $-V_{CE0}$ max. 45 V Emitter-base voltage (open collector) $-V_{EB0}$ max. 5 V

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Collector current (d.c.)			-	$-I_C$	max.	200	mΑ
Base current			-	$-l_B$	max.	<i>50</i>	mΑ
Total power dissipation up to $T_{amb} = 2$	5 °C		j	P_{tot}	max.	<i>250</i>	mW
Storage temperature				T_{stg}	−55 to	+15	0 °C
Junction temperature				T_j	max.	150	° C
THERMAL RESISTANCE							
From junction to ambient			i	R _{th j-a}	=	500	KW
-				ar j u			
CHARACTERISTICS	,						
$T_{amb} = 25$ °C unless otherwise specified	1						
Collector-emitter cut-off current						0.0	
$V_{EB} = 0$; $-V_{CE} = 45 \text{ V}$	00			-I _{CES}	<		nA
$V_{EB} = 0$; $-V_{CE} = 45$ V; $T_{amb} = 150$	·C		-	-I _{CES}	<	20	$\mathfrak{m}A$
Emitter-base cut-off current				T		0.0	. 4
IC = 0; $-VEB = 4 V$			-	-I _{EB0}	<	20	nΑ
Saturation voltages			-	-V _{CEsat}	0,06 to	0.25	V
$-I_C = 10 \text{ mA}; -I_B = 0.25 \text{ mA}$							
			-	-V _{BEsat}	0,0 10	0,00	V
T 70 A 1 107 A			-	-V _{CEsat}	0,12 to	o 0,55	V
$-I_C = 50 \text{ mA}; -I_B = 1,25 \text{ mA}$			_	-VRFcat	0,68 to	0 1.05	V
Transition frequency at $f = 100 \text{ MHz}$				DLSat	-,	, , , , , ,	
$-V_{CE} = 5 \text{ V; } -I_{C} = 10 \text{ mA}$			1	f_T	typ.	180	MHz
Capacitance at $f = 1$ MHz			-	1	ijρ.	100	11112
$-V_{CB} = 10 \text{ V; } I_E = I_e = 0$				C_{c}		4,5	рF
Emitter capacitance at $f = 1$ MHz			`	$\mathcal{L}_{\mathcal{C}}$	typ.	1,0	Pι
$-V_{EB} = 0.5 \text{ V; } I_C = I_C = 0$				$C_{m{e}}$	typ.	11	рF
Noise figure at $R_S = 2 \text{ kW}$			`	$\sim e$	ijρ.	- 11	Pι
$-V_{CE} = 5 \text{ V}; -I_{C} = 200 \text{ mA}; B = 200$	Hz			F	typ.	2	dВ
VE UV, IC 200 Max, 2 200	112		1		< /r		dB
		I	3CX71G	7111	71J	71K	
D.C. sussessed states		L	フレスノエい	71H	/13	/1 N	
IJC. CUTTENT GAIN							
=	hee	>	_ [30	40	100	
$-V_{CE} = 5$ V; $-I_{C} = 10$ mA	hFE hEE	>	- 120	30 180	40 250	100 380	
=	hFE hFE	>	120	180	250	380	
$-V_{CE} = 5 V; -I_{C} = 10 \text{mA}$ $-V_{CE} = 5 V; -I_{C} = 2 \text{ mA}$	hFE	> <	120 220	180 310	250 460	380 630	
$-V_{CE} = \bar{5} \ V; -I_{C} = 10 \text{mA}$ $-V_{CE} = 5 \ V; -I_{C} = 2 \ mA$ $-V_{CE} = 1 \ V; -I_{C} = 50 \ mA$		>	120	180	250	380	
$-V_{CE} = \overline{5} \ V; -I_{C} = 10 \text{mA}$ $-V_{CE} = 5 \ V; -I_{C} = 2 \ \text{mA}$ $-V_{CE} = 1 \ V; -I_{C} = 50 \ \text{mA}$ Small-signal current gain	h _{FE}	> < >	120 220 60	180 310 80	250 460 100	380 630 110	
$-V_{CE} = 5$ V; $-I_{C} = 10$ mA $-V_{CE} = 5$ V; $-I_{C} = 2$ mA $-V_{CE} = 1$ V; $-I_{C} = 50$ mA	h _{FE}	> < >	120 220 60 125	180 310 80 175	250 460 100 250	380 630 110 350	
$-V_{CE} = \bar{5} \ V; -I_{C} = 10 \text{mA}$ $-V_{CE} = 5 \ V; -I_{C} = 2 \ \text{mA}$ $-V_{CE} = 1 \ V; -I_{C} = 50 \ \text{mA}$ Small-signal current gain $-V_{CE} = 5 \ V; -I_{C} = 2 \ \text{mA}; f = 1 \ \text{kHz}$	h _{FE}	> < >	120 220 60	180 310 80	250 460 100	380 630 110	
$-V_{CE} = \bar{5} \ V; \ -I_{C} = 10 \text{mA}$ $-V_{CE} = 5 \ V; \ -I_{C} = 2 \ mA$ $-V_{CE} = 1 \ V; \ -I_{C} = 50 \ mA$ Small-signal current gain $-V_{CE} = 5 \ V; \ -I_{C} = 2 \ mA; \ f = 1 \ kHz$ Output admittance	h_{FE} h_{FE} h_{fe}	> < > >	120 220 60 125 250	180 310 80 175 350	250 460 100 250 500	380 630 110 350 700	C
$-V_{CE} = \bar{5} \ V; \ -I_{C} = 10 \text{mA}$ $-V_{CE} = 5 \ V; \ -I_{C} = 2 \ mA$ $-V_{CE} = 1 \ V; \ -I_{C} = 50 \ mA$ $Small-signal \ current \ gain$ $-V_{CE} = 5 \ V; \ -I_{C} = 2 \ mA; \ f = 1 \ kHz$ $Output \ admittance$ $-V_{CE} = 5 \ V; \ -I_{C} = 2 \ mA; \ f = 1 \ kHz$	h_{FE} h_{FE} h_{fe}	> < >	120 220 60 125	180 310 80 175	250 460 100 250	380 630 110 350	m S
$-V_{CE} = \bar{5} \ V; \ -I_{C} = 10 \text{mA}$ $-V_{CE} = 5 \ V; \ -I_{C} = 2 \ mA$ $-V_{CE} = 1 \ V; \ -I_{C} = 50 \ mA$ $Small-signal \ current \ gain$ $-V_{CE} = 5 \ V; \ -I_{C} = 2 \ mA; \ f = 1 \ kHz$ $Output \ admittance$ $-V_{CE} = 5 \ V; \ -I_{C} = 2 \ mA; \ f = 1 \ kHz$ $Base-emitter \ voltage$	h_{FE} h_{FE} h_{fe} h_{fe}	> < > >	120 220 60 125 250	180 310 80 175 350 24	250 460 100 250 500	380 630 110 350 700	
$-V_{CE} = \bar{5} \ V; \ -I_{C} = 10 \text{mA}$ $-V_{CE} = 5 \ V; \ -I_{C} = 2 \ mA$ $-V_{CE} = 1 \ V; \ -I_{C} = 50 \ mA$ $Small-signal \ current \ gain$ $-V_{CE} = 5 \ V; \ -I_{C} = 2 \ mA; \ f = 1 \ kHz$ $Output \ admittance$ $-V_{CE} = 5 \ V; \ -I_{C} = 2 \ mA; \ f = 1 \ kHz$	h_{FE} h_{FE} h_{fe}	>	120 220 60 125 250	180 310 80 175 350 24	250 460 100 250 500 30	380 630 110 350 700	V
$-V_{CE} = \bar{5} \ V; \ -I_{C} = 10 \text{mA}$ $-V_{CE} = 5 \ V; \ -I_{C} = 2 \ mA$ $-V_{CE} = 1 \ V; \ -I_{C} = 50 \ mA$ $Small-signal \ current \ gain$ $-V_{CE} = 5 \ V; \ -I_{C} = 2 \ mA; \ f = 1 \ kHz$ $Output \ admittance$ $-V_{CE} = 5 \ V; \ -I_{C} = 2 \ mA; \ f = 1 \ kHz$ $Base-emitter \ voltage$	h_{FE} h_{FE} h_{fe} h_{fe}	> < > >	120 220 60 125 250	180 310 80 175 350 24	250 460 100 250 500	380 630 110 350 700	
$-V_{CE} = 5$ V ; $-I_{C} = 2$ mA $-V_{CE} = 1$ V ; $-I_{C} = 50$ mA Small-signal current gain $-V_{CE} = 5$ V ; $-I_{C} = 2$ mA ; $f = 1$ kHz Output admittance $-V_{CE} = 5$ V ; $-I_{C} = 2$ mA ; $f = 1$ kHz Base-emitter voltage	h_{FE} h_{FE} h_{fe} h_{fe}	>	120 220 60 125 250	180 310 80 175 350 24	250 460 100 250 500 30	380 630 110 350 700	V

Notes

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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C-120 Naraina Industrial Area, New Delhi 110 028, India.

Telephone + 91-11-579 6150 Fax + 91-11-579 9569, 579 5290
e-mail sales@cdil.com www.cdil.com