

Technical Data

NTH / NCH Series



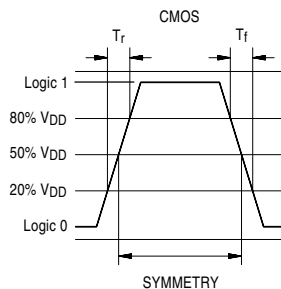
Description

A crystal controlled, low current oscillator providing precise rise and fall times to drive High Speed HCMOS and NMOS microprocessors. The tri-state function on the NTH enables the output to go high impedance. Device is packaged in a 14 or an 8-pin DIP compatible resistance welded, all metal grounded case, to reduce EMI.

Applications & Features

- Clock 16 and 32 bit microprocessors
- Tri-State output on NTH
- HCMOS compatible
- Available up to 70 MHz
- Grounded, all metal, full size or half size case
- Plastic SMD available, see separate data sheet

Output Waveform

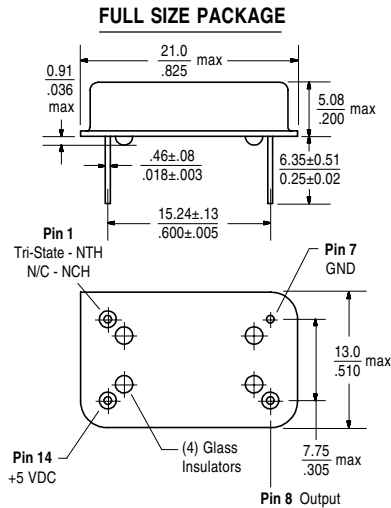


Frequency Range:	1 MHz to 70 MHz
Frequency Stability:	±25, ±50 or ±100 ppm over all conditions: calibration tolerance, operating temperature, input voltage change, load change, aging, shock and vibration.
Temperature Range:	Operating: 0 to +70°C or -40 to +85°C, See Part Numbering Guide Storage: -55 to +125°C
Supply Voltage:	Recommended Operating: 3.3V ±10%
Supply Current:	Frequency: 1 to 27 MHz 15mA max 27+ to 70 MHz 25mA max
Output Drive:	<u>HCMOS</u> Symmetry: 40/60% or 45/55% max @ 50% VDD, See Part Numbering Guide Rise and Fall Times: 4ns max 20% to 80% VDD Logic 0: 10% VDD max Logic 1: 90% VDD min Load: 30 pF Jitter: 8ps max RMS period jitter, 1ps max 1σ cycle-to-cycle jitter
Mechanical:	Shock: MIL-STD-883, Method 2002, Condition B Solderability: MIL-STD-883, Method 2003 Terminal Strength: MIL-STD-202, Method 211, Conditions A & C Vibration: MIL-STD-883, Method 2007, Condition A Solvent Resistance: MIL-STD-202, Method 215 Resistance to Soldering Heat: MIL-STD-202, Method 210, Condition A, B or C
Environmental:	Gross Leak Test: MIL-STD-883, Method 1014, Condition C Fine Leak Test: MIL-STD-883, Method 1014, Condition A2 Thermal Shock: MIL-STD-883, Method 1011, Condition A Moisture Resistance: MIL-STD-883, Method 1004

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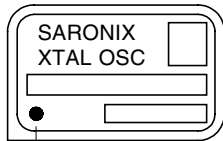
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Package Details

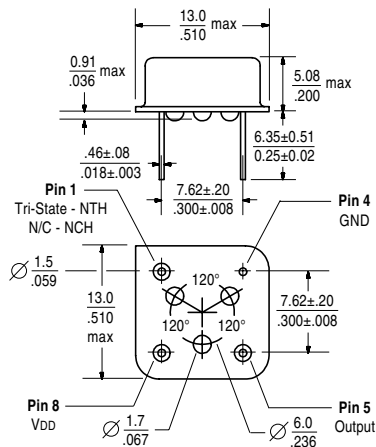


Standard Marking Format**

Includes Date Code, Frequency & Model



HALF SIZE PACKAGE



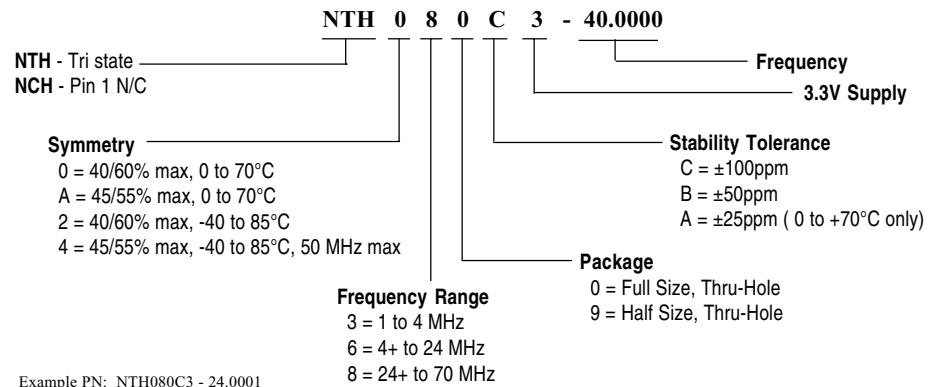
Standard Marking Format**

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**Exact location of items may vary

Part Numbering Guide



Tri-State Logic Table (NTH only)

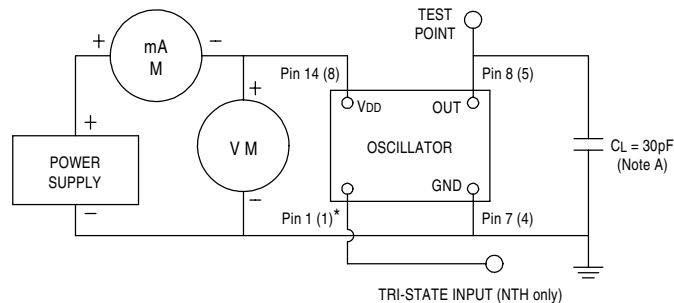
Pin 1 Input	Pin 8 (5) Output
Logic 1 or NC	Oscillation
Logic 0 or GND	High Impedance

Required Input Levels on Pin 1:

Logic 1 = 2.2V min

Logic 0 = 0.8V max

Test Circuit



NOTE A: C_L includes probe and fixture capacitance

* () Indicates pin numbers for half-size package

All specifications are subject to change without notice.