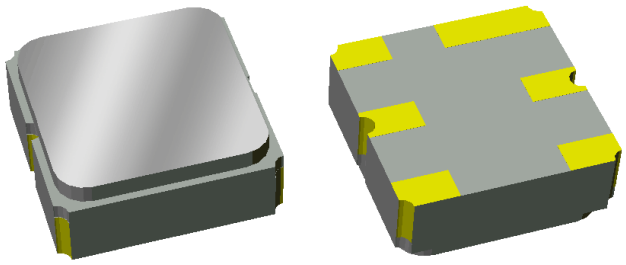
TriQuint SEMICONDUCTOR

Data Sheet

Part Number 856039
1575.42 MHz SAW Filter

Features

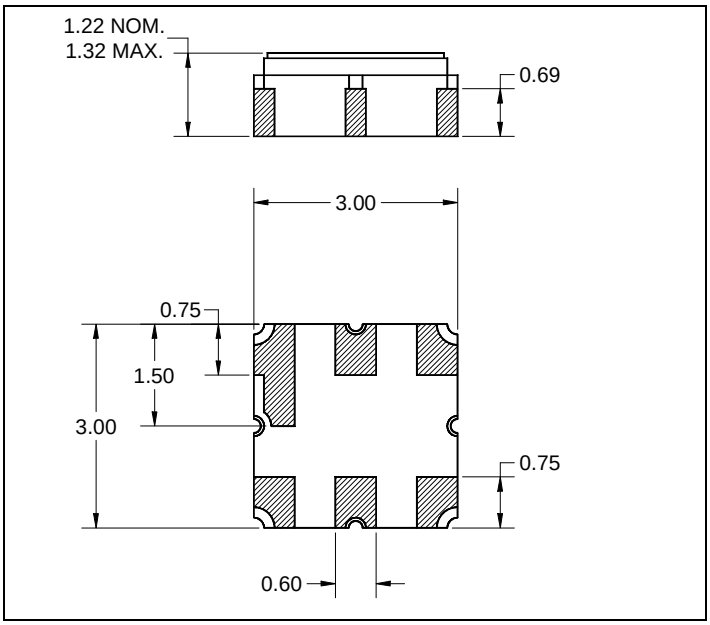
- For GPS applications
- Usable bandwidth 2 MHz
- High attenuation
- No impedance matching required for operation at 50 Ω
- Single-ended operation
- Ceramic Surface Mount Package (SMP)
- Hermetic
- Qualified for Automotive Applications
- Manufacturing facilities are certified with ISO/TS 16949:2002
- RoHS compliant (2002/95/EC), Pb-free



Package

Surface Mount 3.00 x 3.00 x 1.22 mm

SMP-12

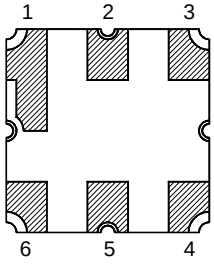


Dimensions shown are nominal in millimeters
All tolerances are ±0.15mm except overall length and width ±0.10mm

Body: Al_2O_3 ceramic
Lid: *Kovar*, *Ni* plated
Terminations: *Au* plating 0.5 - 1.0µm, over a 2 - 6µm *Ni* plating

Pin Configuration

Bottom View



Pin No.	Description
2	Input
5	Output
1,3,4,6	Case Ground

Electrical Specifications [1]

Operating Temperature Range: [2] -40 to +85 °C

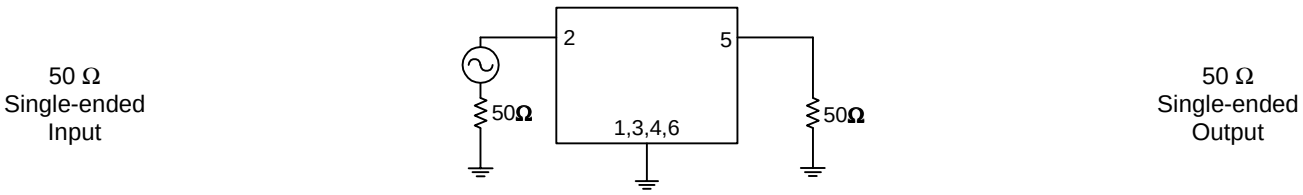
Parameter [3]	Minimum	Typical [4]	Maximum	Unit
Center Frequency	-	1575.42	-	MHz
Insertion Loss				
1574.42 - 1576.42 MHz	-	1.8	3.5	dB
Absolute rejection				
847.50 - 852.50 MHz	45	48	-	dB
1497.50 - 1502.50 MHz	40	43	-	dB
1532.92 - 1537.92 MHz	20	38	-	dB
1612.92 - 1617.92 MHz	20	33	-	dB
1637.50 - 1642.50 MHz	45	65	-	dB
1697.50 - 1702.50 MHz	50	54	-	dB
Input/Output VSWR	-	1.4:1	2.0:1	-
Passband Ripple	-	0.15	1	dB p-p
Source Impedance: [5]	-	50	-	Ω
Load Impedance: [5]	-	50	-	Ω

Notes:

1. All specifications are based on the test circuit shown below
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. Typical values are based on average measurements at room temperature
5. This is the optimum impedance in order to achieve the performance shown

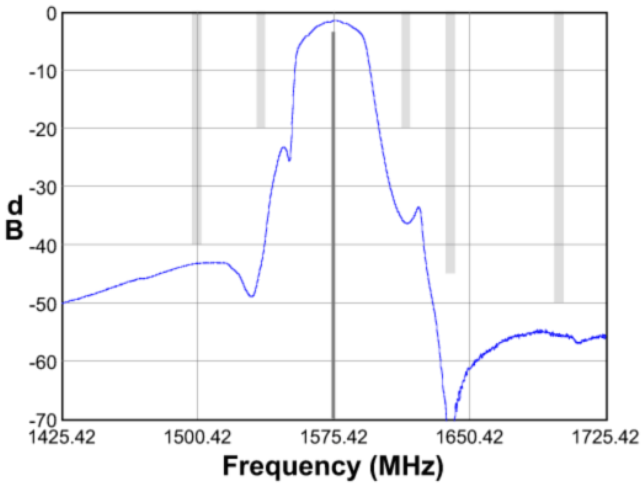
Test Circuit:

Actual matching values may vary due to PCB layout and parasitics

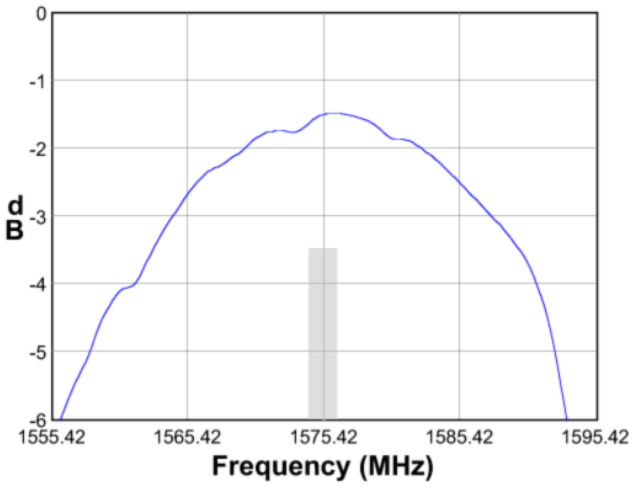


Typical Performance (at +25°C)

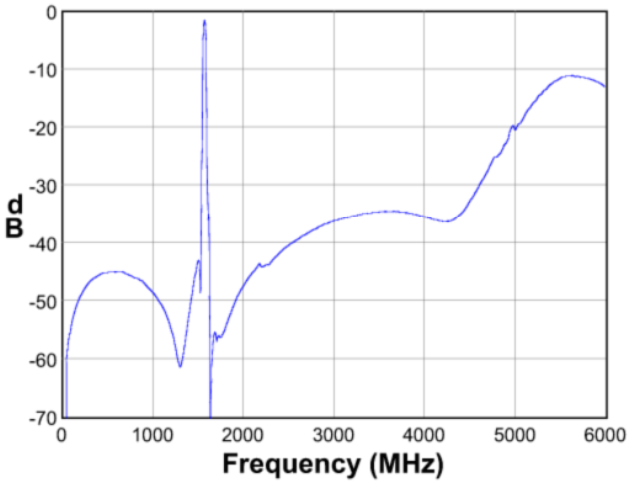
Frequency Response



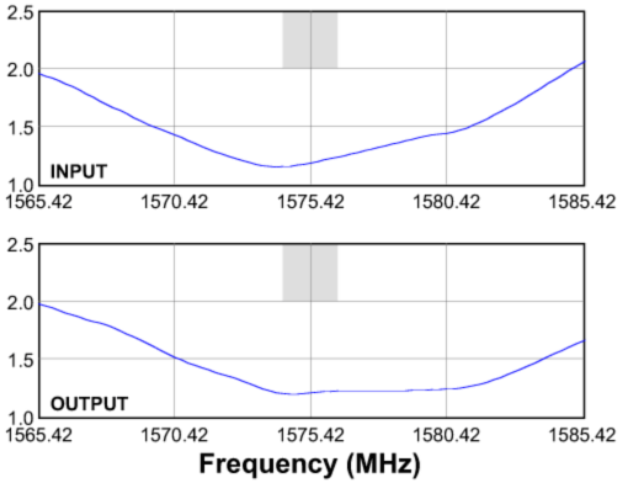
Passband Response



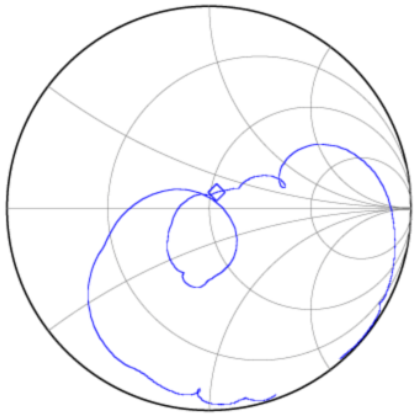
Wideband Response



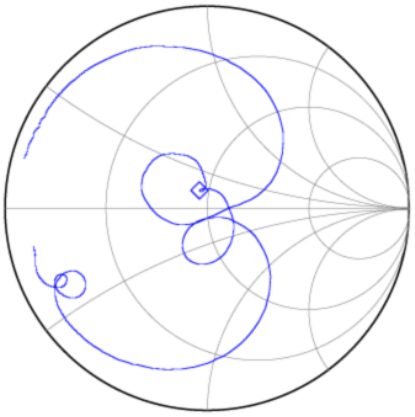
Input/Output VSWR



Input Smith Chart



Output Smith Chart



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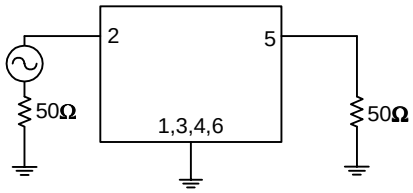
Matching Schematics

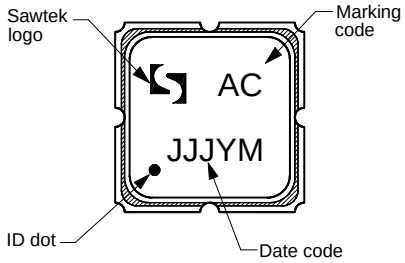
Actual matching values may vary due to PCB layout and parasitics

50 Ω
Single-ended
Input



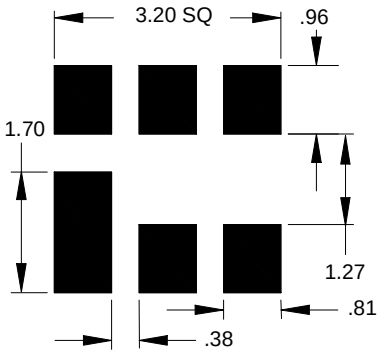
50 Ω
Single-ended
Output

Marking



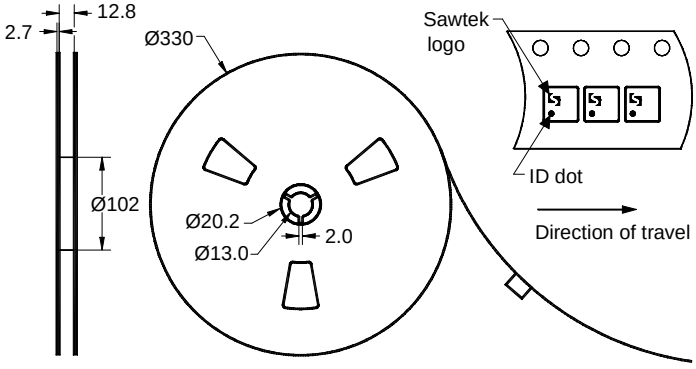
The date code consists of: JJJ = Julian day,
Y = last digit of year, M = manufacturing site code

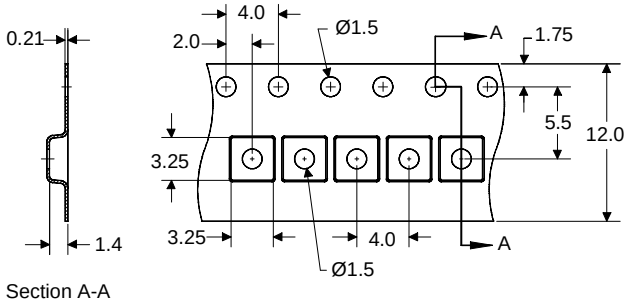
PCB Footprint



This footprint represents a recommendation only
Dimensions shown are nominal in millimeters

Tape and Reel





Dimensions shown are nominal in millimeters
Packaging quantity: 5000 units/reel

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Maximum Ratings

Parameter	Symbol	Minimum	Maximum	Unit
Operating Temperature Range	T	-40	+85	^{o}C
Storage Temperature Range	T_{stg}	-40	+105	^{o}C
RF Power	P_{in}	-	+10	dBm

Important Notes

Warnings

- Electrostatic Sensitive Device (ESD)
- Avoid ultrasonic exposure

RoHS Compliance

- This product complies with EU directive 2002/95/EC (RoHS)

Solderability

- Compatible with JEDEC J-STD-020C **Pb**-free process, **260℃** peak reflow temperature (see soldering profile)

Links to Additional Technical Information

PCB Layout Tips | Qualification Flowchart | Soldering Profile

S-Parameters | RoHS Information | Other Technical Information

Contact Information

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