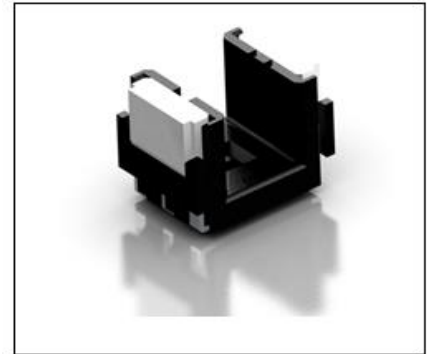


## Photo Interrupter

## KIT3032S

### Description

The KIT3032S is a compact transmission type photo interrupter, which combines high-output GaAs IRED with high sensitive dual photo transistors.



### Features

- Surface mount package
- Slit : 0.3mm (Channel Distance : 0.8mm).
- Moisture Sensitive Level(MSL)2a.
- GAP : 3.0mm.
- RoHS Compliance.



### Applications

- Motor Control.
- Position Encoder.
- Printers.
- Ticket Vending Machines.

### Absolute Maximum Ratings (T<sub>a</sub>=25°C, Unless otherwise specified)

Characteristic		Symbol	Ratings	Unit
Input LED	Power Dissipation	P <sub>D</sub>	75	mW
	Forward Current	I <sub>F</sub>	50	mA
	Reverse Voltage	V <sub>R</sub>	6	V
	Pulse Forward Current *1	I <sub>FP</sub>	0.5	A
Output Detector	Collector Dissipation	P <sub>C</sub>	75	mW
	Collector Current	I <sub>C</sub>	20	mA
	C-E Voltage	V <sub>CEO</sub>	30	V
	E-C Voltage	V <sub>ECO</sub>	7	V
Operating Temperature *2		T <sub>opr.</sub>	-40 ~ +105	°C
Storage Temperature *2		T <sub>stg.</sub>	-40 ~ +105	°C
Soldering Temperature *3		T <sub>sol.</sub>	260	°C
Reflow Soldering Temperature		T <sub>sol.</sub>	255	°C

\*1 : Pulse width  $t_w \leq 100 \mu s$ , period  $T = 10 \text{ ms}$

\*2 : No icebound or dew

\*3 : For 5s or less

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## Electrical Characteristics ( $T_a=25^\circ\text{C}$ )

Characteristic		Symbol	Min.	Typ.	Max.	Unit	Condition
Input	Forward Voltage	$V_F$	-	1.2	1.4	V	$I_F=20\text{ mA}$
	Reverse Current	$I_R$	-	-	10	$\mu\text{A}$	$V_R=5\text{V}$
	Peak Wavelength	$\lambda_P$	-	940	-	nm	$I_F=15\text{ mA}$
Output	Dark Current	$I_{CEO}$	-	1	100	nA	$V_{CE} = 20\text{V}, 0\text{ Lux}$
Collector Current		$I_C$	0.3	-	-	mA	$I_F=15\text{ mA}, V_{CE} = 5\text{V}$
Collector-Emitter Saturation Voltage		$V_{CE(sat)}$	-	-	0.4	V	$I_F=15\text{ mA}, I_C = 0.05\text{mA}$
Response Time	Rise Time	$t_r$	-	4	15	$\mu\text{s}$	$V_{CC}=5\text{V}, I_C=0.3\text{ mA}$ $R_L=100\Omega$
	Fall Time	$t_f$	-	5	20	$\mu\text{s}$	

- Circuit for measuring response time

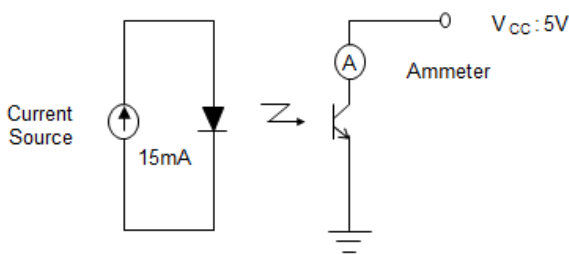


Fig 1. Test Circuit for  $I_C$

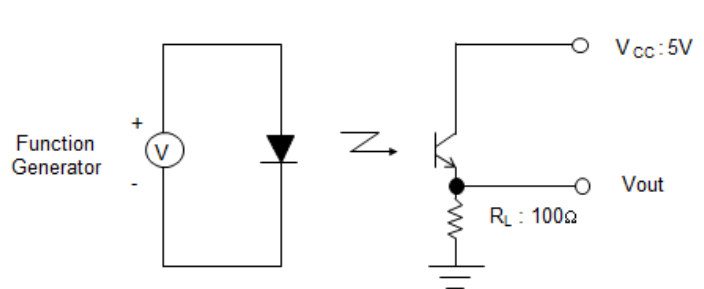


Fig 2. Test Circuit for Rise and Fall Time

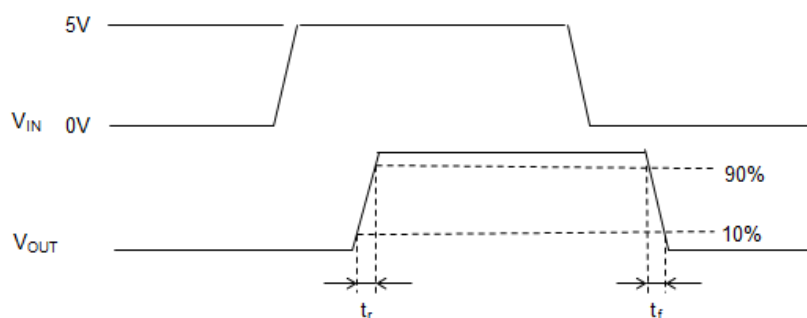
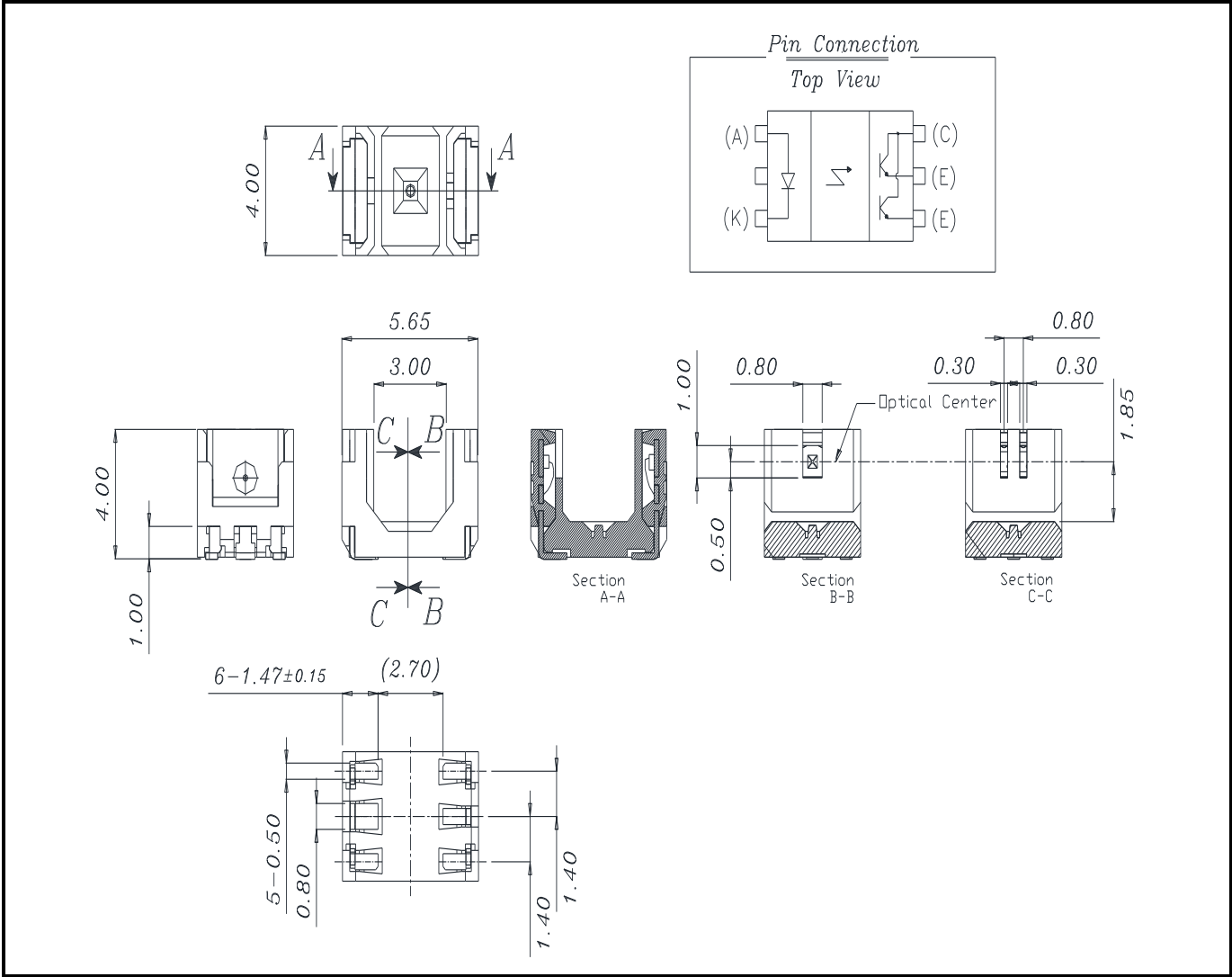


Fig 3. Definitions for Response Times

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Package Outline Dimensions



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