

SURFACE MOUNT SILICON TRANSIENT VOLTAGE SUPPRESSOR



APPLICATIONS:

- User interface protection
- Voltage rail protection
- Data line protection
- Voltage clamping



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CTLTVS6V2 is a low leakage, fast response silicon TVS packaged in an ultra small, ultra low profile TLM2D3D6 surface mount case. This device is designed to protect sensitive equipment against ESD damage.

MARKING CODE: S

FEATURES:

- Ultra small, ultra low profile 0.3mm x 0.6mm x 0.3mm leadless surface mount package
- High peak pulse current capability
- · Low leakage current

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Peak Power Dissipation (8x20µs)	P _{PK}	35	W
ESD Voltage (IEC 61000-4-2, Air)	VESD	15	kV
ESD Voltage (IEC 61000-4-2, Contact)	VESD	8.0	kV
Operating and Storage Junction Temperature	T _J , T _{stg}	-55 to +150	°C

ELECTRICAL CHARACTERISTICS: (T_A=25°C) V_F=1.0V MAX @ I_F=10mA

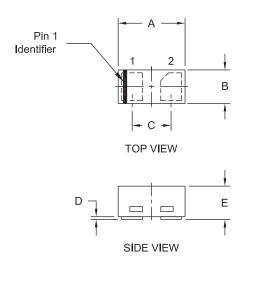
Maximum Reverse Stand-off Voltage VRWM		Breakdow Voltage VBR @ IT		Test Current ^I T	Maximum Reverse Leakage Current IR @ VRWM	Maximum Clamping Voltage VC @ IPP	Peak Pulse Current IPP	Typical Junction Capacitance @ 0V Bias CJ
V	MIN	NOM	MAX	mA	μA	۷ ۲ س	A	pF
4.0	5.8	6.2	6.6	5.0	1.0	10.7	3.0	25

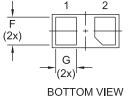
R2 (2-April 2015)



CTLTVS6V2

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R2

TLM2D3D6 CASE - MECHANICAL OUTLINE

DIMENSIONS					
	INCHES		MILLIMETERS		
SYMBOL	MIN	MAX	MIN	MAX	
A	0.022	0.026	0.55	0.65	
В	0.010	0.014	0.25	0.35	
С	0.014		0.35		
D	0.000	0.002	0.00	0.05	
E	0.011	0.013	0.28	0.32	
F	0.008	0.012	0.20	0.30	
G	0.005	0.009	0.13	0.24	
TLM2D3D6 (REV: R2)					

LEAD CODE:

1) Cathode 2) Anode

MARKING CODE: S

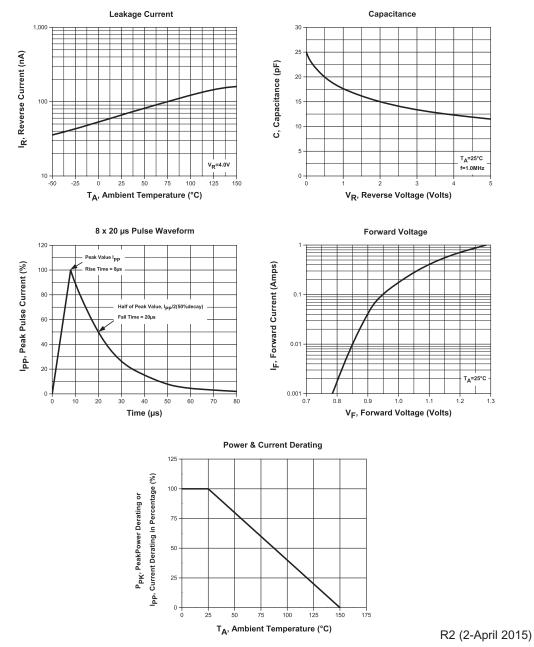
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TYPICAL ELECTRICAL CHARACTERISTICS

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SERVICES

- · Bonded Inventory
- Custom Electrical Screening
- Custom Electrical Characteristic Curves
- SPICE Models
- Custom Packaging
- Package Base Options
- Custom Device Development/Multi Discrete Modules (MDM™)
- Bare Die Available for Hybrid Applications

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