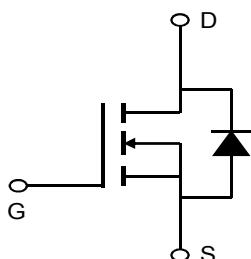
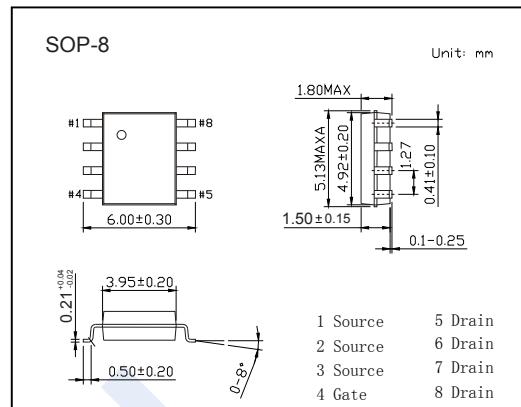


N-Channel MOSFET

AO4266 (KO4266)

■ Features

- $V_{DS} (V) = 60V$
- $I_D = 10 A (V_{GS} = 10V)$
- $R_{DS(ON)} < 15m\Omega (V_{GS} = 10V)$
- $R_{DS(ON)} < 19m\Omega (V_{GS} = 4.5V)$



■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	
VDS Spike @ 10us	V_{SPIKE}	72	
Continuous Drain Current	I_D	10	A
		8	
Pulsed Drain Current	I_{DM}	40	A
Avalanche Current	I_{AS}	20	
Avalanche Energy	E_{AS}	20	mJ
Power Dissipation	P_D	3.1	W
		2	
Thermal Resistance.Junction- to-Ambient	R_{thJA}	40	$^\circ C/W$
		75	
Thermal Resistance.Junction- to-Lead	R_{thJL}	24	$^\circ C$
Junction Temperature	T_J	150	
Storage Temperature Range	T_{stg}	-55 to 150	

N-Channel MOSFET

AO4266 (KO4266)

■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DSS}	$I_D=250 \mu\text{A}, V_{GS}=0\text{V}$	60			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=60\text{V}, V_{GS}=0\text{V}$			1	μA
		$V_{DS}=60\text{V}, V_{GS}=0\text{V}, T_J=55^\circ\text{C}$			5	
Gate-Body Leakage Current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.5		2.5	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10\text{V}, I_D=10\text{A}$			15	$\text{m}\Omega$
		$V_{GS}=10\text{V}, I_D=10\text{A}, T_J=125^\circ\text{C}$			25	
		$V_{GS}=4.5\text{V}, I_D=9\text{A}$			19	
Forward Transconductance	g_{FS}	$V_{DS}=5\text{V}, I_D=10\text{A}$		35		S
Input Capacitance	C_{iss}	$V_{GS}=0\text{V}, V_{DS}=30\text{V}, f=1\text{MHz}$		1340		pF
Output Capacitance	C_{oss}			123		
Reverse Transfer Capacitance	C_{rss}			10		
Gate Resistance	R_g	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$	0.7		2.3	Ω
Total Gate Charge (10V)	Q_g	$V_{GS}=10\text{V}, V_{DS}=30\text{V}, I_D=10\text{A}$		21	30	nC
Total Gate Charge (4.5V)				9	15	
Gate Source Charge	Q_{gs}	$V_{GS}=10\text{V}, V_{DS}=30\text{V}, I_D=10\text{A}$		4.7		nC
Gate Drain Charge	Q_{gd}			2.6		
Turn-On Delay Time	$t_{d(on)}$			6		
Turn-On Rise Time	t_r			2.5		
Turn-Off Delay Time	$t_{d(off)}$	$V_{GS}=10\text{V}, V_{DS}=30\text{V}, R_L=3\Omega, R_{GEN}=3\Omega$		22		ns
Turn-Off Fall Time	t_f			2.5		
Body Diode Reverse Recovery Time	t_{rr}			15.5		
Body Diode Reverse Recovery Charge	Q_{rr}	$I_F= 10\text{A}, dI/dt= 500\text{A}/\mu\text{s}$		55.5		nC
Maximum Body-Diode Continuous Current	I_S				4	
Diode Forward Voltage	V_{SD}	$I_S=1\text{A}, V_{GS}=0\text{V}$			1	V

Note : The static characteristics in Figures 1 to 6 are obtained using $<300 \mu\text{s}$ pulses, duty cycle 0.5% max.

■ Marking

Marking	4266 KC****
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N-Channel MOSFET

AO4266 (KO4266)

■ Typical Characteristics

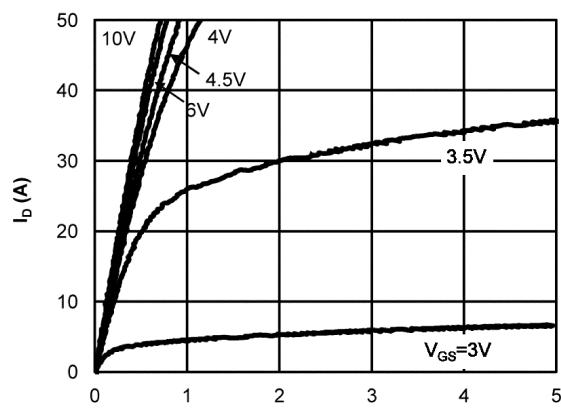


Figure 1: On-Region Characteristics (Note E)

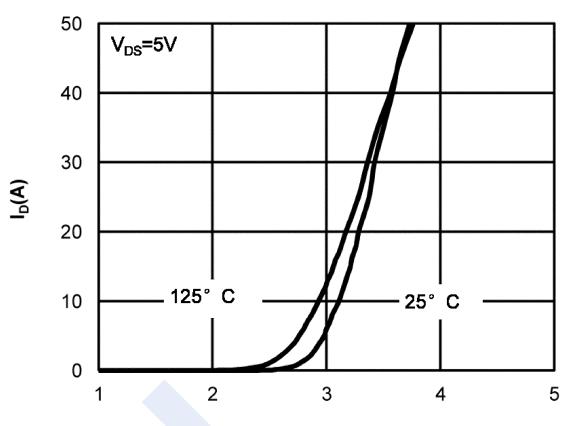


Figure 2: Transfer Characteristics (Note E)

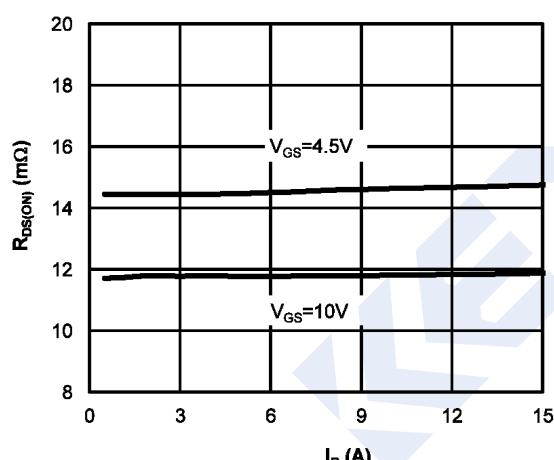


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

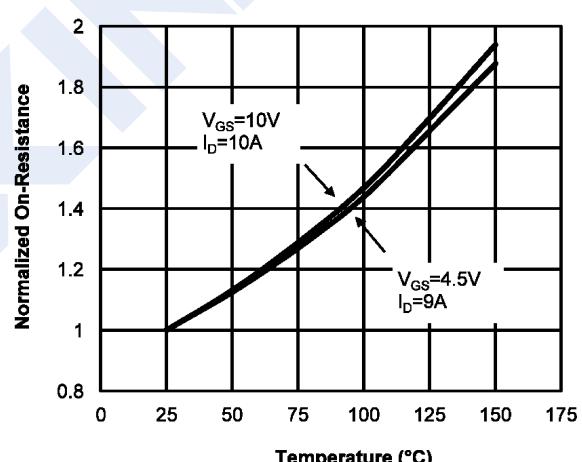


Figure 4: On-Resistance vs. Junction Temperature (Note E)

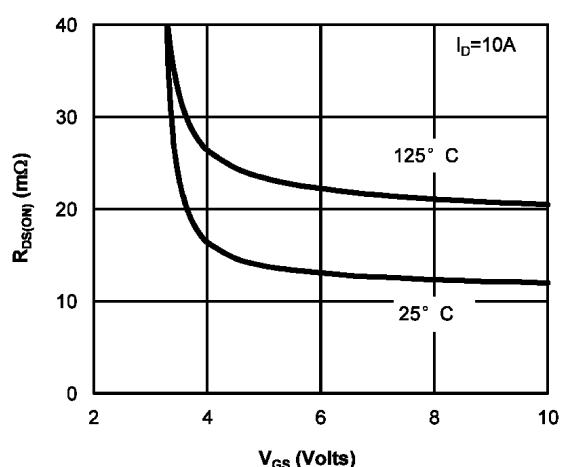


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

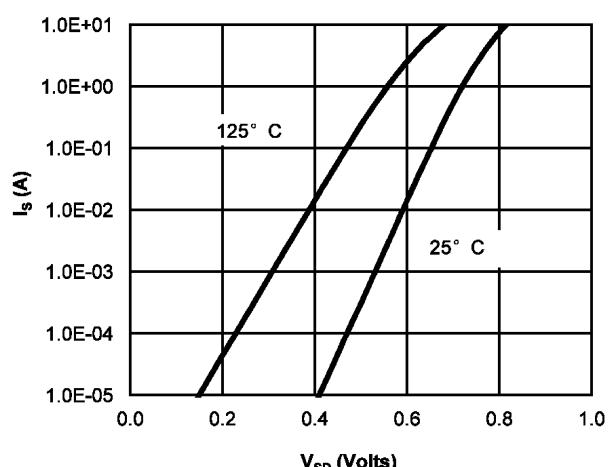


Figure 6: Body-Diode Characteristics (Note E)

N-Channel MOSFET

AO4266 (KO4266)

■ Typical Characteristics

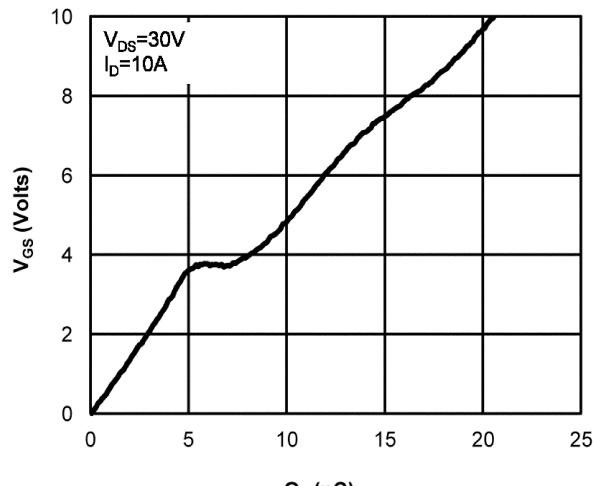


Figure 7: Gate-Charge Characteristics

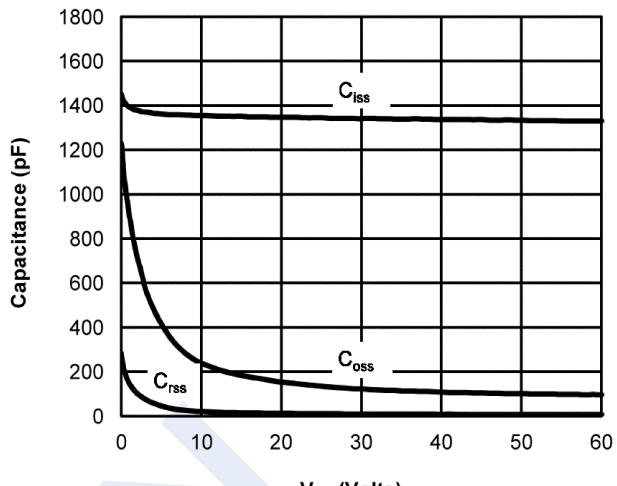


Figure 8: Capacitance Characteristics

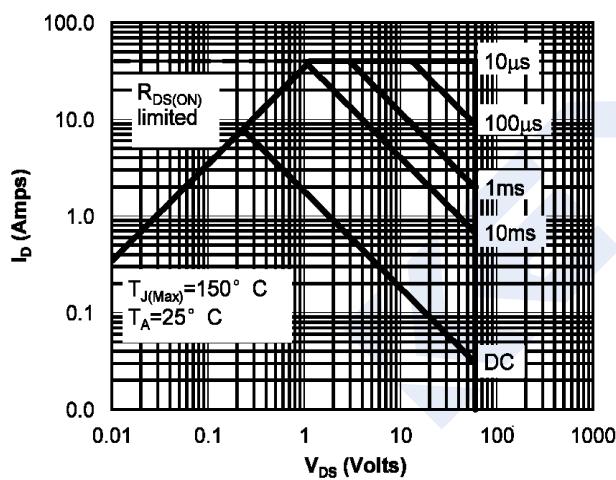


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

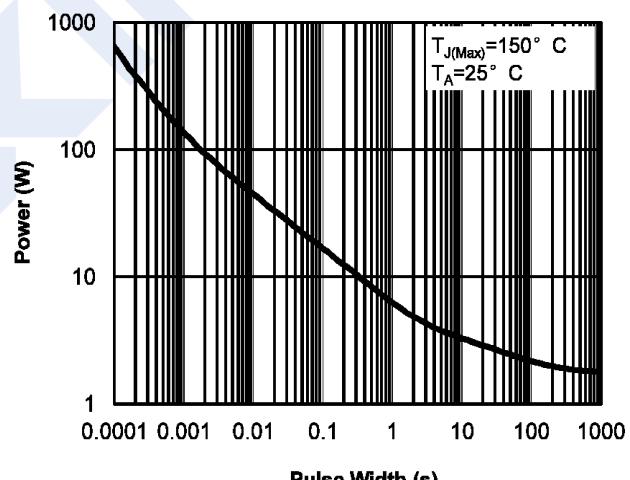


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

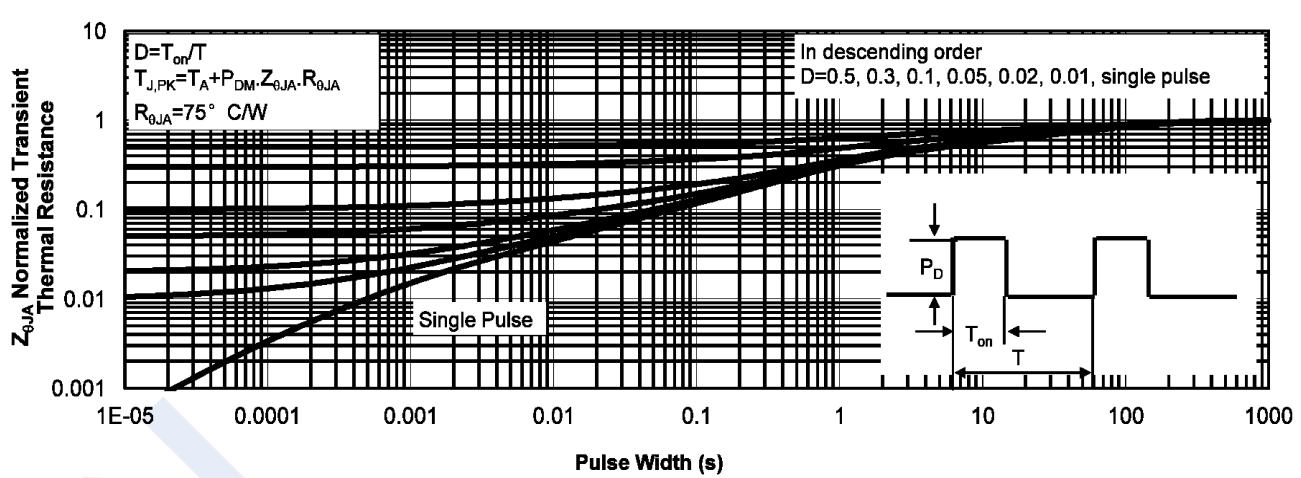


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)