

# Schottky barrier diode

## RB095B-40

### ●Applications

General rectification  
(Common cathode dual chip)

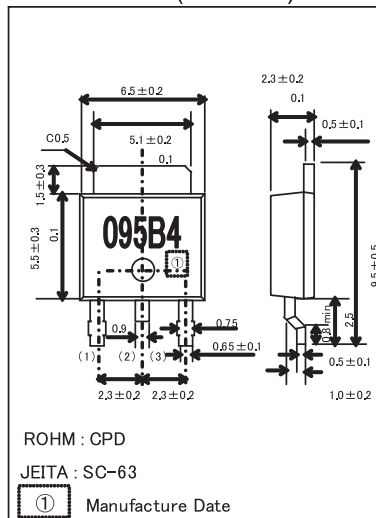
### ●Features

- 1) Power mold type.(CPD3)
- 2) Low  $V_F$
- 3) High reliability

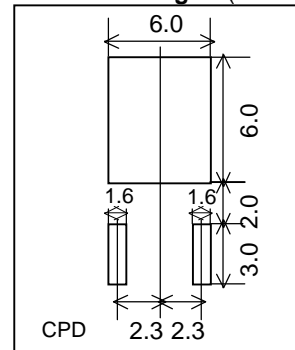
### ●Construction

Silicon epitaxial planar

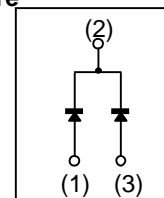
### ●Dimensions(Unit : mm)



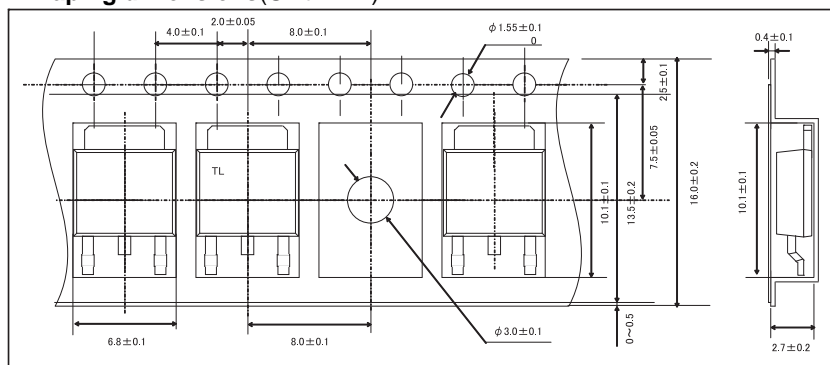
### ●Land size figure(Unit : mm)



### ●Structure



### ●Taping dimensions(Unit : mm)



### ●Absolute maximum ratings (Ta=25°C)

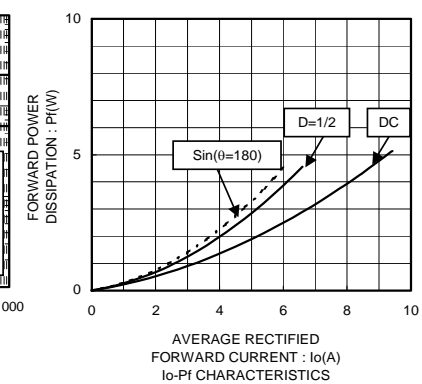
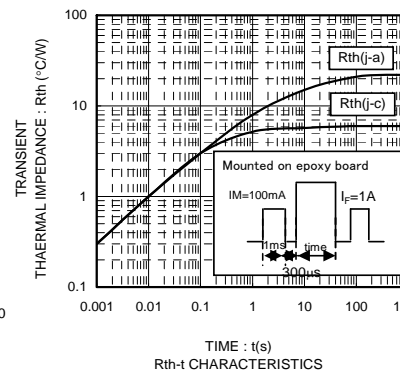
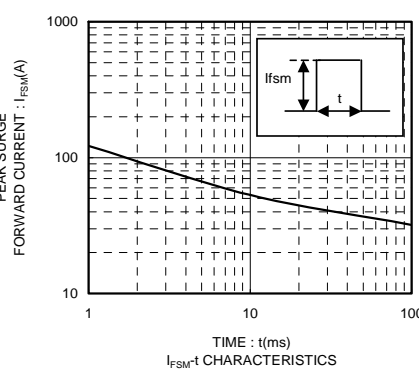
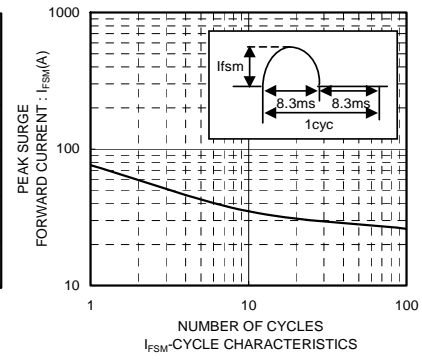
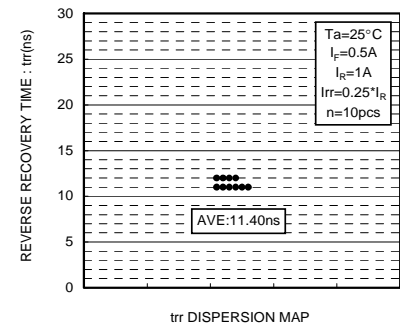
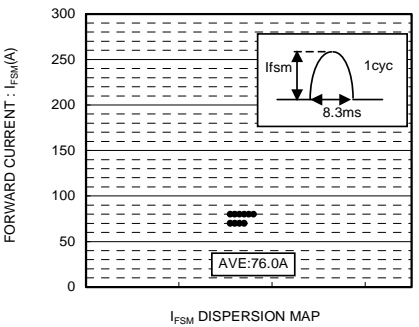
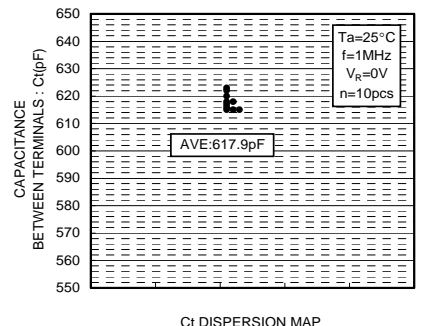
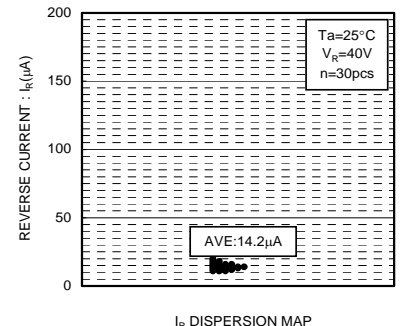
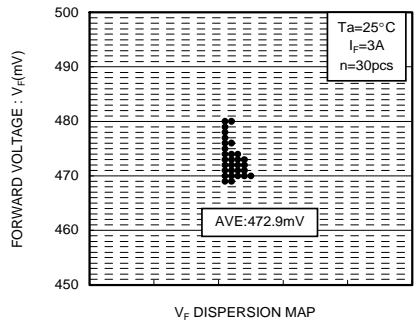
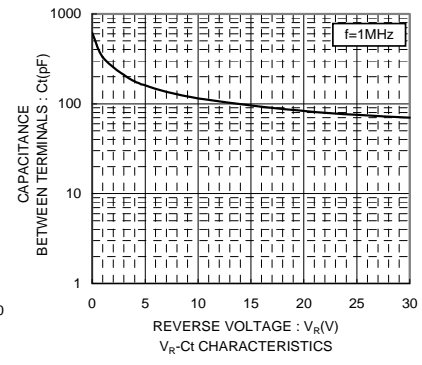
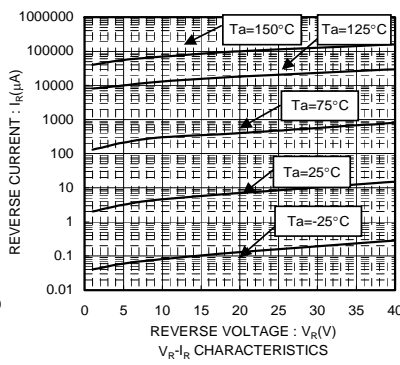
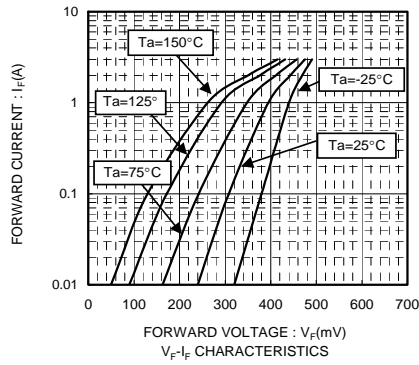
Parameter	Symbol	Limits	Unit
Reverse voltage (repetitive peak)	$V_{RM}$	45	V
Reverse voltage (DC)	$V_R$	40	V
Average rectified forward current(*1)	$I_O$	6	A
Forward current surge peak (60Hz·1cyc)(*1)	$I_{FSM}$	45	A
Junction temperature	$T_j$	150	°C
Storage temperature	$T_{stg}$	-40 to +150	°C

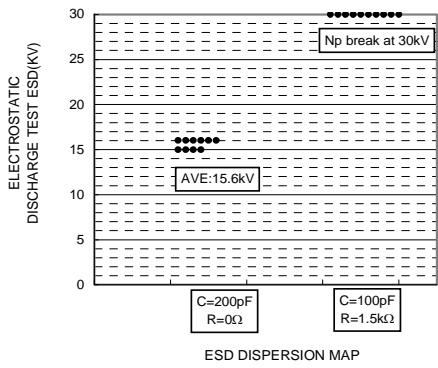
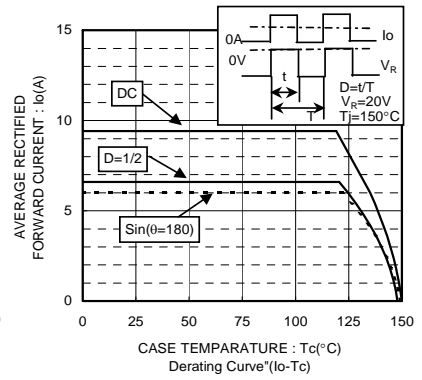
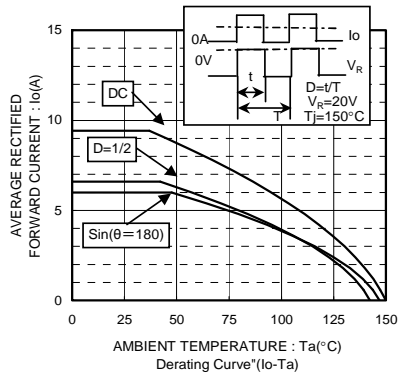
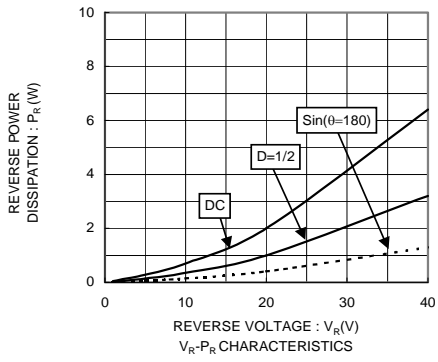
(\*1) Business frequencies, Rating of R-load, 1/2  $I_O$  per diode,  $T_c=120^\circ\text{C}$

### ●Electrical characteristic (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	$V_F$	-	-	0.55	V	$I_F=3.0\text{A}$
Reverse current	$I_R$	-	-	100	$\mu\text{A}$	$V_R=40\text{V}$
Thermal impedance	$\theta_{jc}$	-	-	6.0	°C/W	junction to case

●Electrical characteristic curves





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