

# RJK0701DPP-E0

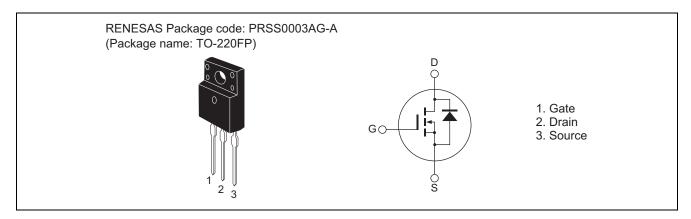
N-Channel MOS FET 75 V, 100 A, 3.8 m $\Omega$ 

R07DS0628EJ0200 Rev.2.00 Oct 17, 2012

#### **Features**

- High speed switching
- Low drive current
- Low on-resistance  $R_{DS(on)} = 3.0 \text{ m}\Omega$  typ. (at  $V_{GS} = 10 \text{ V}$ )
- Package TO-220FP

#### **Outline**



## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	75	V
Gate to source voltage	$V_{GSS}$	±20	V
Drain current	I <sub>D</sub>	100	A
Drain peak current	I <sub>D (pulse)</sub> Note1	300	A
Body-drain diode reverse drain current	I <sub>DR</sub>	100	Α
Avalanche current	I <sub>AP</sub> Note2	50	A
Avalanche energy	E <sub>AS</sub> Note2	375	mJ
Channel dissipation	Pch Note3	30	W
Channel to case thermal impedance	θch-c	4.17	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

- 2. Value at L = 100  $\mu$ H, Tch = 25°C, Rg  $\geq$  50 $\Omega$ ,
- 3.  $Tc = 25^{\circ}C$

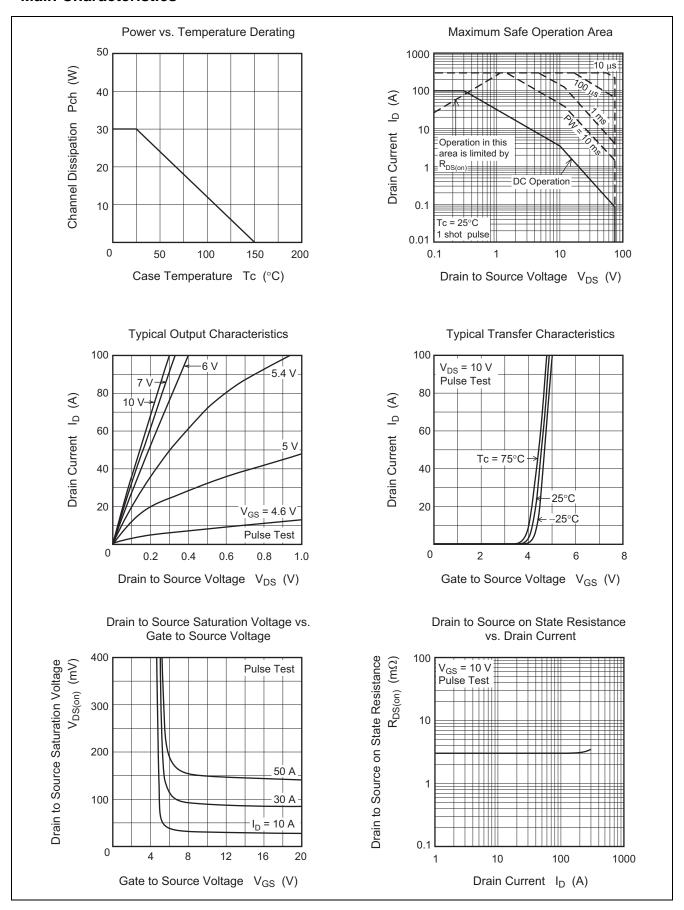
## **Electrical Characteristics**

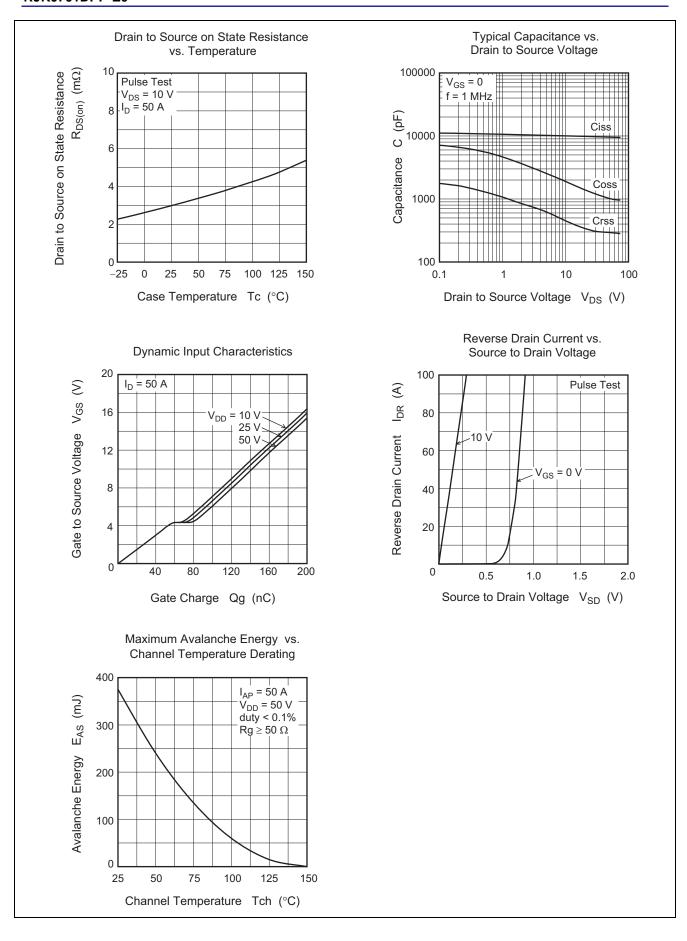
 $(Ta = 25^{\circ}C)$ 

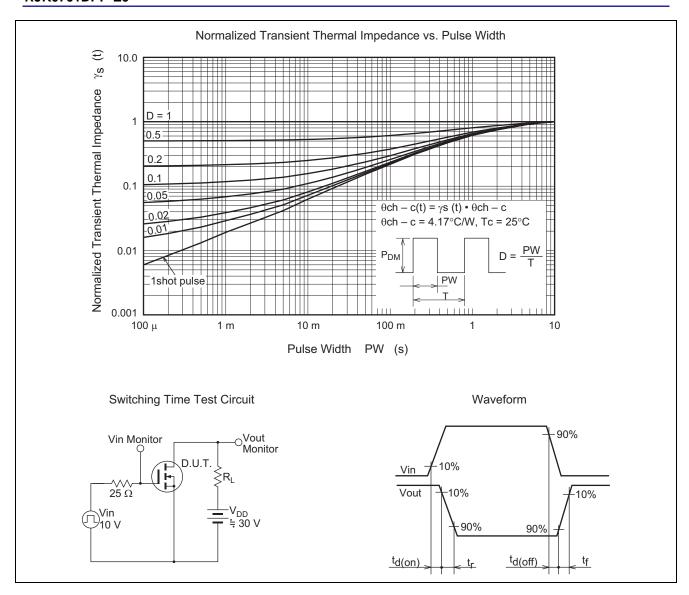
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	75			V	$I_D = 10 \text{mA}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 75 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	_	4.0	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	_	3.0	3.8	mΩ	$I_D = 50 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance						
Forward transfer admittance	y <sub>fs</sub>		130		S	$I_D = 50 \text{ A}, V_D = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss		10000	_	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss		1900	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	480	_	pF	f = 1 MHz
Gate Resistance	Rg	_	1.6	_	Ω	
Total gate charge	Qg	_	140	_	nC	V <sub>DD</sub> = 25 V
Gate to source charge	Qgs	_	50	_	nC	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 50 A
Gate to drain charge	Qgd	_	21	_	nC	
Turn-on delay time	t <sub>d(on)</sub>	_	53	_	ns	V <sub>GS</sub> = 10 V
Rise time	t <sub>r</sub>	_	25	_	ns	$\begin{split} I_D &= 50 \text{ A} \\ V_{DD} &\cong 30 \text{ V} \\ Rg &= 4.7  \Omega \end{split}$
Turn-off delay time	t <sub>d(off)</sub>	_	100	_	ns	
Fall time	t <sub>f</sub>	_	24	_	ns	
Body-drain diode forward voltage	$V_{DF}$	_	0.85	1.5	V	$I_F = 100 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	65	_	ns	$I_F = 100 \text{ A}, V_{GS} = 0$
						di <sub>F</sub> /dt = 100 A/μs

Notes: 4. Pulse test

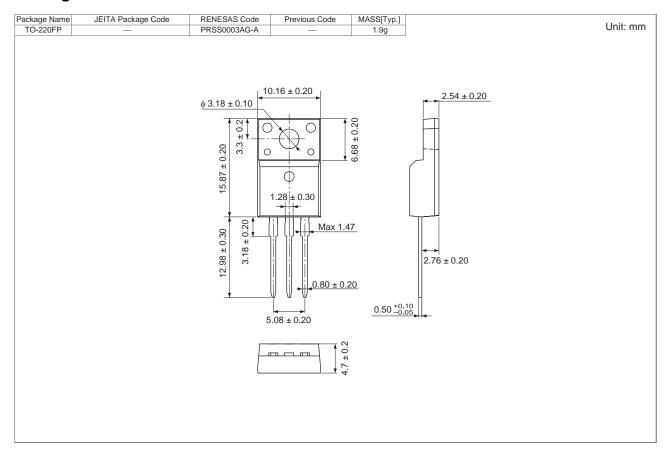
### **Main Characteristics**







## **Package Dimensions**



# **Ordering Information**

Orderable Part Number	Quantity	Shipping Container
RJK0701DPP-E0-T2	50 pcs	Magazine (Tube)

Note: The symbol of 2nd "-" is occasionally presented as "#".

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