

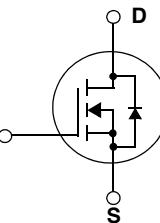
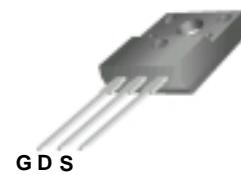


WFF4N60

600V N-Channel MOSFET

Features

- Low Intrinsic Capacitances
- Excellent Switching Characteristics
- Extended Safe Operating Area
- Unrivalled Gate Charge : $Q_g = 15\text{nC}$ (Typ.)
- $\text{BV}_{DSS}=600\text{V}, \text{ID}=4\text{A}$
- $R_{DS(on)} : 2.3 \Omega$ (Max) @ $\text{VG}=10\text{V}$
- 100% Avalanche Tested



TO-220F

G-Gate,D-Drain,S-Source

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	WFF4N60	Units
V_{DSS}	Drain-Source Voltage	600	V
I_D	Drain Current -continuous ($T_c=25^\circ\text{C}$)	4*	A
	-continuous ($T_c=100^\circ\text{C}$)	2.8*	A
V_{GS}	Gate-Source Voltage	± 30	V
E_{AS}	Single Plused Avalanche Energy (Note1)	240	mJ
I_{AR}	Avalanche Current (Note2)	4	A
P_D	Power Dissipation ($T_c=25^\circ\text{C}$)	33	W
T_J, T_{STG}	Operating and Storage Temperature Range	-55 ~ +150	°C
T_L	Maximum lead temperature for soldering purpose, 1/8" from case for 5 seconds	300	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max	Units
$R_{\theta JC}$	Thermal Resistance,Junction to Case	--	3.47	°C/W
$R_{\theta JA}$	Thermal Resistance,Junction to Ambient	--	62.5	°C/W

* Drain current limited by maximum junction temperature.

Electrical Characteristics Tc=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Typ.	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	ID=250 μA, VGS=0	600	--	--	V
△BV _{DSS} /△T _J	Breakdown Voltage Temperature Coefficient	I _D =250 μA, Reference to 25°C	--	0.6	--	V/°C
IDSS	Zero Gate Voltage Drain Current	V _{DS} =600V, V _{GS} =0V	--	--	10	μA
		V _{DS} =480V, Tc=125°C			100	μA
IGSSF	Gate-body leakage Current, Forward	V _{GS} =+30V, V _{DS} =0V	--	--	100	nA
IGSSR	Gate-body leakage Current, Reverse	V _{GS} =-30V, V _{DS} =0V	--	--	-100	nA

On Characteristics

V _{GS(th)}	Date Threshold Voltage	I _D =250uA, V _{DS} =V _{GS}	2	--	4	V
R _{DS(on)}	Static Drain-Source On-Resistance	I _D =2A, V _{GS} =10V	--	--	2.3	Ω

Dynamic Characteristics

C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0, f=1.0MHz	--	520	670	pF
C _{oss}	Output Capacitance		--	70	90	pF
C _{rss}	Reverse Transfer Capacitance		--	8	11	pF

Switching Characteristics

T _{d(on)}	Turn-On Delay Time	VDD=300V, ID=4A RG=25 Ω (Note 3,4)	--	13	35	nS
T _r	Turn-On Rise Time		--	45	100	nS
T _{d(off)}	Turn-Off Delay Time		--	25	60	nS
T _f	Turn-Off Fall Time		--	35	80	nS
Q _g	Total Gate Charge	V _{DS} =480, V _{GS} =10V, ID=4A (Note 3,4)	--	15	20	nC
Q _{gs}	Gate-Source Charge		--	3.4	--	nC
Q _{gd}	Gate-Drain Charge		--	7.1	--	nC

Drain-Source Diode Characteristics and Maximum Ratings

I _S	Maximum Continuous Drain-Source Diode Forward Current	--	--	4	A	
I _{SM}	Maximum Plated Drain-Source Diode Forward Current	--	--	16	A	
V _{SD}	Drain-Source Diode Forward Voltage	I _D =4A	--	--	1.25	V
t _{rr}	Reverse Recovery Time	I _S =4A, V _{GS} =0V di _F /dt=100A/ μs (Note 3)	--	250	--	nS
Q _{rr}	Reverse Recovery Charge		--	1.25	--	μC

*Notes 1, L=27.5mH, IAS=4A, VDD=50V, RG=25Ω, Starting TJ =25°C

2, Repetitive Rating : Pulse width limited by maximum junction temperature

3, Pulse Test : Pulse Width ≤ 300μs, Duty Cycle ≤ 2%

4, Essentially Independent of Operating Temperature

Typical Characteristics

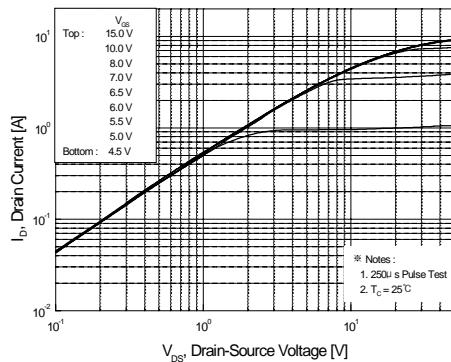


Figure 1. On-Region Characteristics

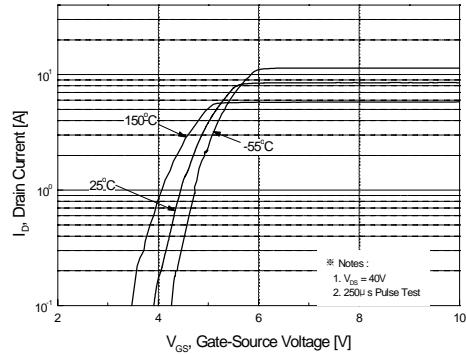


Figure 2. Transfer Characteristics

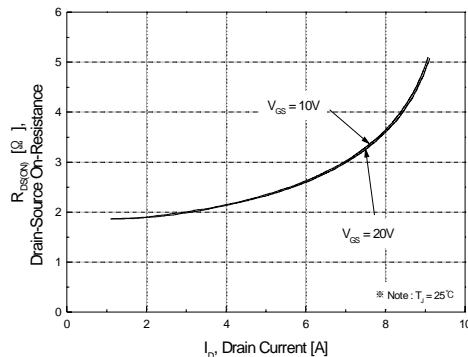


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

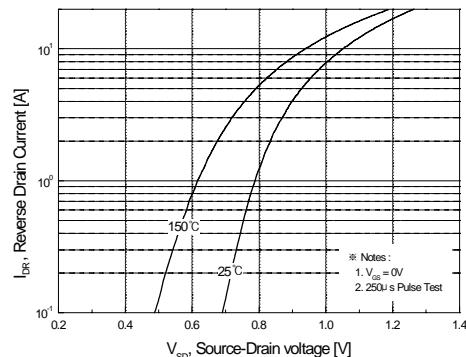


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

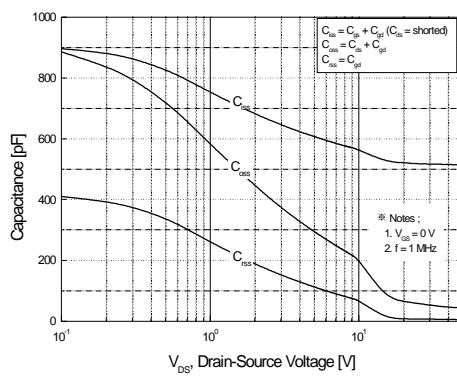


Figure 5. Capacitance Characteristics

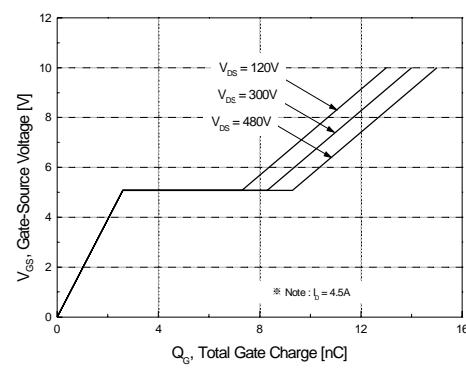


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

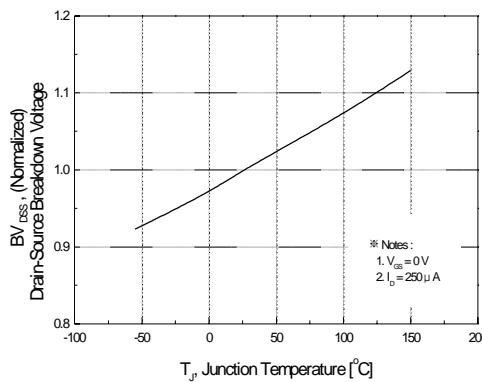


Figure 7. Breakdown Voltage Variation vs Temperature

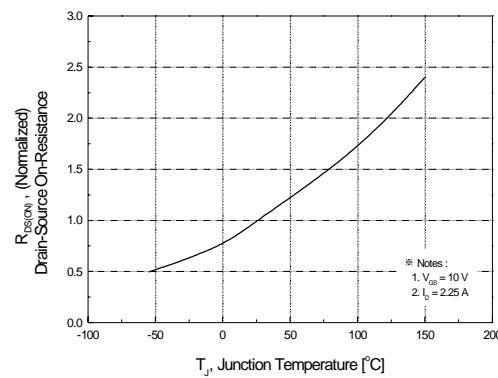


Figure 8. On-Resistance Variation vs Temperature

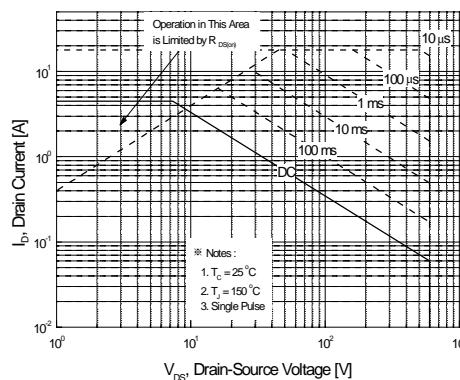


Figure 9-2. Maximum Safe Operating Area for WFF4N60

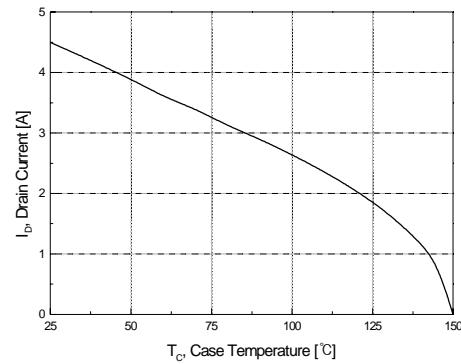


Figure 10. Maximum Drain Current vs Case Temperature

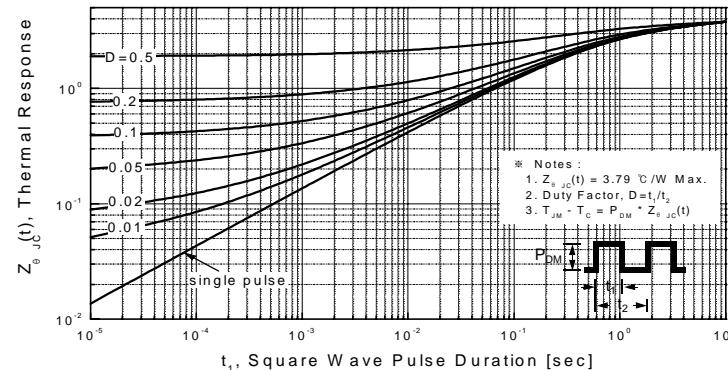
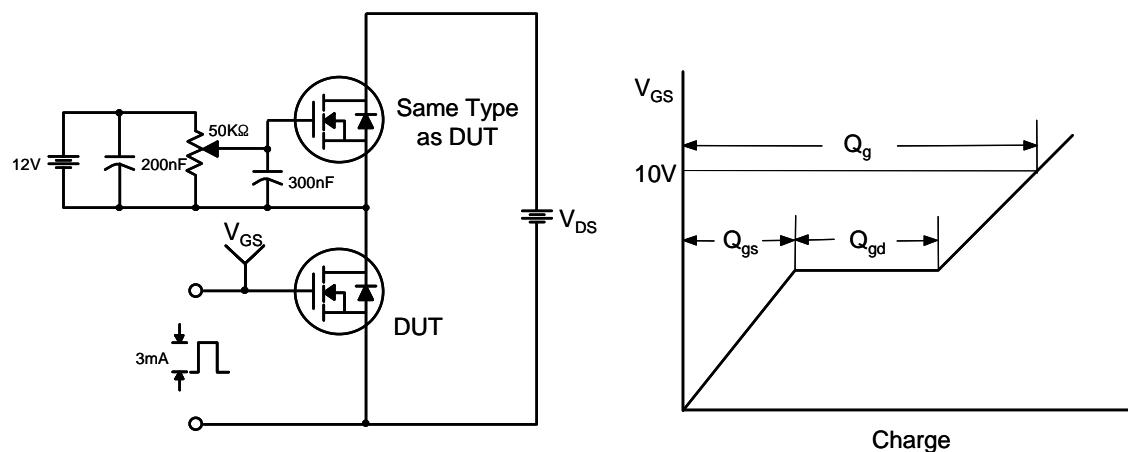
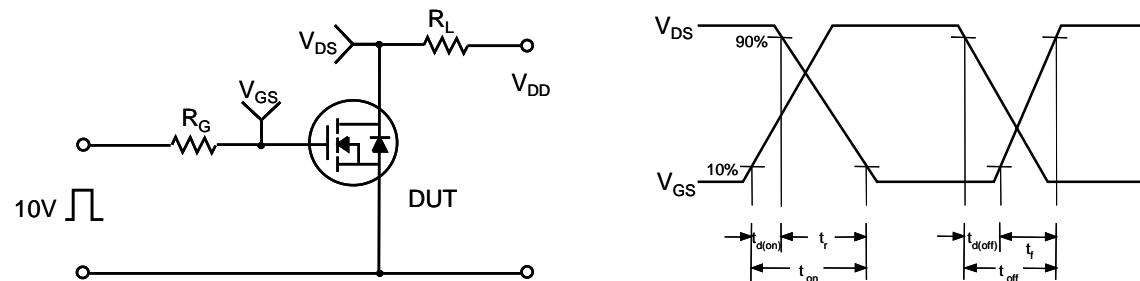
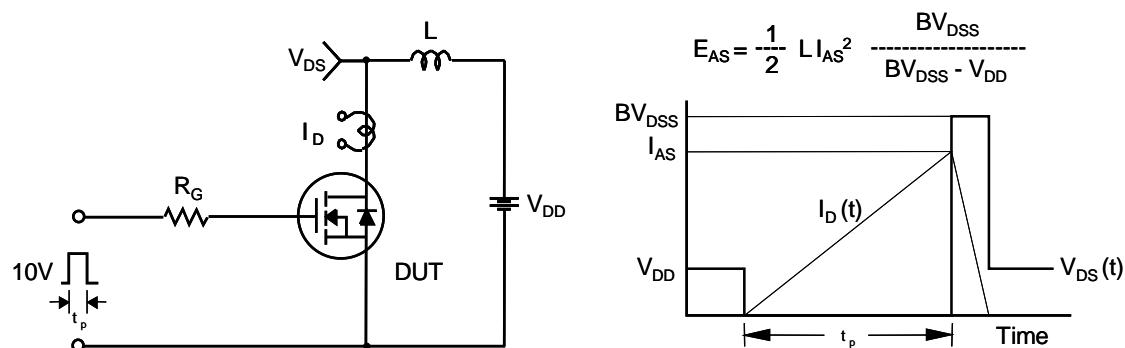


Figure 11-2. Transient Thermal Response Curve for WFF4N60

Gate Charge Test Circuit & Waveform

Resistive Switching Test Circuit & Waveforms

Unclamped Inductive Switching Test Circuit & Waveforms


$$E_{AS} = \frac{1}{2} L I_{AS}^2 \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$

Peak Diode Recovery dv/dt Test Circuit & Waveforms

