MP5120, MP5220, MP5420

High Speed, +/-9V, Rail-to-Rail Input-Output Op Amps

The Future of Analog IC Technology

DESCRIPTION

The MP51 20 (single), MP5220 (dual), and MP5420 (q uad) are high-speed, high-voltage rail-to-rail input-output amplifiers for use as voltage reference buffers in Thin Film Transistor Liquid Crystal Displa ys (TFT-LCDs). The MP5120 family provi des excellent overall performance and versatility. The 20MHz –3d B bandwidth and 45V/ μs slew rate make these amplifier suitable f or many portable applications.

The MP5120, MP5220, and MP5240 are designed to operate at supply voltages as low as 3.2V and up to 18 V at 1.6mA of supply current per amplifier. The input can swing 0.5 V below the negative rail and 0.5V above the positive rail. The output can swing within 100mV of each rail.

The MP542 0 quad channel is available in the space-saving 14-pin TSSOP pa ckage. The MP5220 du al channel is available in the 8-pin MSOP pa ckage and the MP5 120 single channel is a vailable in 5-pin TSOT p ackage. All feature a standard operational amplifier pin out.

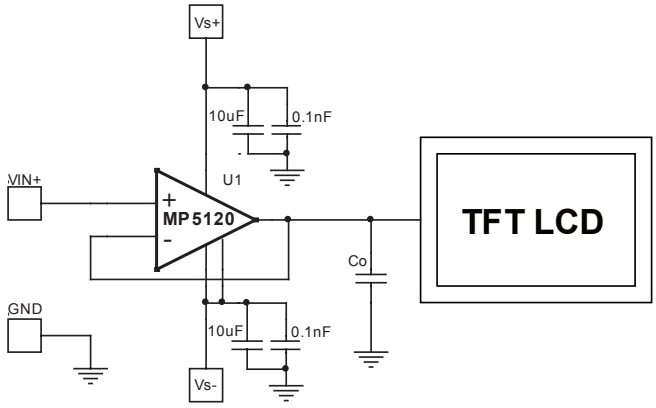
FEATURES

- Supply Operation: +/-9V
- 20MHz –3dB Bandwidth
- 45V/ μs Slew Rate
- Supply Current (per amplifier)1.6mA
- Unity-Gain Stable
- Output Swing within 100mV of Supply Rail
- Rail-to-Rail Input Capability
- High Output Drive Capability (50mA)
- MP5120 Available in TSOT-5
- MP5220 Available in MSOP-8
- MP5420 Available in TSSOP14

APPLICATIONS

- TFT-LCD Drive Circuits
- Electronic Notebooks
- Electronic Games
- Touch-Screen Displays
- Personal Communication Devices
- Personal Digital Assistants (PDA)
- Portable Instrumentation
- Sampling ADC Amplifiers
- Wireless LAN
- Office Automation
- Active Filters
- ADC/D AC Buffer

TYPICAL APPLICATION



ORDERING INFORMATION

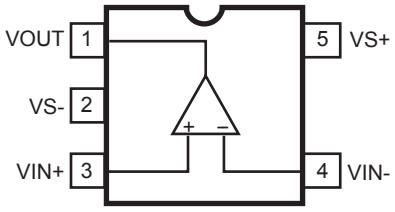
Part Number*	Package	Top Marking	Free Air Temperature (T_A)
MP5120DJ	TSOT	8F	–40°C to +85°C
MP5220DK	MSOP	5220D	
MP5420DM TSSOP		M5420DM	

* For Tape & Reel, add suffix –Z (e.g. MP5120DJ–Z).

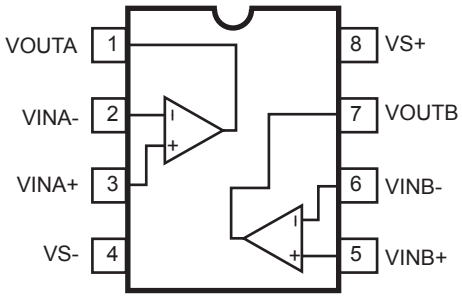
For RoHS Compliant packaging, add suffix –LF (e.g. MP5120DJ–LF–Z)

PACKAGE INFORMATION

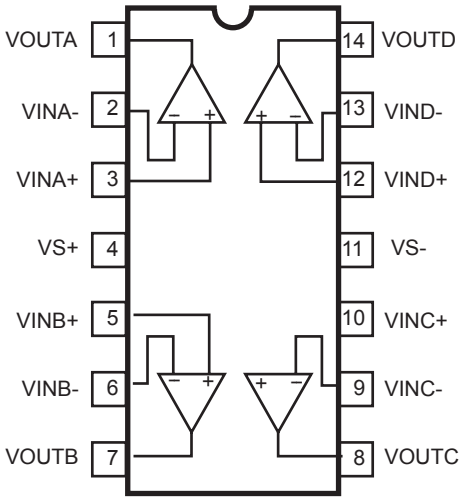
MP5120
(5-PIN TSOT)
TOP VIEW



MP5220
(8-PIN MSOP)
TOP VIEW



MP5420
(14-PIN TSSOP)
TOP VIEW



ABSOLUTE MAXIMUM RATINGS (1)

(T_A=25°C)

Supply Voltage between V_{S+} and V_{S-} .....–0.3V to +22V
Input Voltage.....V_{S-} - 0.5V, V_{S+} +0.5V
Maximum Continuous Output Current 50mA
Maximum Die Temperature .....+125°C
Storage Temperature..... -60°C to +150°C
Ambient Operating Temp..... -40°C to +85°C
Power Dissipation. ..... See Curves (2)

Recommended Operating Conditions (3)

Power Supply Operation (V_{S+} to V_{S-}) .....3.2V to +18V
Operating Junct. Temp (T_J) ..... -40°C to +125°C

Thermal Resistance (4) θ_{JA} θ_{JC}

MSOP .....150 ..... 65.. °C/W
TSOT .....220 .... 110.. °C/W
TSSOT .....40 …… 6.. °C/W

Notes:

1) Exceeding these ratings may damage the device.
2) The maximum allowable power dissipation is a function of the maximum junction tempe rature T_J (MAX), the junction-to-ambient thermal resistance θ_{JA}, and the ambient temperatu re T_A. The ma ximum allow able continuous pow er dissipation at any ambient te mperature is calculated b y P_D (MAX) = (T_J (MAX)-T_A)/θ_{JA}. Exceeding th e maximum allo wable po wer dissipation w ill cause excessive die temperatur e, and the regulator will go into thermal shutdown. Inte rnal thermal shutdown circuitr y protects th e device from perman ent damage.
3) The device is not guaran teed to function outside of its operating conditions.
4) Measured on JESD51-7, 4-layer PCB.

ELECTRICAL CHARACTERISTICS

V_{S+} = +5V, V_S- = -5V, R_L = 10kΩ and C_L = 10pF, T_A =T_J= 25°C, unless otherwise noted.

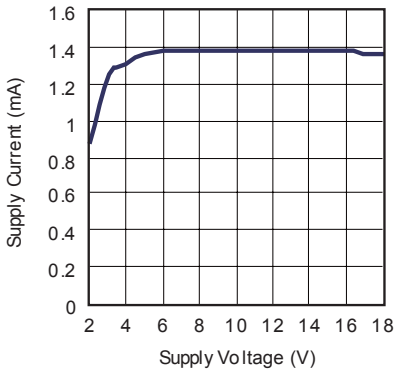
Parameter	Symbol	Condition	Min	Typ	Max	Units
INPUT CHARACTERISTICS						
Input Offset Voltage	V_{OS}	V_{CM}=5V		2	20	mV
Average Offset Voltage Drift[3]	TCV_{OS}			5		µ/°C
Input Bias Current	I_B	V_{CM}=5V		0.5	2	µA
Input Impedance	R_{IN}			1		GΩ
Input Capacitance	C_{IN}			1.35		pF
Common-Mode Input Range	CMIR		-5.5		+5.5	V
Common-Mode Reje ction Ratio	CMRR for	V_{IN} from -5.5V to +5.5V	60	85		dB
Open Loop Gain	A_{VOL}	-4.5V ≤ V_{OUT} ≤ +4.5V	50	60	≤	dB
OUTPUT CHARACTERISTICS						
Output Swing Low	V_{OL}	I_L = -5mA		-4.95		V
Output Swing High	V_{OH}	I_L = -5mA		4.82		V
Short Circuit Current	I_{SC}	Sourcing		70		mA
		Sinking		130		
POWER SUPPLY PERFORMANCE						
Power Su pply Reje ction Ratio	PSRR	V_S is moved from ±2.25V to ±7.75V	70 95			dB
Supply Curre nt (Per Amplifier)	I_S No	load		1.6		mA
DYNAMIC PERFORMANCE						
Slew Rate (Rise/Fall)	SR	-4.0V ≤ V_{OUT} ≤ +4.0V, 20% to 80%	45			V/µs
Settling to +0.1% (A_V = +1)	t_S	(AV = +1), V_O = 2V step		500		ns
-3dB Bandwidth	BW	R_L = 10kΩ, C_L = 10pF		20		MHz
Gain-Bandwidth Product	GBWP	R_L = 10kΩ, C_L = 10pF		14		MHz
Phase Margin	PM	R_L = 10kΩ, C_L = 10pF		50		
Channel Separation	CS	f = 5MHz (MP5220 & MP5420 only)	70			dB

TYPICAL PERFORMANCE CHARACTERISTICS

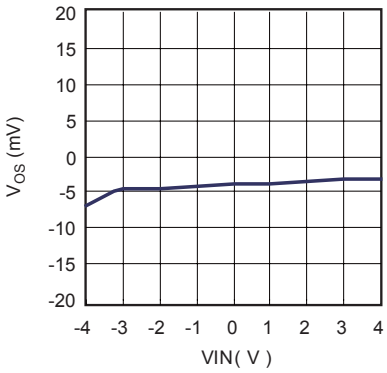
V_{S+}=5V, V_{S-}=-5V, R_L=10KΩ, C_L=12pF, T_A=25ºC, unless otherwise noted.

Supply Current vs. Supply Voltage

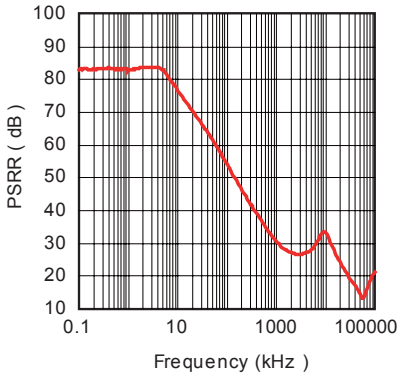
MP5120



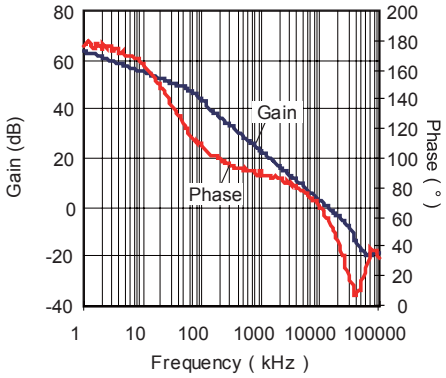
V_{OS} vs. V_{IN}



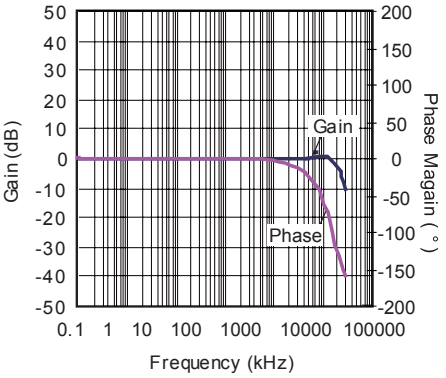
PSRR vs. Frequency



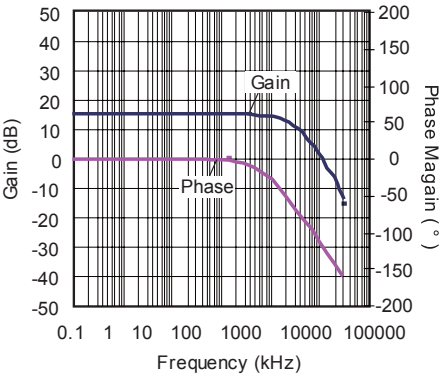
Open Loop



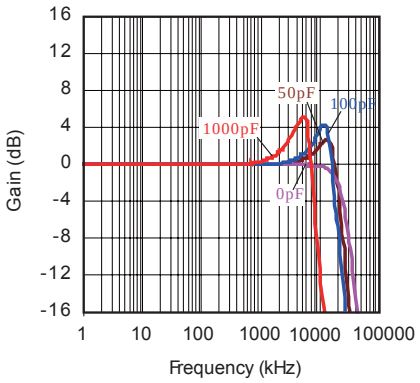
Closed Loop Av=1



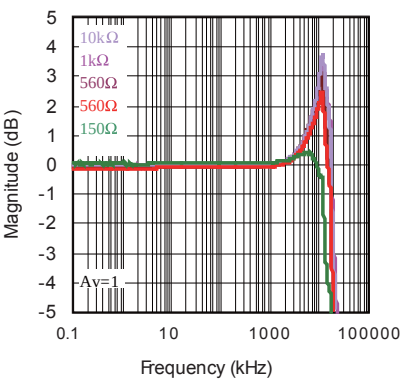
Closed Loop Av=2



Frequency Response vs. C_L

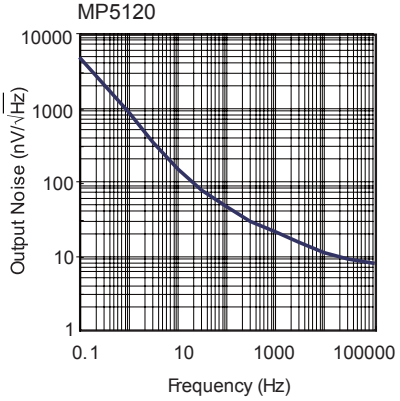


Frequency Response vs. R_L



Output Noise vs. Frequency

MP5120

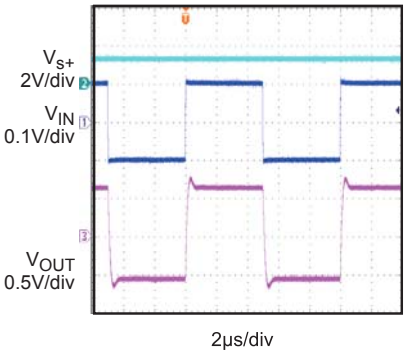


TYPICAL PERFORMANCE CHARACTERISTICS *(continued)*

V_{S+}=5V, V_{S-}=-5V, R_L=10KΩ, C_L=12pF, T_A=25ºC, unless otherwise noted.

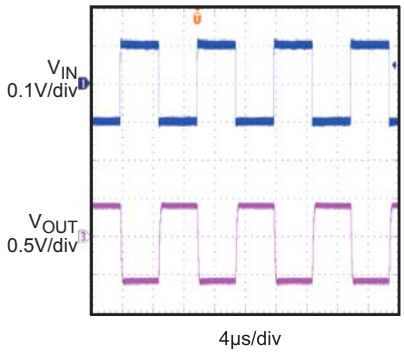
Small Signal Pulse Response

V_{s+}=+1.2V, V_{s-}=-1.2V, V_{IN}=±0.1V, A_V=6



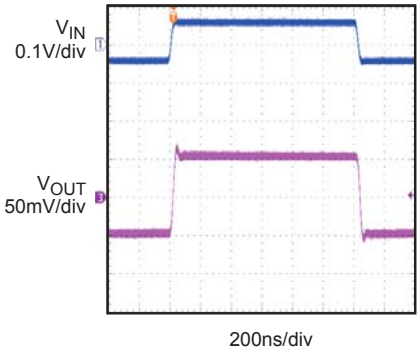
Small Signal Pulse Response

V_{s+}=+1.2V, V_{s-}=-1.2V, V_{IN}=±0.1V, A_V=-5



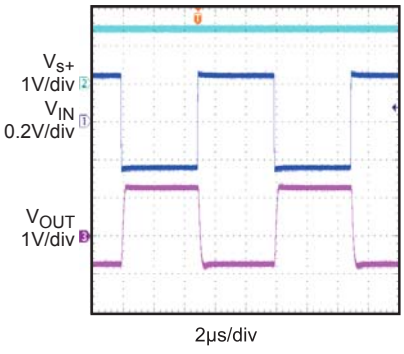
Small Signal Pulse Response

V_{IN}=±50mV, A_V=+1



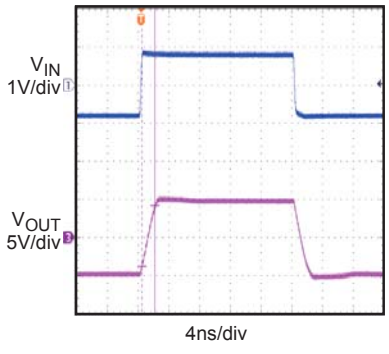
Rail to Rail Operation Response

V_{s+}=+1.2V, V_{s-}=-1.2V, V_{IN}=±0.24V, A_V=-5



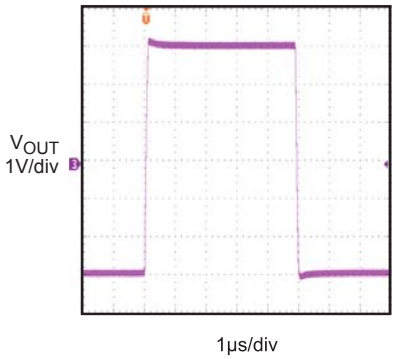
Small Signal Pulse Response

V_{IN}=±0.8V, A_V=+6



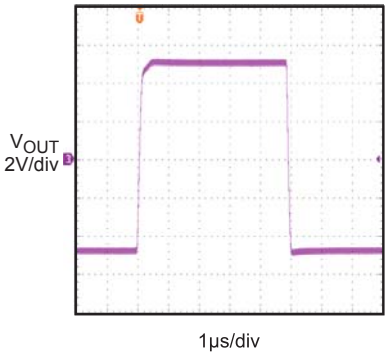
Large Signal Pulse Response

V_{IN}=±3V, A_V=+1



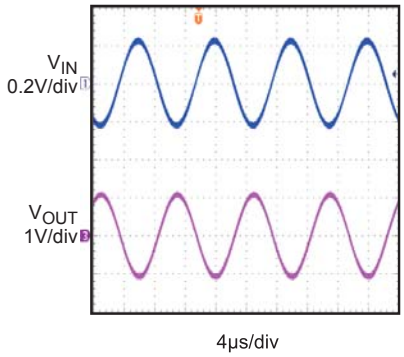
Rail to Rail Operation Response

V_{IN}=±5V, A_V=1



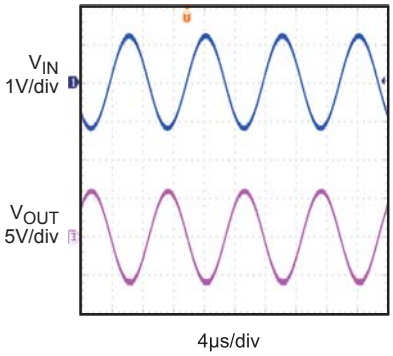
Rail to Rail Output Response

V_{s+}=+1.2V, V_{s-}=-1.2V, V_{IN}=±0.22V, A_V=-5



Rail to Rail Output Response

V_{s+}=+6V, V_{s-}=-6V, V_{IN}=±1.2V, A_V=-5



OPERATION DESCRIPTION

The MP5120/MP5220/MP5420 are high-speed, high slew rate, rail-to-rail input-output operational amplifiers. These devices can operate up to 50mA output current and 20MHz bandwidth.

INPUT

The MP5X21 can operate with inputs from rail to rail. It does this through the use of two differential pairs. A traditional PNP differential pair is used from 0.5V below the negative rail to 1V below the positive rail. At that point the input is switched to a NPN differential pair to operate up to 0.5V above the positive rail. The transition from one input differential pair to the other can cause distortion. Inputs near the rails can also cause distortion and degradation of other specifications.

OUTPUT

Current Rating

The MP5X21 can sink or source 50mA. It can provide high values of peak current, and much reduced value of average current. When the output voltages are near the rails the ability to provide current will be reduced.

Output Power

Make sure that the rms power is such that the die junction temperature will remain below 125ºC.

Power Requirements

The MP5x20 family operates from a voltage supply, of ±Vs and ground, or from a Vs split supply. Dual supply range is ±1.6V to ±9.0V.

PSRR and Noise

A common figure of merit is the PSRR (Power Supply Rejection Ratio). The PSRR is a measure of how much noise gets from the supply rails into the output. Notice that the PSRR falls with increasing frequency. In order to have good PSRR the ripple voltages and frequencies of the systems switching power supplies should be measured. If the PSRR is not acceptable, inductors can be inserted in series with the power supply rails to provide improved PSRR. Also make sure there are no transients created on the power supply lines when the MP5X21 load current changes suddenly. This can damage the part.

Transients

In addition to the ripple and noise on the power supplies, there are also transient voltage changes. This can be caused by another device on the same power supply suddenly drawing current or suddenly stopping a current draw. The design engineer should insure that there are no damaging transients induced on the power supply lines when the op amp suddenly changes current delivery.

LAYOUT

Ground Plane

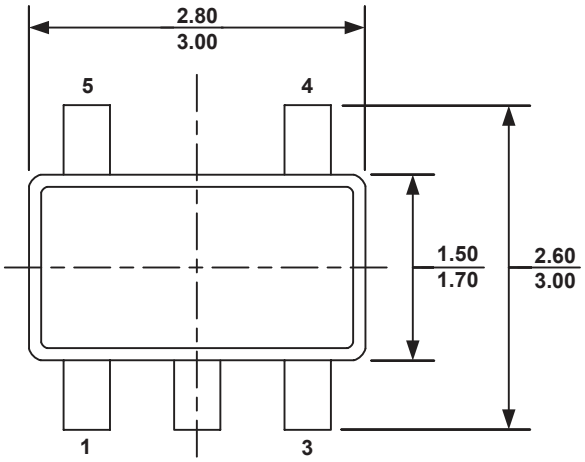
Connect the opamp to a ground plane rather than ground traces for very low impedance. If this is not possible then make the ground traces as fat and short as possible

Decoupling

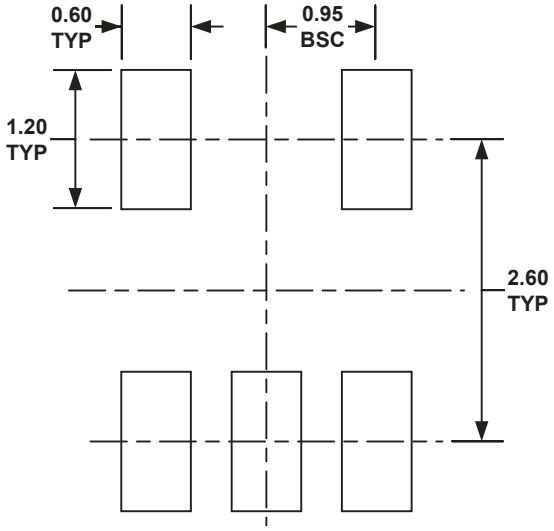
High performance devices such as the MP5X21, with high slew rates and high currents, need large decoupling capacitors. These should be placed as close to the supply pins as possible. Use ground and power planes to make these decoupling capacitors as effective as possible. If that is not realistic then make the ground and power traces as thick and short as possible.

PACKAGE INFORMATION

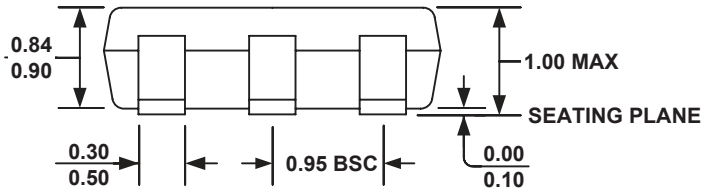
TSOT23-5



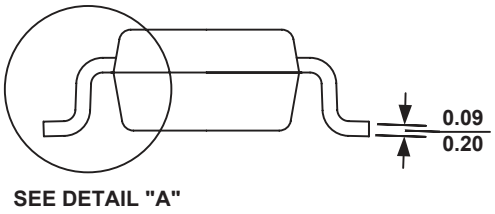
TOP VIEW



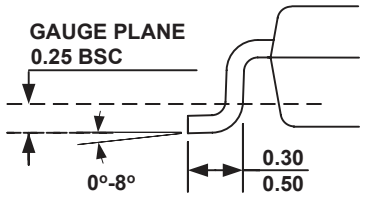
RECOMMENDED LAND PATTERN



FRONT VIEW



SIDE VIEW



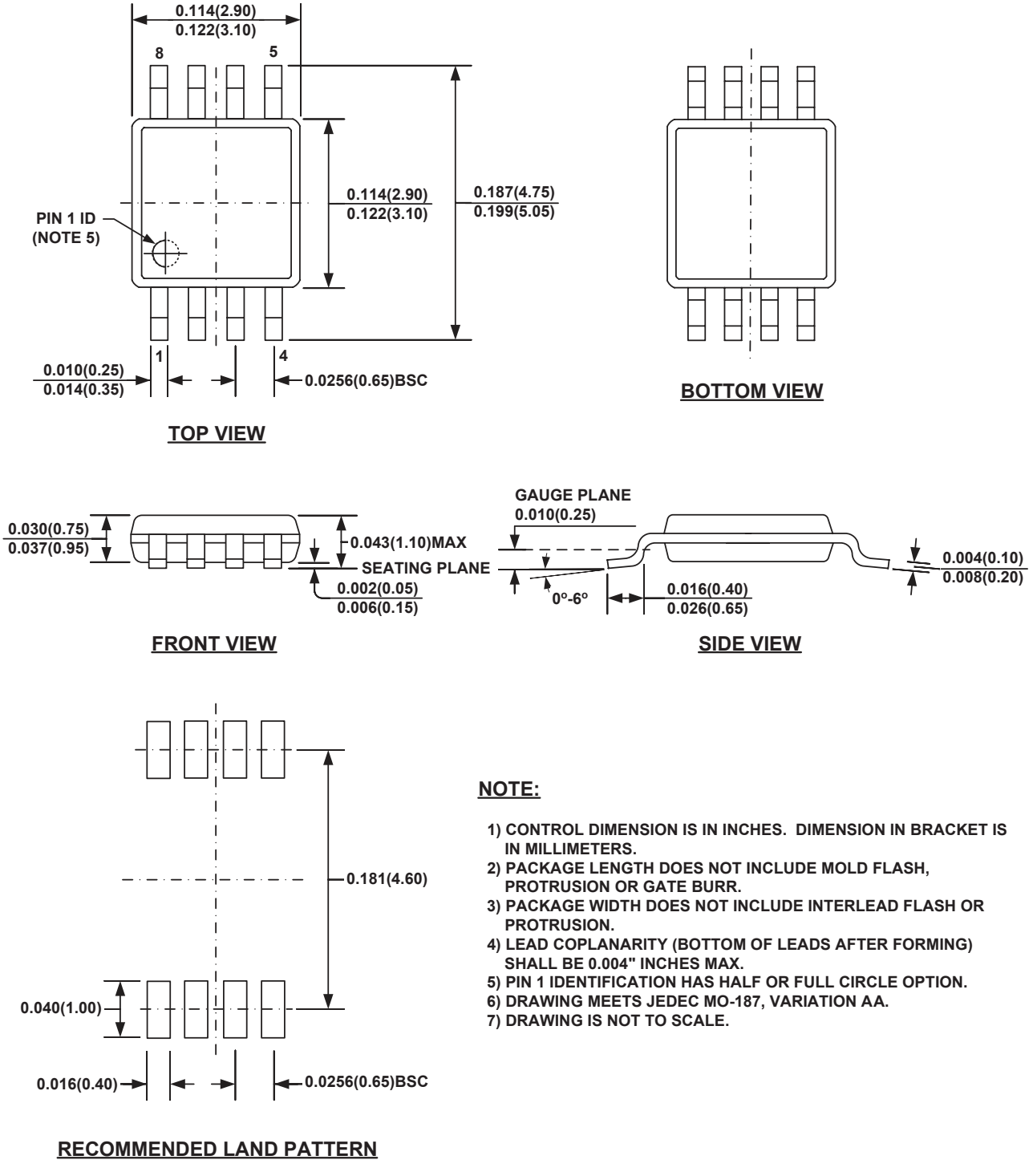
DETAIL "A"

NOTE:

1) ALL DIMENSIONS ARE IN MILLIMETERS.
2) PACKAGE LENGTH DOES NOT INCLUDE MOLD FLASH, PROTRUSION OR GATE BURR.
3) PACKAGE WIDTH DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION.
4) LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.10 MILLIMETERS MAX.
5) DRAWING CONFORMS TO JEDEC MO-193, VARIATION AA.
6) DRAWING IS NOT TO SCALE.

PACKAGE INFORMATION

MSOP8

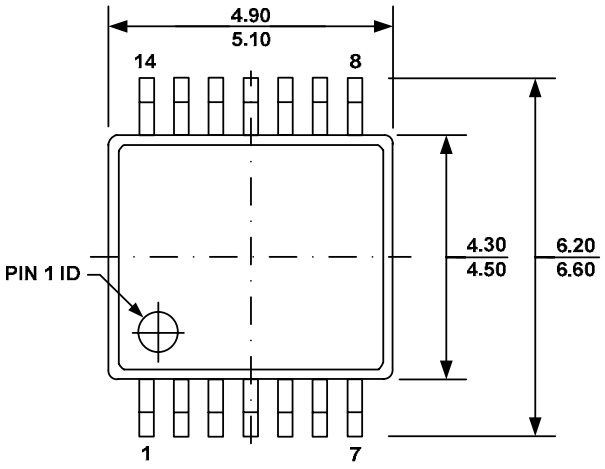


NOTE:

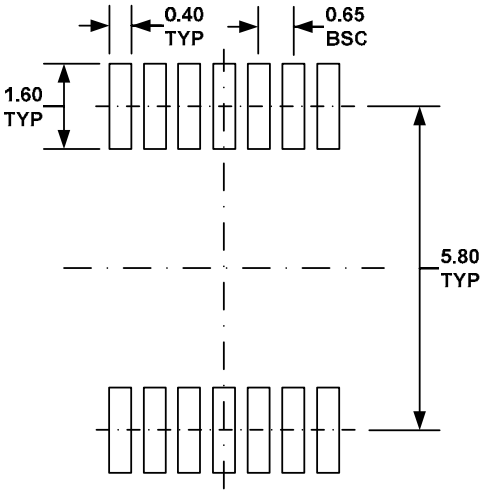
1) CONTROL DIMENSION IS IN INCHES. DIMENSION IN BRACKET IS IN MILLIMETERS.
2) PACKAGE LENGTH DOES NOT INCLUDE MOLD FLASH, PROTRUSION OR GATE BURR.
3) PACKAGE WIDTH DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION.
4) LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.004" INCHES MAX.
5) PIN 1 IDENTIFICATION HAS HALF OR FULL CIRCLE OPTION.
6) DRAWING MEETS JEDEC MO-187, VARIATION AA.
7) DRAWING IS NOT TO SCALE.

PACKAGE INFORMATION

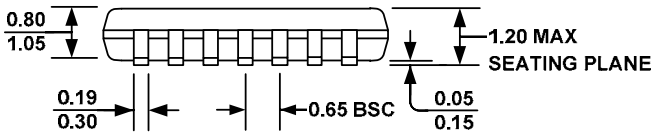
TSSOP14



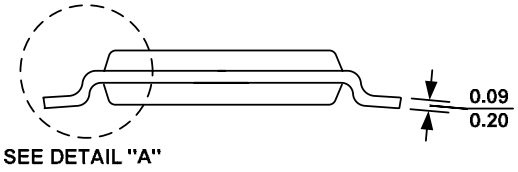
TOP VIEW



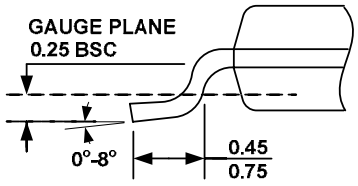
RECOMMENDED LAND PATTERN



FRONT VIEW



SIDE VIEW



DETAIL "A"

NOTE:

1) ALL DIMENSIONS ARE IN MILLIMETERS.
2) PACKAGE LENGTH DOES NOT INCLUDE MOLD FLASH, PROTRUSION OR GATE BURR.
3) PACKAGE WIDTH DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION.
4) LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.10 MILLIMETERS MAX.
5) DRAWING CONFORMS TO JEDEC MO-153, VARIATION AB-1.
6) DRAWING IS NOT TO SCALE.