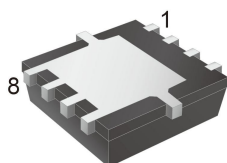


# TSM180N03PQ33

## 30V N-Channel Power MOSFET

### PDFN33



### Pin Definition:

- |           |          |
|-----------|----------|
| 1. Source | 8. Drain |
| 2. Source | 7. Drain |
| 3. Source | 6. Drain |
| 4. Gate   | 5. Drain |

### Key Parameter Performance

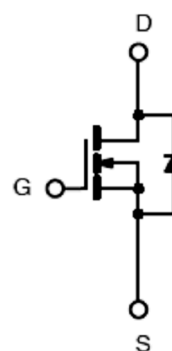
Parameter	Value	Unit
$V_{DS}$	30	V
$R_{DS(on)}$ (max)	$V_{GS} = 10V$	18
	$V_{GS} = 4.5V$	28
$Q_g$	4.1	nC

### Ordering Information

Part No.	Package	Packing
TSM180N03PQ33 RGG	PDFN33	5Kpcs / 13+Reel

**Note:** %G+denotes for Halogen- and Antimony-free as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds

### Block Diagram



N-Channel MOSFET

### Absolute Maximum Ratings ( $T_C = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	$T_C = 25^\circ C$	25
		$T_C = 100^\circ C$	16
Pulsed Drain Current <sup>(Note 1)</sup>	$I_{DM}$	100	A
Single Pulse Avalanche Energy <sup>(Note 2)</sup>	$E_{AS}$	32	mJ
Power Dissipation @ $T_C = 25^\circ C$	$P_D$	21	W
Operating Junction Temperature	$T_J$	+150	$^\circ C$
Storage Temperature Range	$T_{STG}$	-55 to +150	$^\circ C$

### Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance - Junction to Ambient	$R_{JA}$	62	$^\circ C/W$
Thermal Resistance - Junction to Case	$R_{JC}$	6	$^\circ C/W$

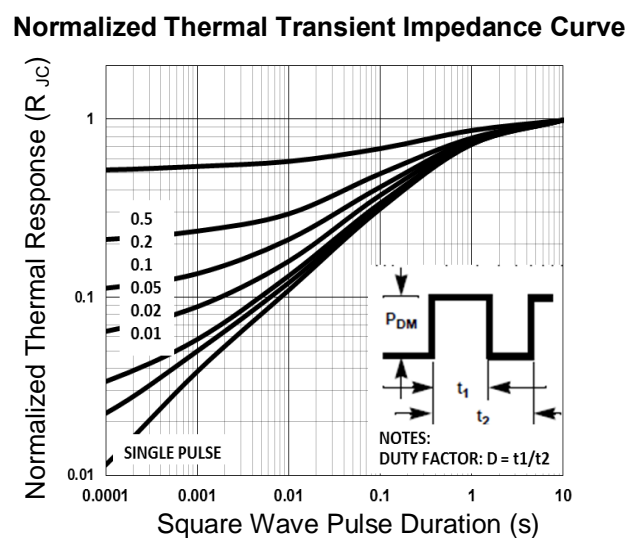
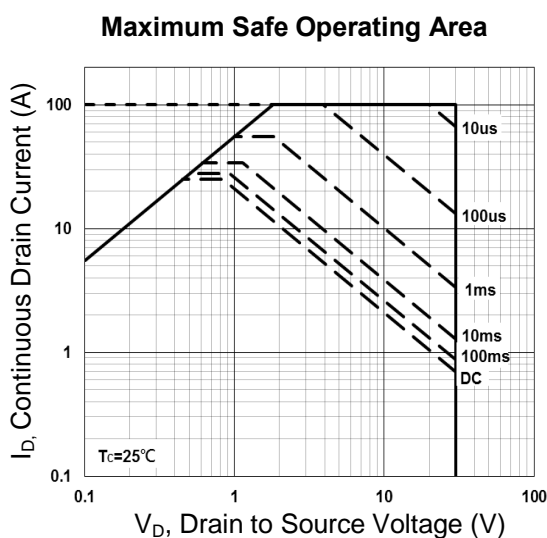
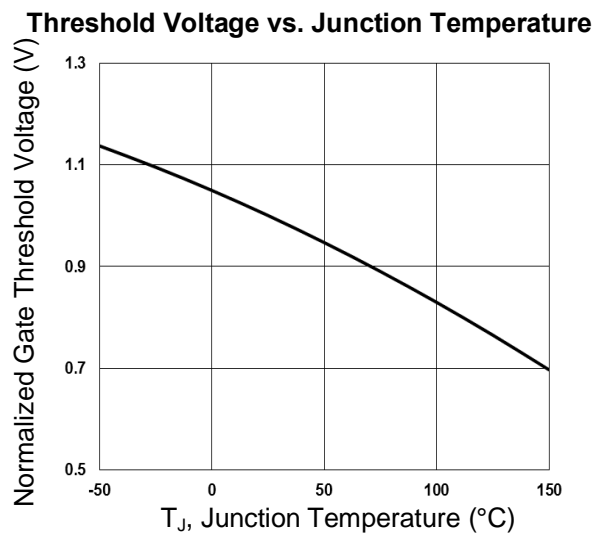
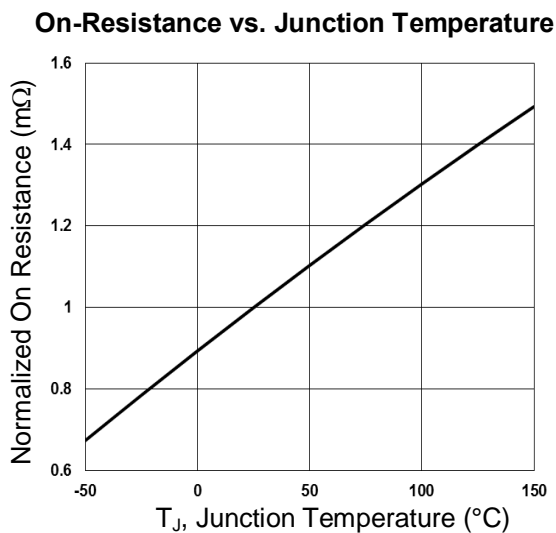
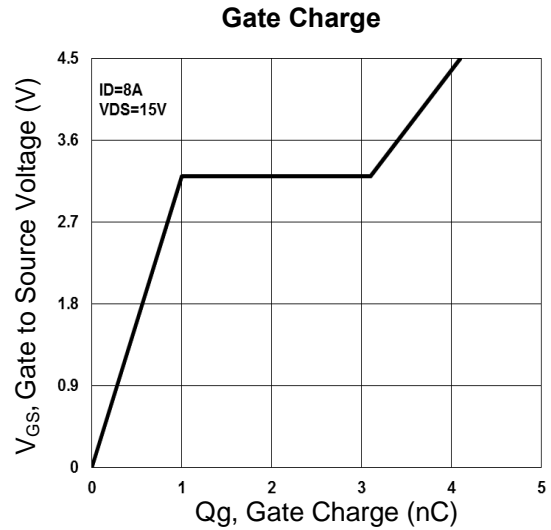
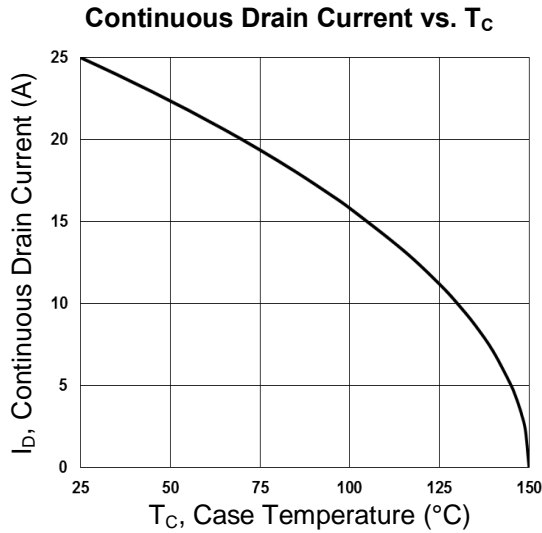
### Electrical Specifications ( $T_C = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Conditions	Symbol	Min	Typ	Max	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	$BV_{DSS}$	30	--	--	V
Drain-Source On-State Resistance	$V_{GS} = 10V, I_D = 12A$	$R_{DS(ON)}$	--	14	18	m
	$V_{GS} = 4.5V, I_D = 8A$		--	20	28	
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	$V_{GS(TH)}$	1.2	1.6	2.5	V
Zero Gate Voltage Drain Current	$V_{DS} = 30V, V_{GS} = 0V$	$I_{DSS}$	--	--	1	$\mu A$
	$V_{DS} = 24V, T_J = 125^\circ C$		--	--	10	
Gate Body Leakage	$V_{GS} = \pm 20V, V_{DS} = 0V$	$I_{GSS}$	--	--	$\pm 100$	nA
Forward Transconductance <sup>(Note 3)</sup>	$V_{DS} = 10V, I_D = 6A$	$g_{fs}$	--	6.5	--	S
<b>Dynamic</b>						
Total Gate Charge <sup>(Note 3,4)</sup>	$V_{DS} = 15V, I_D = 6A,$ $V_{GS} = 4.5V$	$Q_g$	--	4.1	--	nC
Gate-Source Charge <sup>(Note 3,4)</sup>		$Q_{gs}$	--	1	--	
Gate-Drain Charge <sup>(Note 3,4)</sup>		$Q_{gd}$	--	2.1	--	
Input Capacitance	$V_{DS} = 25V, V_{GS} = 0V,$ $f = 1.0MHz$	$C_{iss}$	--	345	--	pF
Output Capacitance		$C_{oss}$	--	55	--	
Reverse Transfer Capacitance		$C_{rss}$	--	32	--	
<b>Switching</b>						
Turn-On Delay Time <sup>(Note 3,4)</sup>	$V_{DD} = 15V, I_D = 1A,$ $V_{GS} = 10V, R_G = 6$	$t_{d(on)}$	--	2.8	--	ns
Turn-On Rise Time <sup>(Note 3,4)</sup>		$t_r$	--	7.2	--	
Turn-Off Delay Time <sup>(Note 3,4)</sup>		$t_{d(off)}$	--	15.8	--	
Turn-Off Fall Time <sup>(Note 3,4)</sup>		$t_f$	--	4.6	--	
<b>Source-Drain Diode Ratings and Characteristic</b>						
Maximum Continuous Drain-Source Diode Forward Current	Integral reverse diode in the MOSFET	$I_S$	--	--	25	A
Maximum Pulse Drain-Source Diode Forward Current		$I_{SM}$	--	--	100	A
Diode-Source Forward Voltage	$V_{GS} = 0V, I_S = 1A$	$V_{SD}$	--	--	1	V

#### Note:

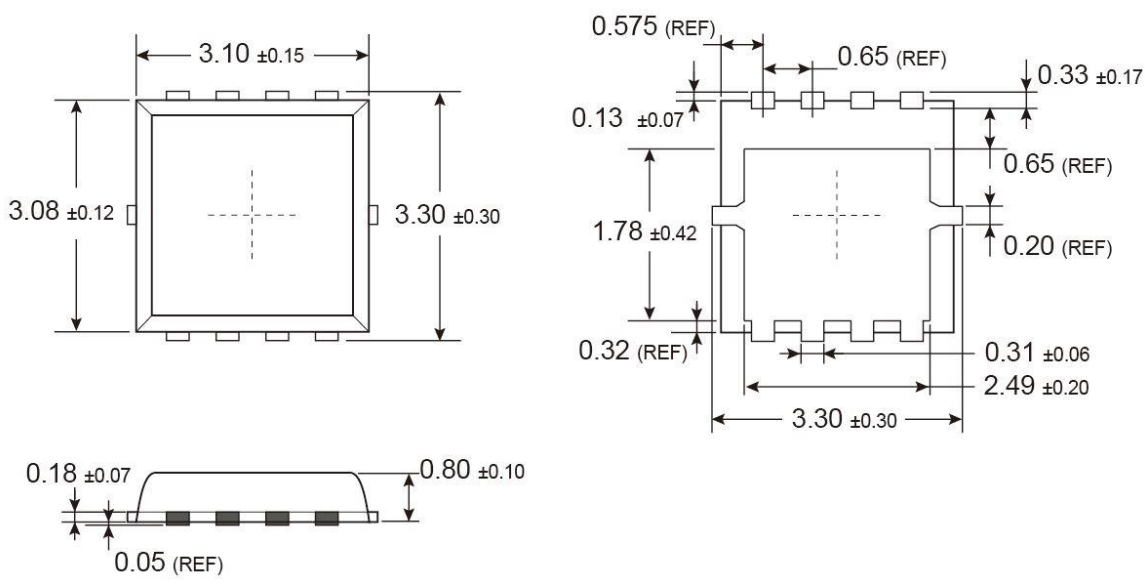
1. Pulse width limited by safe operating area
2.  $L = 1mH, I_{AS} = 8A, V_{DD} = 25V, R_G = 25$  , Starting  $T_J = 25^\circ C$
3. Pulse test: pulse width  $m300\mu s$ , duty cycle  $m2\%$
4. Switching time is essentially independent of operating temperature.

### Electrical Characteristics Curve



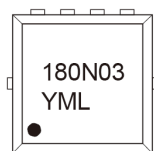


**PDFN33 Mechanical Drawing**



Unit: Millimeters

**Marking Diagram**



- Y** = Year Code
- M** = Month Code for Halogen Free Product  
(**O**=Jan, **P**=Feb, **Q**=Mar, **R**=Apr, **S**=May, **T**=Jun, **U**=Jul, **V**=Aug, **W**=Sep, **X**=Oct, **Y**=Nov, **Z**=Dec)
- L** = Lot Code

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