CMLT2222AG

SURFACE MOUNT SILICON **DUAL NPN TRANSISTOR**



• Device is Halogen Free by design



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMLT2222AG consists of two (2) isolated 2222A NPN silicon transistors, manufactured by the epitaxial planar process and epoxy molded in an SOT-563 surface mount package. These devices have been designed for small signal general purpose and switching applications.

MARKING CODE: 2CG

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Collector-Base Voltage	V _{CBO}	75	V
Collector-Emitter Voltage	VCEO	40	V
Emitter-Base Voltage	V_{EBO}	6.0	V
Continuous Collector Current	I _C	600	mA
Power Dissipation (Note 1)	P_{D}	350	mW
Power Dissipation (Note 2)	P_{D}	300	mW
Power Dissipation (Note 3)	P_{D}	150	mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	Θ.ΙΔ	357	°C/W

ELECTRICAL CHARACTERISTICS PER TRANSISTOR: (TA=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I _{CBO}	V _{CB} =60V		10	nA
I _{CBO}	V _{CB} =60V, T _A =125°C		10	μΑ
ICEV	V_{CE} =60V, V_{EB} =3.0V		10	nA
I _{EBO}	V _{EB} =3.0V		10	nA
BV _{CBO}	I _C =10μA	75		V
BVCEO	I _C =10mA	40		V
BV _{EBO}	I _E =10μΑ	6.0		V
VCE(SAT)	I _C =150mA, I _B =15mA		0.3	V
VCE(SAT)	I _C =500mA, I _B =50mA		1.0	V
V _{BE(SAT)}	I _C =150mA, I _B =15mA	0.6	1.2	V
V _{BE} (SAT)	I _C =500mA, I _B =50mA		2.0	V
h _{FE} ` ′	V_{CE} =10V, I_{C} =0.1mA	35		
h _{FE}	V _{CE} =10V, I _C =1.0mA	50		
hFE	V _{CE} =10V, I _C =10mA	75		
hFE	V _{CE} =1.0V, I _C =150mA	50		
h _{FE}	V _{CE} =10V, I _C =150mA	100	300	
hFE	V _{CE} =10V, I _C =500mA	40		

Notes: (1) Ceramic or aluminum core PC Board with copper mounting pad area of 4.0mm² (2) FR-4 Epoxy PC Board with copper mounting pad area of 4.0mm²

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⁽³⁾ FR-4 Epoxy PC Board with copper mounting pad area of 1.4mm²

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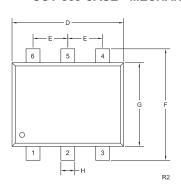


 $\textbf{ELECTRICAL CHARACTERISTICS PER TRANSISTOR - Continued:} \ (T_{A} = 25^{\circ}\text{C unless otherwise noted})$

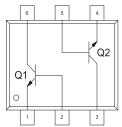
		()	4	
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
f_{T}	V _{CE} =20V, I _C =20mA, f=100MHz	300		MHz
C _{ob}	V _{CB} =10V, I _E =0, f=1.0MHz		8.0	pF
C _{ib}	V _{EB} =0.5V, I _C =0, f=1.0MHz		25	pF
h _{ie}	V _{CE} =10V, I _C =1.0mA, f=1.0kHz	2.0	8.0	kΩ
h _{ie}	V _{CE} =10V, I _C =10mA, f=1.0kHz	0.25	1.25	kΩ
h _{re}	V _{CE} =10V, I _C =1.0mA, f=1.0kHz		8.0	x10 ⁻⁴
h _{re}	V _{CE} =10V, I _C =10mA, f=1.0kHz		4.0	x10 ⁻⁴
h _{fe}	V _{CE} =10V, I _C =1.0mA, f=1.0kHz	50	300	
h _{fe}	V _{CE} =10V, I _C =10mA, f=1.0kHz	75	375	
h _{oe}	V _{CE} =10V, I _C =1.0mA, f=1.0kHz	5.0	35	μS
h _{oe}	V _{CE} =10V, I _C =10mA, f=1.0kHz	25	200	μS
rb'C _C	V _{CB} =10V, I _E =20mA, f=31.8MHz		150	ps
NF	V_{CE} =10V, I_{C} =100mA, R_{S} =1.0k Ω , f=1.0kHz		4.0	dB
^t d	V _{CC} =30V, V _{BE} =0.5, I _C =150mA, I _{B1} =15mA		10	ns
t _r	V _{CC} =30V, V _{BE} =0.5, I _C =150mA, I _{B1} =15mA		25	ns
t _S	V_{CC} =30V, I_{C} =150mA, I_{B1} = I_{B2} =15mA		225	ns
t _f	V _{CC} =30V, I _C =150mA, I _{B1} =I _{B2} =15mA		60	ns

SOT-563 CASE - MECHANICAL OUTLINE





DIMENSIONS					
	INCHES		MILLIMETERS		
SYMBOL	MIN	MAX	MIN	MAX	
Α	0.0027	0.007	0.07	0.18	
В	0.008		0.20		
С	0.017	0.024	0.45	0.60	
D	0.059	0.067	1.50	1.70	
E	0.020		0.50		
F	0.059	0.067	1.50	1.70	
G	0.043	0.051	1.10	1.30	
H	0.006	0.012	0.15	0.30	
SOT-563 (REV: R2)					



LEAD CODE:

- 1) Emitter Q1
- 2) Base Q1 3) Collector Q2
- 4) Emitter Q2
- 5) Base Q2
- 6) Collector Q1

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SURFACE MOUNT SILICON DUAL NPN TRANSISTOR



SERVICES

- · Bonded Inventory
- · Custom Electrical Screening
- Custom Electrical Characteristic Curves
- SPICE Models
- Custom Packaging
- Package Base Options
- Custom Device Development/Multi Discrete Modules (MDM™)
- · Bare Die Available for Hybrid Applications

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