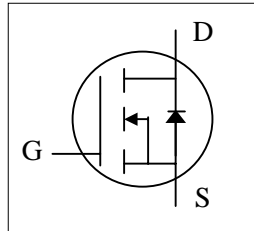




- ▼ Simple Drive Requirement
- ▼ Ultra-low On-resistance
- ▼ Fast Switching Characteristic

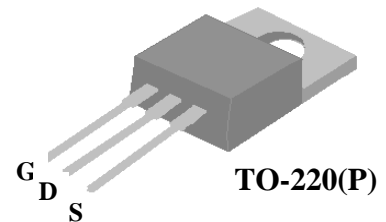


$BV_{DSS}$	30V
$R_{DS(ON)}$	2.8m $\Omega$
$I_D$	200A

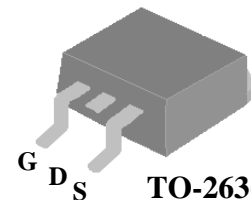
## Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-263 package is widely preferred for commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters. The through-hole version (AP98T03GP) are available for low-profile applications.



**TO-220(P)**



**TO-263(S)**

## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_C = 25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^3$	200	A
$I_D @ T_C = 100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V$	125	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	800	A
$P_D @ T_C = 25^\circ\text{C}$	Total Power Dissipation	156	W
	Linear Derating Factor	1.25	W/ $^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

## Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Maximum Thermal Resistance, Junction-case	0.8	$^\circ\text{C}/\text{W}$
Rthj-a	Maximum Thermal Resistance, Junction-ambient	62	$^\circ\text{C}/\text{W}$



# AP98T03GP/S

## Electrical Characteristics @T<sub>j</sub>=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30	-	-	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =40A	-	-	2.8	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =30A	-	-	4	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	0.8	-	2.5	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =30A	-	30	-	S
I <sub>DSS</sub>	Drain-Source Leakage Current (T <sub>j</sub> =25°C)	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V	-	-	1	uA
	Drain-Source Leakage Current (T <sub>j</sub> =150°C)	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V	-	-	25	uA
I <sub>GSS</sub>	Gate-Source Leakage	V <sub>GS</sub> = ±20V	-	-	±100	nA
Q <sub>g</sub>	Total Gate Charge <sup>2</sup>	I <sub>D</sub> =30A	-	71	115	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =24V	-	9	-	nC
Q <sub>gd</sub>	Gate-Drain ("Miller") Charge	V <sub>GS</sub> =4.5V	-	41	-	nC
t <sub>d(on)</sub>	Turn-on Delay Time <sup>2</sup>	V <sub>DS</sub> =15V	-	14	-	ns
t <sub>r</sub>	Rise Time	I <sub>D</sub> =30A	-	78	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time	R <sub>G</sub> =3.3Ω, V <sub>GS</sub> =10V	-	74	-	ns
t <sub>f</sub>	Fall Time	R <sub>D</sub> =0.5Ω	-	136	-	ns
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V	-	4960	7940	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =25V	-	1210	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	f=1.0MHz	-	1200	-	pF
R <sub>g</sub>	Gate Resistance	f=1.0MHz	-	1	1.5	Ω

## Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V <sub>SD</sub>	Forward On Voltage <sup>2</sup>	I <sub>S</sub> =40A, V <sub>GS</sub> =0V	-	-	1.3	V
t <sub>rr</sub>	Reverse Recovery Time <sup>2</sup>	I <sub>S</sub> =10A, V <sub>GS</sub> =0V	-	54	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=100A/μs	-	74	-	nC

### Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Package limitation current is 80A, calculated continuous current based on maximum allowable junction temperature is 200A.

THIS PRODUCT IS AN ELECTROSTATIC SENSITIVE, PLEASE HANDLE WITH CAUTION.

THIS PRODUCT HAS BEEN QUALIFIED FOR CONSUMER MARKET. APPLICATIONS OR USES AS CRITERIAL COMPONENT IN LIFE SUPPORT DEVICE OR SYSTEM ARE NOT AUTHORIZED.

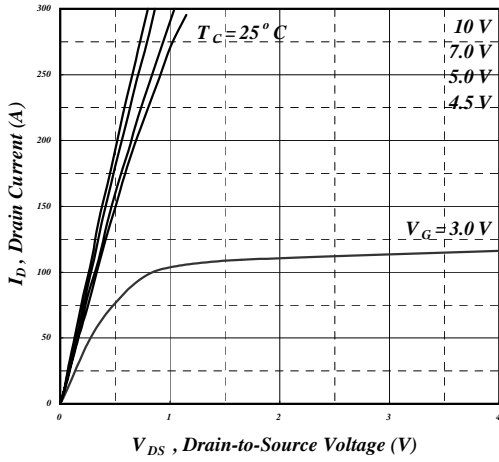


Fig 1. Typical Output Characteristics

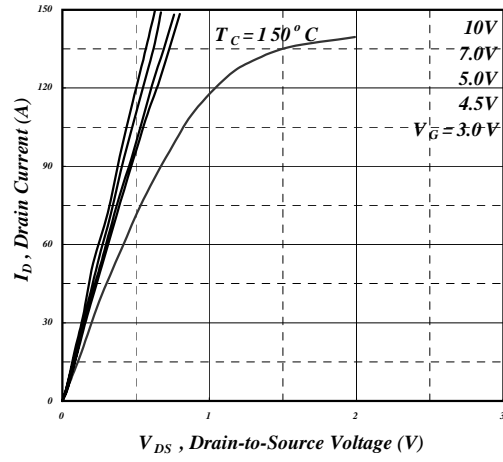


Fig 2. Typical Output Characteristics

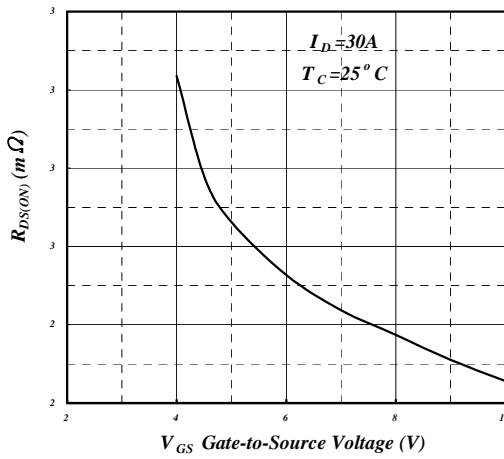


Fig 3. On-Resistance v.s. Gate Voltage

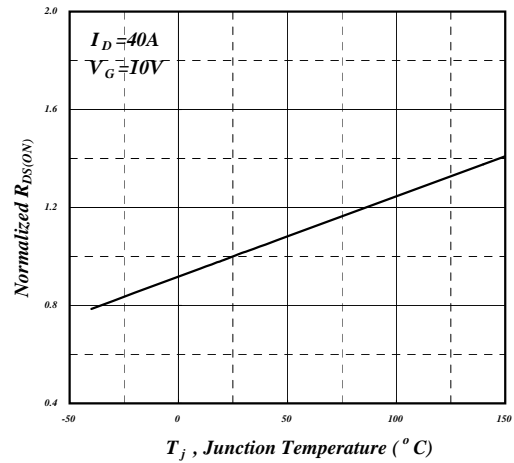


Fig 4. Normalized On-Resistance v.s. Junction Temperature

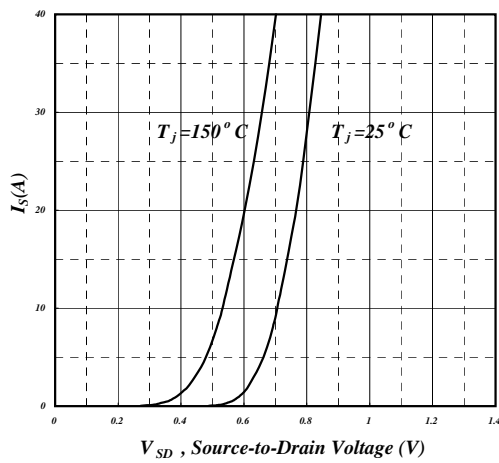


Fig 5. Forward Characteristic of Reverse Diode

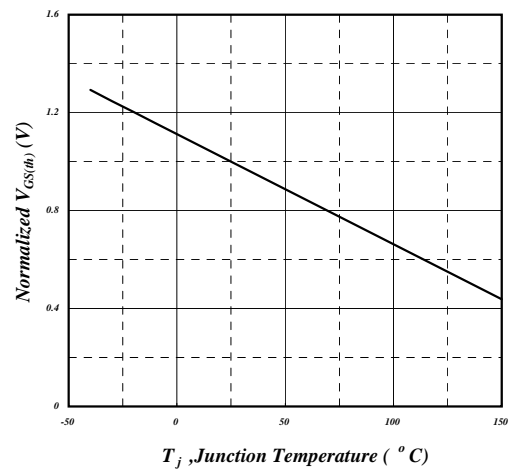


Fig 6. Gate Threshold Voltage v.s. Junction Temperature



# AP98T03GP/S

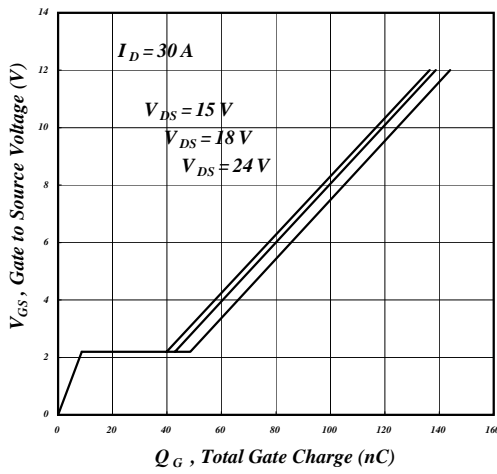


Fig 7. Gate Charge Characteristics

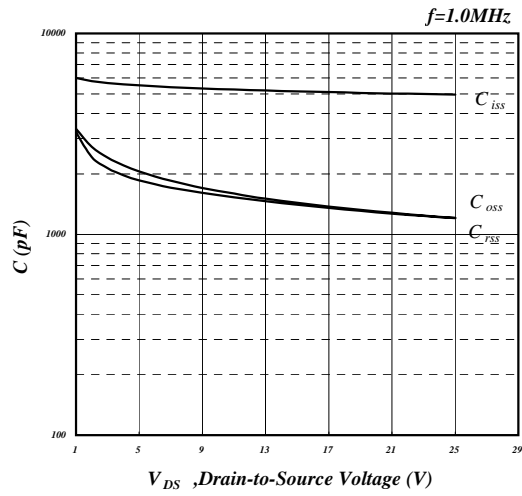


Fig 8. Typical Capacitance Characteristics

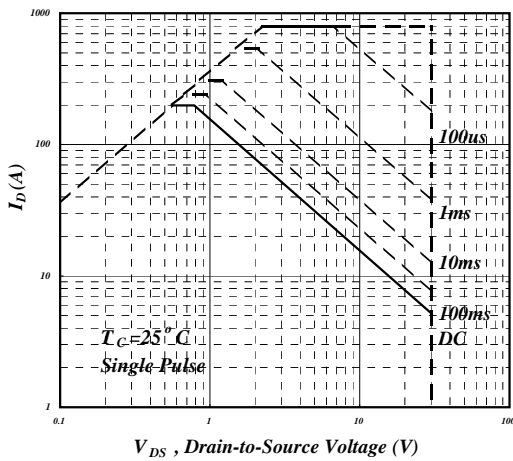


Fig 9. Maximum Safe Operating Area

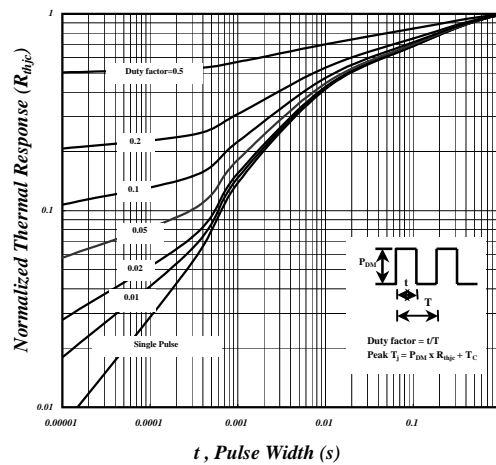


Fig 10. Effective Transient Thermal Impedance

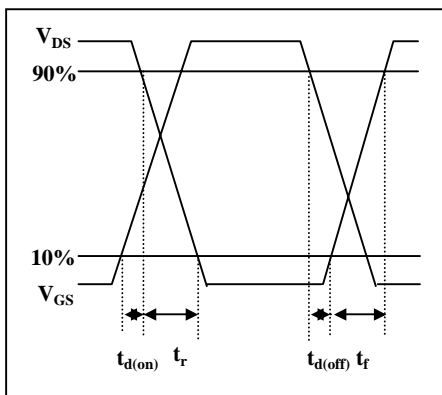


Fig 11. Switching Time Waveform

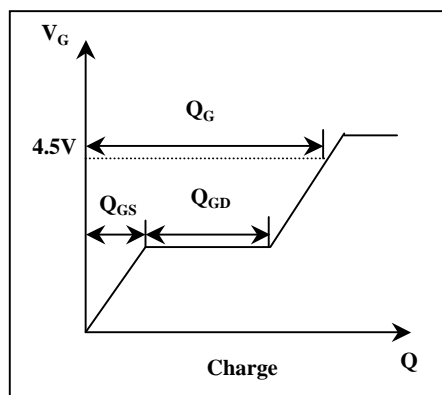
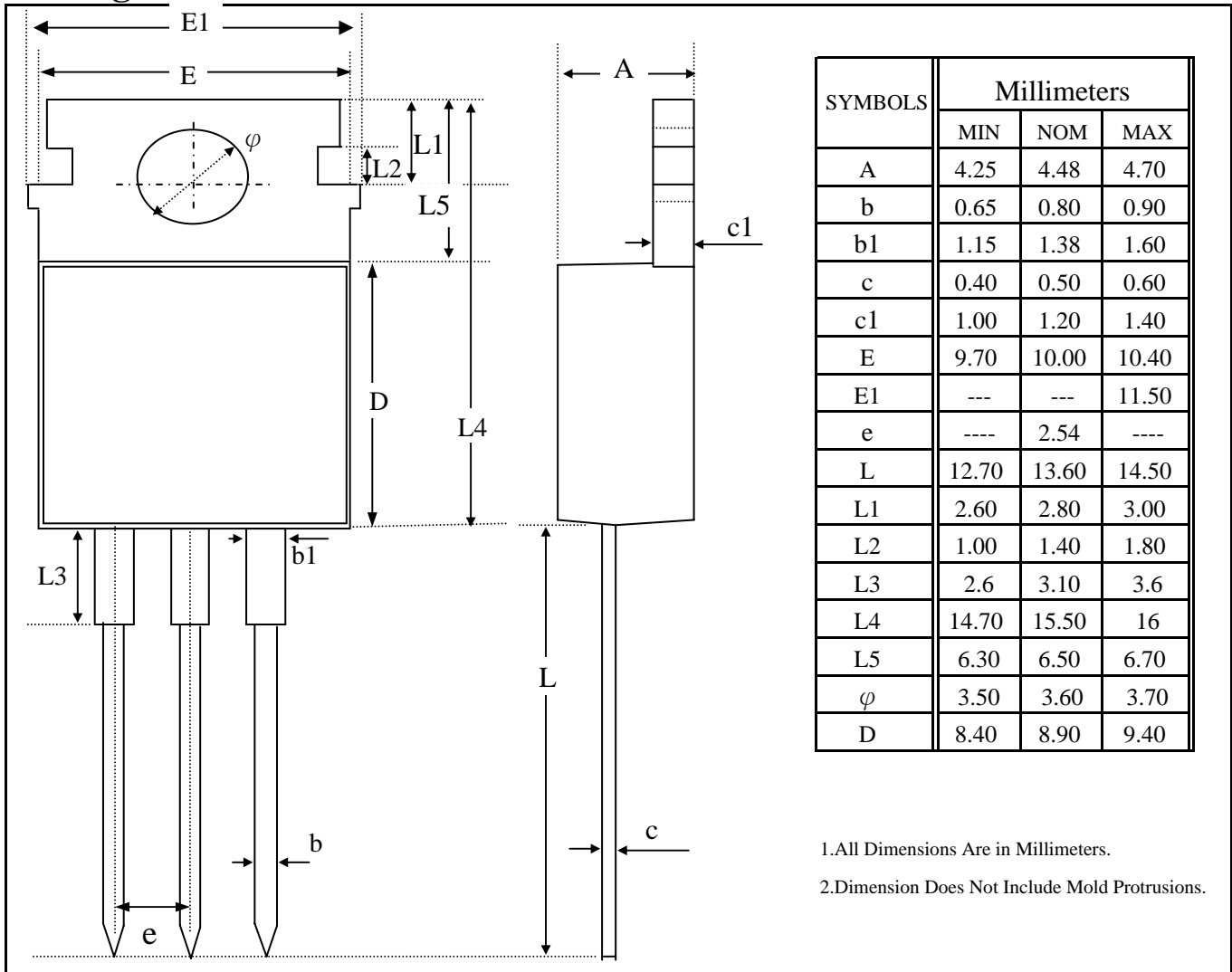


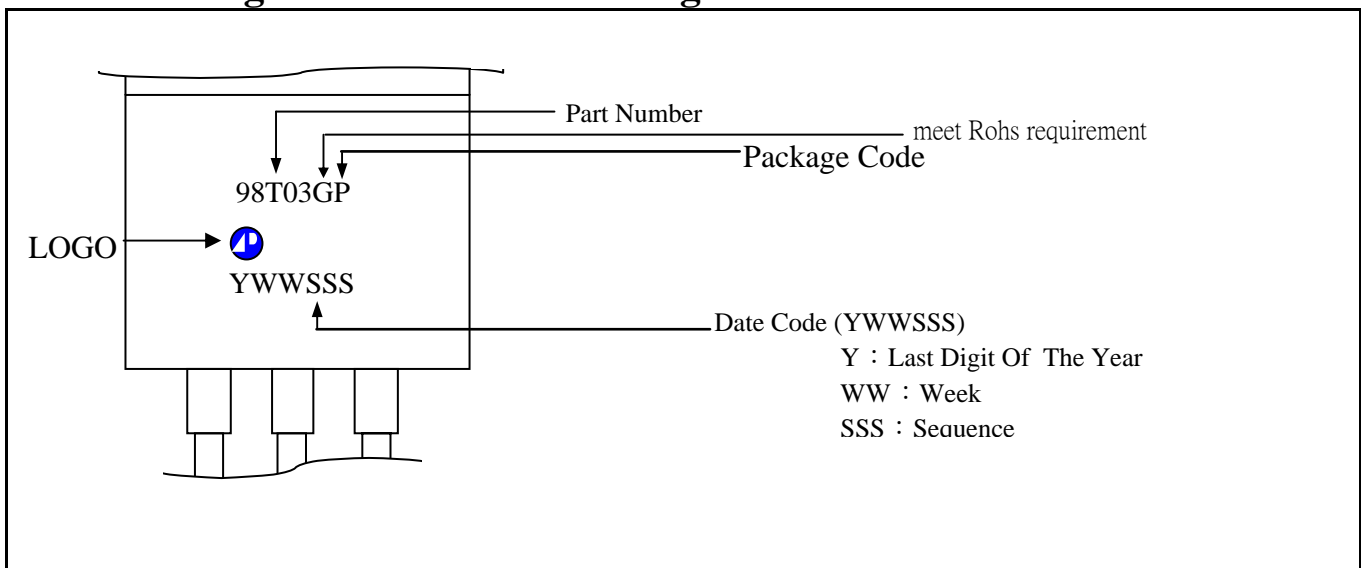
Fig 12. Gate Charge Waveform



### Package Outline : TO-220

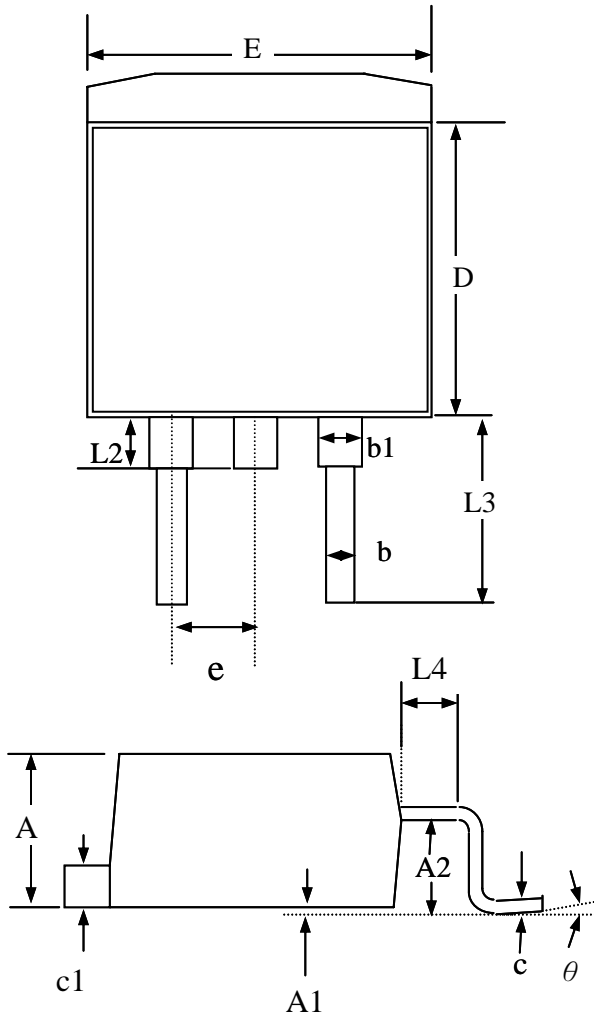


### Part Marking Information & Packing : TO-220





## Package Outline : TO-263



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	4.25	4.75	5.20
A1	0.00	0.15	0.30
A2	2.20	2.45	2.70
b	0.70	0.90	1.10
b1	1.07	1.27	1.47
c	0.30	0.45	0.60
c1	1.15	1.30	1.45
D	8.30	8.90	9.40
E	9.70	10.10	10.50
e	2.04	2.54	3.04
L2	-----	1.50	-----
L3	4.50	4.90	5.30
L4	-----	1.50	----

- 1.All Dimensions Are in Millimeters.
- 2.Dimension Does Not Include Mold Protrusions.

## Part Marking Information & Packing : TO-263

