

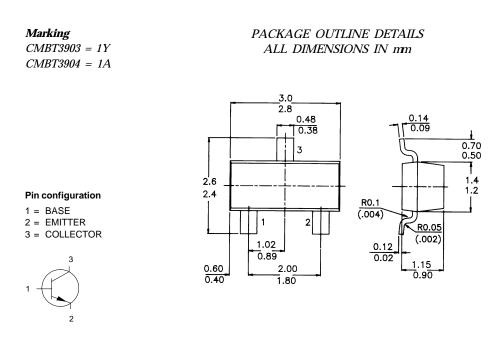


SOT-23 Formed SMD Package

CMBT3903 CMBT3904

SILICON EPITAXIAL TRANSISTORS

N-P-N transistors



ABSOLUTE MAXIMUM RATINGS

Collector-base voltage (open emitter)		V _{CB0}	max.	60 V
Collector-emitter voltage (open base)		V _{CE0}	max.	40 V
Emitter-base voltage (open collector)		V _{EB0}	max.	6 V
Collector current (DC)		I_C	max.	200 mA
Total power dissipation up to $T_{amb} = 25 \ ^{\circ}C$		P _{tot}	max.	250 mW
DC current gain	CMBT3903		>	50
$I_C = 10 \text{ mA}; V_{CE} = 1 \text{ V}$		h_{FE}	<	150
	CMBT3904		>	100
$I_C = 10 mA; V_{CE} = 1 V$		h _{FE}	<	300
Transition frequency at $f = 35$ MHz				
$I_C = 10 mA; V_{CE} = 20 V$		f_T	>	<i>300</i> MHz

CMBT3903 CMBT3904

RATINGS (at $T_A = 25^{\circ}C$ unless otherwise specified)			
Limiting values			
Collector-base voltage (open emitter)	VCB0	max.	60 V
Collector-emitter voltage (open base)	V_{CE0}	max.	40 V
Emitter-base voltage (open collector)	V_{EB0}	max.	6 V
Collector current (d.c.)	I_C	max.	200 mA
Total power dissipation			
up to $T_{amb} = 25 \ ^{\circ}C$	P _{tot}	max.	250 mW
Storage temperature	Tstg	–55 ta	+150
°C			
Junction temperature	T_j	max.	150 °C
THERMAL RESISTANCE			
$T_j = P (R_{th j-t} + R_{th t-s} + R_{th s-a}) + T_{amb}$			
Thermal resistance			
from junction to ambient	R _{th j-a}	=	<i>500</i> K/W
CHARACTERISTICS			
$T_{amb} = 25 \ ^{\circ}C \ unless \ otherwise \ specified$			
1 amb 25 C unless onlerwise specificu			
Collector–emitter breakdown voltage			
$I_C = 1 mA; l_B = 0$	V(BR)CEO	min.	40 V
Collector-base breakdown voltage			
$I_{C} = 10 \mu A; I_{E} = 0$	V(BR)CBO	min.	60 V
Emitter-base breakdown voltage			
$I_E = 10 \mu A; I_C = 0$	V _{(BR)EBO}	min.	6 V
Collector cut-off current	(210)2200		
$V_{CE} = 30 V; V_{EB} = 3 V$	ICEX	max.	50 nA
Output capacitance at $f = 1$ MHz	CLM		
$I_E = 0; V_{CB} = 5 V$	C_{c}	max.	4 pF
Input capacitance at $f = 1$ MHz	-		
$I_C = 0; V_{BE} = 0.5 V$	C_e	max.	8 pF
Base current	e		1
with reverse biased emitter junction			
$V_{EB} = 3 V; V_{CE} = 30 V$	IBEX	max.	50 nA
Saturation voltages			
$I_C = 10 mA; I_B = 1 mA$	V _{CEsat}	max.	0.2 V
$I_C = 50 \text{ mA}; I_B = 5 \text{ mA}$	V _{CEsat}	max.	0.3 V
	• CLSdl		
$I_C = 10 \ mA; \ I_B = 1 \ mA$	V _{BEsat}	min.	0.65 V
υ	· DESdl	max.	0.85 V
$I_{C} = 50 mA; l_{B} = 5 mA$	VBEsat	max.	0.95 V

CMBT3903 CMBT3904

		CMBT3903	CMBT3904
D.C. current gain *			
$I_C = 0.1 mA; V_{CE} = 1 V$	h_{FE}	> 20	40
$I_C = 1 mA; V_{CE} = 1 V$	h_{FE}	> 35	70
$I_C = 10 mA; V_{CE} = 1 V$	h _{FE}	> 50	100
		< 150	300
$I_C = 50 mA; V_{CE} = 1 V$	h _{FE}	> 30	60
$I_C = 100 \text{ mA}; V_{CE} = 1 \text{ V}$	h_{FE}	> 15	30
Transition frequency at $f = 100 \text{ MHz}$			
$I_C = 10 \text{ mA}; V_{CE} = 20 \text{ V}$	f_T	min. 250	<i>300</i> MHz
Noise figure at $R_S = 1 \ k\Omega$			
$I_C = 100 \mu A; V_{CE} = 5 V$			
f = 10 Hz to 15,7 kHz	F	<i>max.</i> 6	5 dB
Small Signal Current Gain			
$V_{CE} = 10V; I_C = 1 \text{ mA}; f = 1 \text{ KHz}$	h _{fe}	<i>min. 50</i>	100
	-	max. 200	400

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/ CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered Trademark of Continental Device India Limited C-120 Naraina Industrial Area, New Delhi 110 028, India. Telephone + 91-11-2579 6150, 5141 1112 Fax + 91-11-2579 5290, 5141 1119 email@cdil.com www.cdilsemi.com