

Continental Device India Limited

An ISO/TS16949 and ISO 9001 Certified Company



SOT-23 Formed SMD Package

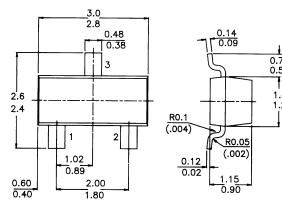
CMBT4125

GENERAL PURPOSE TRANSISTOR

P-N-P transistor

Marking CMBT4125 = 5D

PACKAGE OUTLINE DETAILS
ALL DIMENSIONS IN mm



Pin configuration

1 = BASE

2 = EMITTER

3 = COLLECTOR



ABSOLUTE MAXIMUM RATINGS

Collector-base voltage (open emitter)	$-V_{CBO}$	max.	30	V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	30	V
Emitter-base voltage (open collector)	$-V_{EBO}$	max.	4	V
Collector current (d.c.)	$-I_C$	max.	<i>200</i>	mA
Total power dissipation at $T_{amb} = 25^{\circ}C$	P_{tot}	max	<i>350</i>	mW
D.C. current gain				
$-I_C = 2 \text{ mA}; -V_{CE} = 1 \text{ V}$	h	min.	<i>50</i>	
-1C - 2 mA, -vCE - 1 v	hFE	max.	<i>150</i>	

RATINGS (at $T_A = 25^{\circ}C$ unless otherwise specified)

Limiting values

Collector-base voltage (open emitter) $-V_{CBO}$ max. 30 V Collector-emitter voltage (open base) $-V_{CEO}$ max. 30 V

Emitter-base voltage (open collector) Collector current (d.c.) Total power dissipation at $T_{amb} = 25^{\circ}C$	$-V_{EBO}$ $-I_{C}$ P_{tot}	max. max. max	4 200 350	V mA mW
Storage temperature	T_{Stg}		to +150	° C
Junction temperature	Tj	max.	150	° C
THERMAL CHARACTERISTICS				
$T_j = P (R_{th j-t} + R_{th s-a}) + T_{amb}$				
Thermal resistance	D		7.7.0	00/ 11/
from junction to ambient	$R_{th\ j-a}$		556	°C/mW
CHARACTERISTICS (at $T_A = 25^{\circ}C$ unless otherwise	e specified)			
Collector-emitter breakdown voltage $-I_C = 1 \text{ mA}$; $I_B = 0$	Vanaga	min	30	V
-1C = 1 mA, 1B = 0 Collector-base breakdown voltage	-V _(BR) CEO	111111.	30	V
$-I_C = 10 \text{ mA}; I_E = 0$	-Van and	min	30	V
Emitter-base breakdown voltage	−V _(BR) CBO	111111.	30	V
$-I_E = 10 \text{ mA}; I_C = 0$	-V _{(BR)EBO}	min	4	V
Collector cut-off current	V (DK)EDU	111111.		•
$-V_{CB} = 20 \text{ V}; I_E = 0 \text{ V}$	$-I_{CBO}$	max.	50	nA
Emitter cut-off current	-000			
$V_{BE} = 3 \ V; I_{C} = 0$	I_{EBO}	max.	50	nΑ
Output capacitance at $f = 100 \text{ kHz}$	LDC			
$I_E = 0$; $-V_{CB} = 5 V$	C_{c}	max.	4.5	рF
Input capacitance at $f = 100 \text{ kHz}$	·			1
$I_C = 0$; $-V_{BE} = 0.5 V$	C_e	max.	10	pF
Saturation voltages				
$-I_C = 50 \text{ mA; } -I_B = 5 \text{ mA}$	-V _{CEsat}	max.	0.4	V
$-I_C = 50 \text{ mA}; -I_B = 5 \text{ mA}$ $-I_C = 50 \text{ mA}; -I_B = 5 \text{ mA}$	-V _{BEsat}	max.	0.95	\overline{V}
D.C. current gain	• DESat	111421.	0.00	•
$-I_C = 2 \text{ mA; } -V_{CE} = 1 \text{ V}$	h_{FE}	min.	50	
-1C - 2 mA, -vCE - 1 v	11FE	max.	150	
$-I_C = 50 \text{ mA; } -V_{CE} = 1 \text{ V}$	h_{FE}	min.	25	
Noise figure at $R_S = 1 \text{ kW}$				
$-I_C = 100 \text{ mA}; -V_{CE} = 5 \text{ V}$				
f = 10 Hz to 15.7 kHz	NF	max.	5	dΒ
Small signal current gain				
$-V_{CE} = 1V$; $-I_{C} = 2 \text{ mA}$; $f = 1 \text{ KHz}$	h_{fe}	min.	50	
0		max.	150	
Transition frequency				
$-V_{CE} = 20V$; $-I_{C} = 10$ mA; $f = 100$ MHz	f_T	min.	200	MHz

Customer Notes

Disclaimer

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C-120 Naraina Industrial Area, New Delhi 110 028, India.

Telephone + 91-11-2579 6150, 5141 1112 Fax + 91-11-2579 5290, 5141 1119 email@cdil.com www.cdilsemi.com